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**VFPB010R046NA**

**Datasheet**

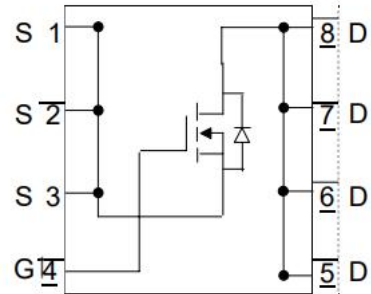


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**Description**

$V_{(BR)DSS}$	$R_{DS(ON)_{max}}$	$I_D$
100V	4.6mΩ@10V	85A

- 100V N-channel SGT MOSFET
- It has been designed to very low on-state resistance ( $R_{DS(ON)}$ ) and yet maintain superior switching performance

**Symbol**


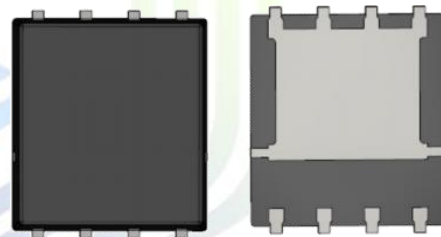
Symbol of VFPB010R046NA

**Features**

- N-channel, optimized for high-speed smooth switching
- Excellent Gate charge  $\times R_{DS(ON)}$  (FOM)
- Very low on-resistance
- RoHS compliant<sup>Note 1</sup>
- Halogen-free<sup>Note 1</sup>

**Package Type**
**Application**

- Motor Drivers
- DC-DC Converter
- Power Management



Package Type of VFPB010R046NA

**Ordering Information**

Product Name	Package
VFPB010R046NA	PDFN5×6

**Absolute Maximum Ratings** ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

Parameter	Symbol	Value	Units
Drain-Source Voltage	$V_{DS}$	100	V
Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ )	$I_D$	85	A
Drain Current - Continuous ( $T_C = 100^\circ\text{C}$ )		53	A
Drain Current – Pulsed <sup>Note 1,2</sup>	$I_{DM}$	260	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Single Pulsed Avalanche Energy <sup>Note 3</sup>	$E_{AS}$	256	mJ
Power Dissipation ( $T_C = 25^\circ\text{C}$ )	$P_D$	56.8	W
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

**Thermal Resistance**

Parameter	Symbol	Value	Units
Thermal Resistance, Junction-to-Case, Steady-State	$R_{\theta JC}$	2.2	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient, Steady State <sup>Note 4</sup>	$R_{\theta JA}$	50	$^\circ\text{C}/\text{W}$

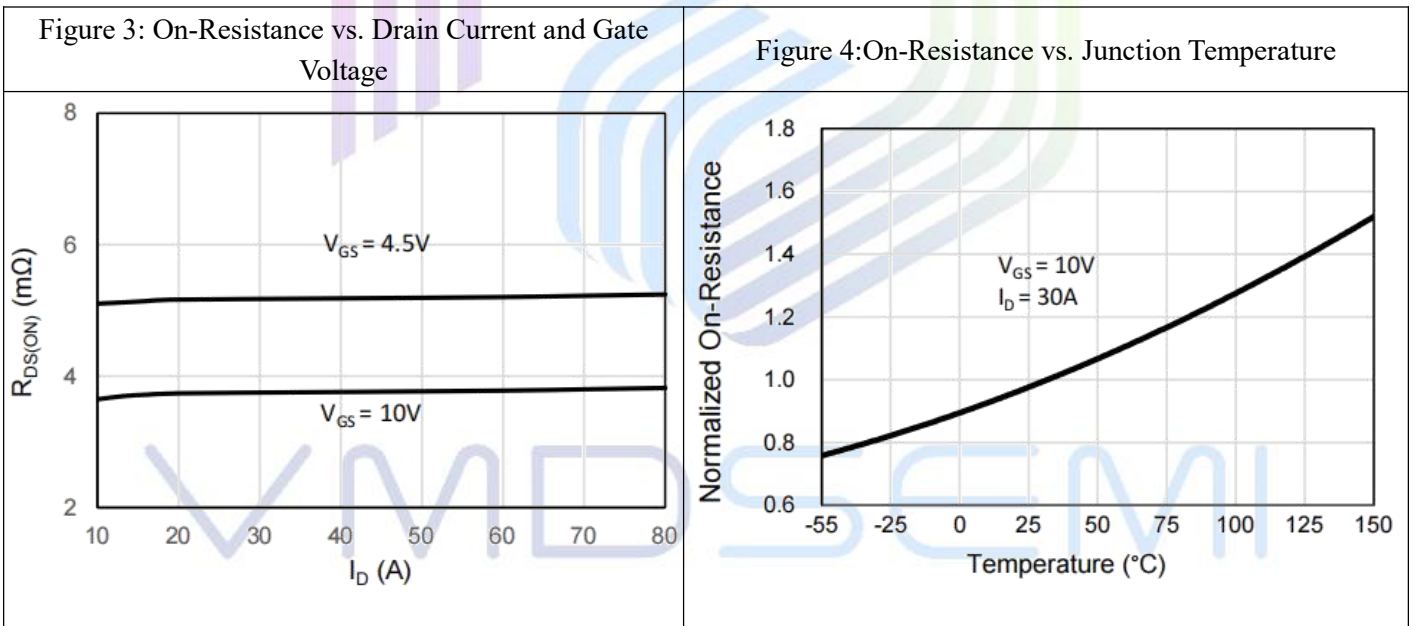
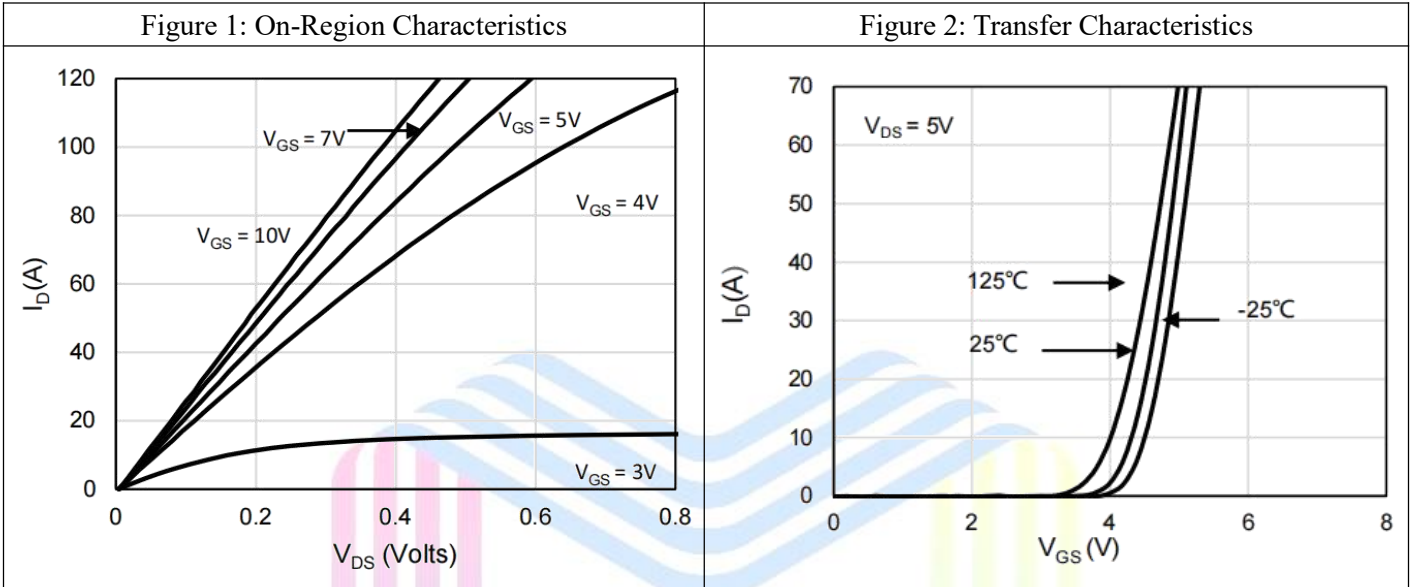
**Notes:**

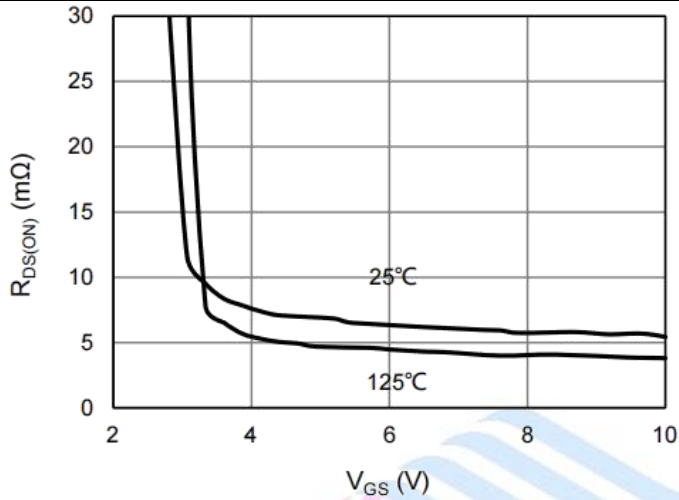
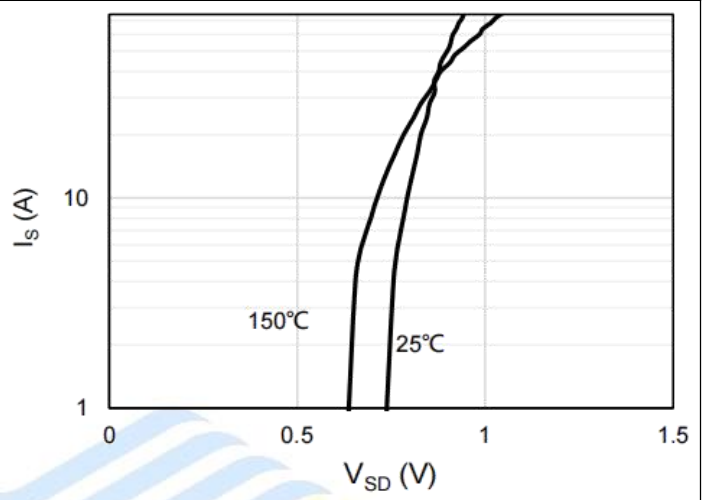
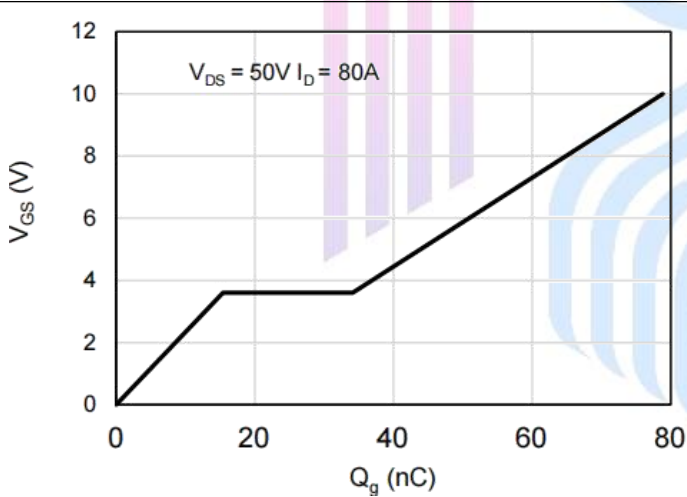
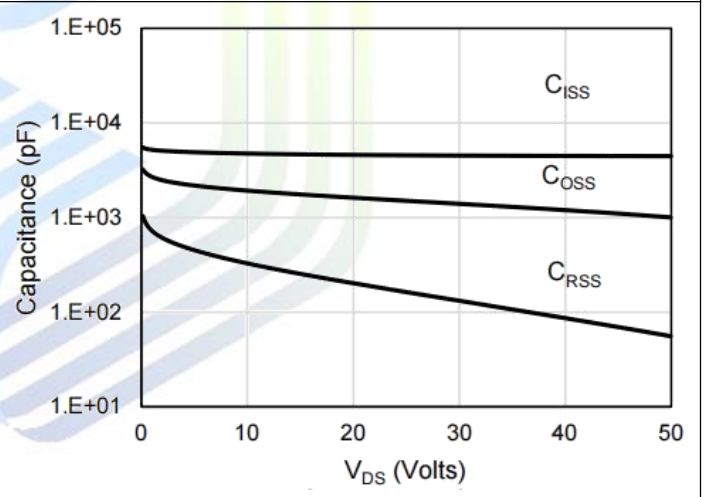
1. The max drain current rating is package limited.
2. Repetitive Rating: Pulse width limited by maximum junction temperature.
3.  $L = 0.5\text{ mH}$ ,  $V_{DD} = 50\text{ V}$ ,  $I_{AS} = 32\text{ A}$ ,  $R_g = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$ .
4. Mount on minimum PCB layout.

**Electrical Characteristics** (T<sub>J</sub>= 25 °C, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units	
<b>Static Characteristics</b>							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	100	-	-	V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> =0V	-	-	1	μA	
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ± 20 V, V <sub>DS</sub> = 0 V	-	-	±100	nA	
Gate Threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	1.4	1.9	2.4	V	
Drain-Source on-state resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30 A	-	3.8	4.6	mΩ	
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 20 A	-	5.2	6.4	mΩ	
<b>Dynamic Characteristics</b>							
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V f = 1 MHz	-	4590	-	pF	
Output Capacitance	C <sub>oss</sub>		-	1060	-	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	38.4	-	pF	
Gate Resistance	R <sub>g</sub>	f = 1 MHz	-	2.5	-	Ω	
<b>Switching Characteristics</b>							
Turn On Delay Time	T <sub>D(on)</sub>	V <sub>DS</sub> = 50V, I <sub>D</sub> = 80A V <sub>GS</sub> = 10V, R <sub>G</sub> = 6 Ω	-	20.4	-	ns	
Rise Time	t <sub>r</sub>		-	31	-	ns	
Turn Off Delay Time	t <sub>D(off)</sub>		-	76.8	-	ns	
Fall Time	T <sub>f</sub>		-	36.2	-	ns	
Total Gate Charge	Q <sub>g</sub>		V <sub>DS</sub> = 50 V, I <sub>D</sub> = 40 A	-	79	-	nC
Gate-Source Charge	Q <sub>gs</sub>		V <sub>GS</sub> = 10 V	-	16	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	16.4	-	nC	
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>							
Maximum Continuous Body-Diode Forward Current	I <sub>S</sub>		-	85	-	A	
Maximum Pulsed Body-Diode Forward Current <sup>Note1</sup>	I <sub>SM</sub>		-	260	-	A	
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 40 A	-	0.85	-	V	
Reverse recovery time	t <sub>rr</sub>	I <sub>F</sub> = 80 A	-	43.4	-	ns	
Reverse recovery charge	Q <sub>rr</sub>	di/dt = 100 A/μS	-	52.7	-	nC	

## Electrical Characteristics Diagrams



**Figure 5: On-Resistance vs. Gate-Source Voltage**

**Figure 6: Body-Diode Characteristics**

**Figure 7: Gate-Charge Characteristics**

**Figure 8: Capacitance Characteristics**


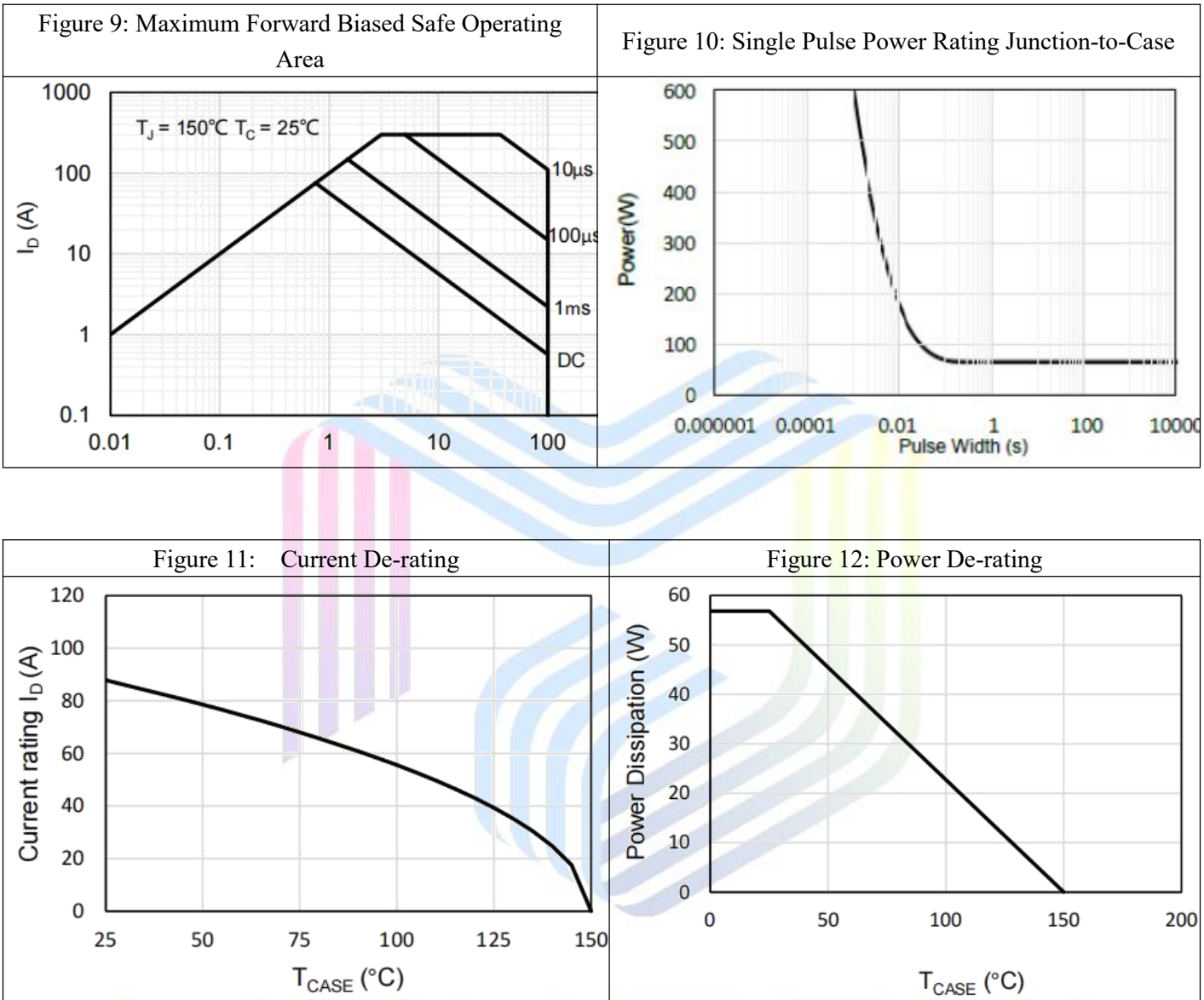
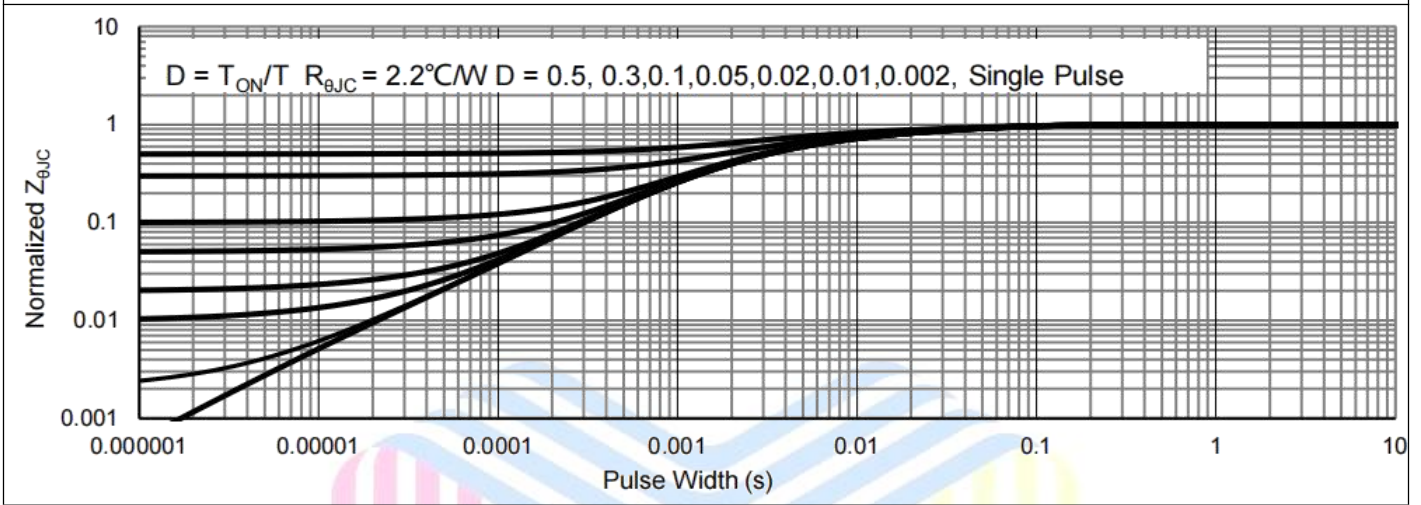
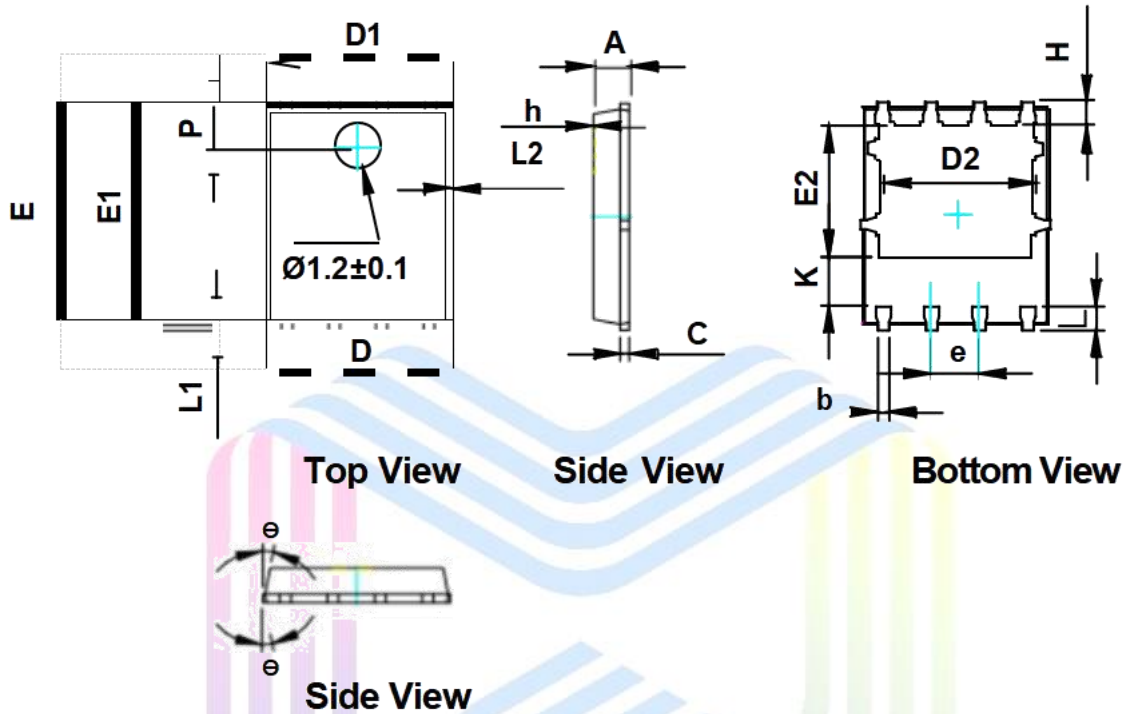


Figure 13: Normalized Maximum Transient Thermal Impedance



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**Mechanical Dimensions (PDFN5×6 Unit: mm)**

**COMMON DIMENSIONS: (UNITS OF MEASURE = MILLIMETER)**

SYMBOL	MIN	NOM	MAX
A	0.90	1.00	1.10
b	0.20	0.30	0.40
c	0.21	0.25	0.34
D			5.10
D1	4.80	4.90	5.00
D2	3.91	4.01	4.11
e	1.27 BSC		
E	5.90	6.00	6.10
E1	5.65	5.75	5.85
E2	3.375	3.475	3.575
H	0.55	0.65	0.75
h	-	-	0.05
K	1.20	-	-
L	0.55	0.65	0.75
L1	0.05	0.15	0.25
L2	-	-	0.12
$\theta$	8°	10°	12°
p	1.00	1.10	1.20

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