

VFTW010R016NA

Datasheet



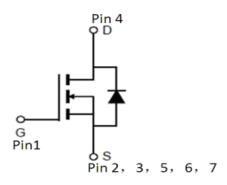


VFTW010R016NA

General Description

V _{(BR)DSS}	R _{DS(ON)_max}	I_{D}	
100V	1.6mΩ@10V	330A	

Symbol



Symbol of VFTW010R016NA

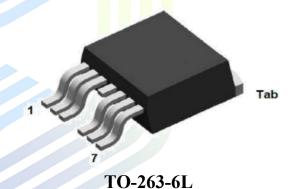
Features

- \blacksquare Extremely low $R_{DS(ON)}$
- Excellent stability and uniformity
- Excellent Low FOM
- 100% EAS Guaranteed

Application

- BMS
- Switched mode power supply
- Telecom power
- Server power
- LED Backlighting

Package Type



Package Type of VFTW010R016NA

Ordering Information

Product Name	Package		
VFTW010R016NA	TO-263-6L		



VFTW010R016NA

Absolute Maximum Ratings (T_A= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit		
Drain-Source Voltage	V_{DS}	100	V		
Gate-Source Voltage	V _{GS}	±20	V		
Cantinuana Dunin Commant Note 1	T _C =25°C	т	330	A	
Continuous Drain Current Note 1	T _C =100°C	I_{D}	210		
Pulsed Drain Current Note 2	T _C =25°C	I _{D, pulse}	1320	A	
Continuous Diode Forward Current Note 1	T _C =25°C	I_{S}	330	A	
Max Power Dissipation Note 3	T _C =25°C	P _D	295	W	
Avalanche Energy, Single Pulse Note4	Eas	2862	mJ		
Operation and storage temperature	T _J ,T _{STG}	-55 to 150	°C		

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	-	0.498	-	°C/W
Thermal Resistance, Junction-to-Ambient Note5	$R_{ heta JA}$	A/ -	31.8	-	C/W

Notes:

- 1. Calculated continuous current based on maximum allowable junction temperature.
- 2. Pulse width limited by safe operating area.
- 3. Based on max. junction temperature, using junction-case thermal resistance.
- 4. V_{DD} =80V, V_{GS} =10V, L=0.5mH, starting T_A =25 °C.
- 5. When mounted on 1 inch square copper board, t≤10sec. The value in any given application depends on the user's specific board design.

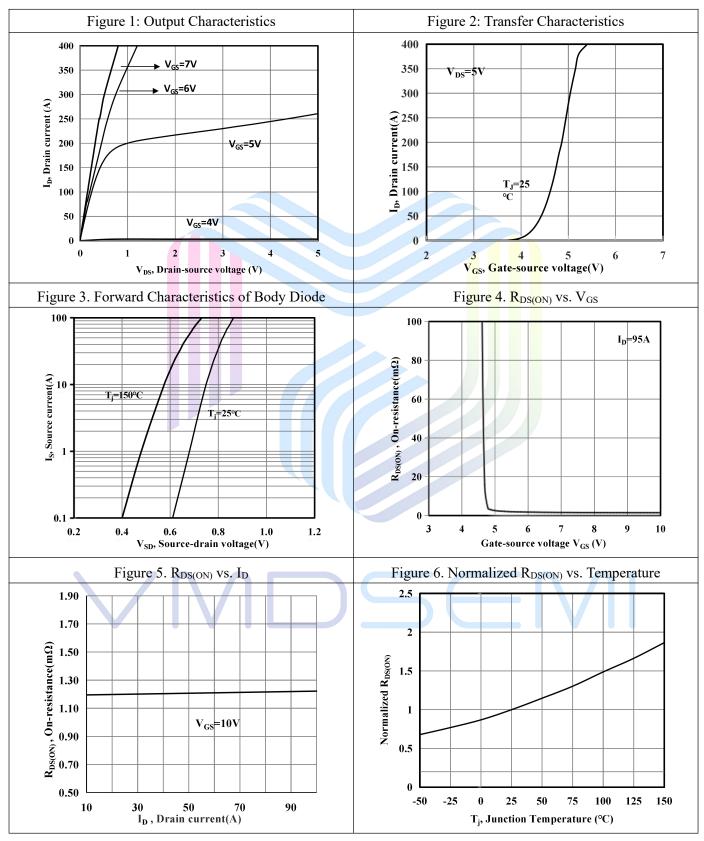


VFTW010R016NA

Electrical Characteristics (T_A= 25 °C, unless otherwise specified)

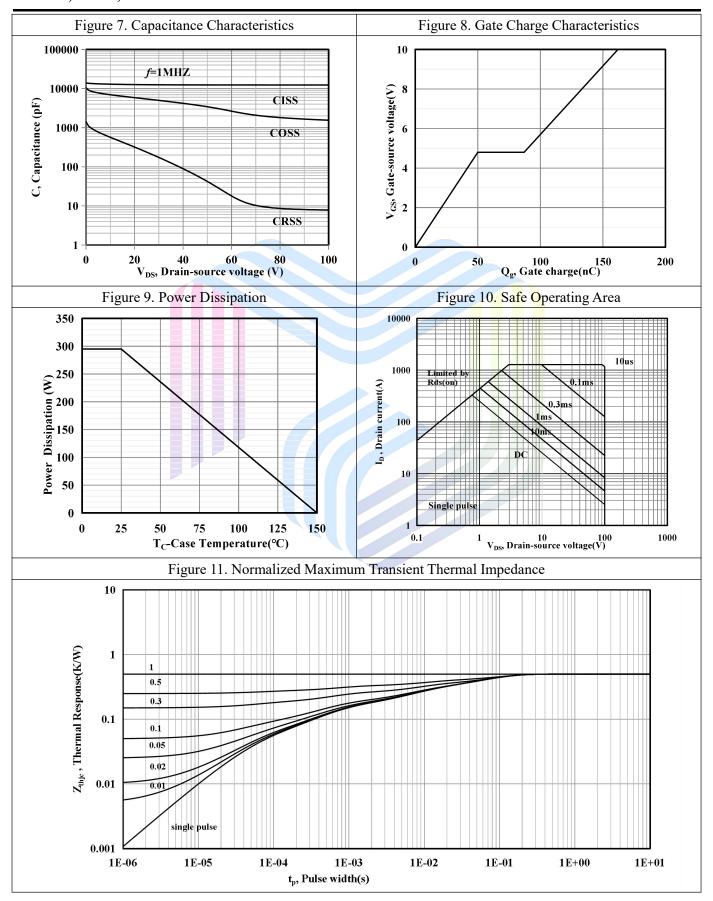
Parameter		Symbol	Test Conditions	Min	Тур	Max	Unit	
Statistic Characteristics								
Drain-Source Breakdown Voltage		$\mathrm{BV}_{\mathrm{DSS}}$	V _{GS} =0V, I _D =250uA	100	-	-	V	
Drain-Source Leakage Current		I_{DSS}	V _{DS} =100V, V _{GS} =0V	-	-	1	uA	
Gate-Source Leakage Current	Forward	I_{GSSF}	V _{GS} =20V, V _{DS} =0V	-	-	100	A	
	Reverse	I _{GSSR}	V_{GS} =-20V, V_{DS} =0V	-	-	-100	nA	
Gate Threshold Voltage		$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250uA$	2	3	4	V	
Drain-Source On-State Resistan	ce	R _{DS(ON)}	V _{GS} =10V, I _D =95A	-	1.29	1.6	mΩ	
Dynamic Characteristics								
Input Capacitance		C_{iss}	V _{DS} =50V	-	12425	-	pF	
Output Capacitance		C_{oss}	$V_{GS}=0V$		3385	-	pF	
Reverse Transfer Capacitance		C_{rss}	f=1MHz	-	41	-	pF	
Gate Resistance		R_G	F=1MHz, Open Drain	-	1.9	-	Ω	
Gate to Source Charge		Q_{gs}	$V_{DS}=50V$	-	50	-		
Gate to Drain Charge		Q_{gd}	I _D =95A	-	37	-	nC	
Gate Charge Total		Qg	$V_{GS}=10V$	-	162	-		
Switching Characteristics								
Turn-on Delay Time		$t_{d(on)}$	V _{DS} =50V		51	-		
Rise Time		$t_{\rm r}$	I _D =20A	//-/	67	-	12 G	
Turn-off Delay Time		$t_{ m d(off)}$	$R_G=2.7\Omega$	-/-	121	_	ns	
Fall Time		t_{f}	$V_{GS}=10V$	-	39	-		
Reverse Diode Characteristics								
Drain-Source Diode Forward Voltage		V_{SD}	$V_{GS}=0V, I_{S}=95A$	-	0.87	1.2	V	
Reverse Recovery Time		t _{rr}	V _R =50V	-	104	-	ns	
Reverse Recovery Charge		Q_{rr}	I _S =20A di/dt=100A/us	-	294	-	uC	
VMDSEMI								

Electrical Characteristics Diagrams





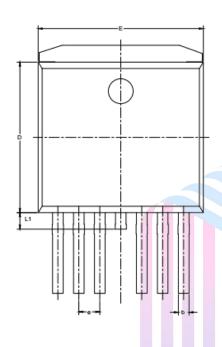
VFTW010R016NA

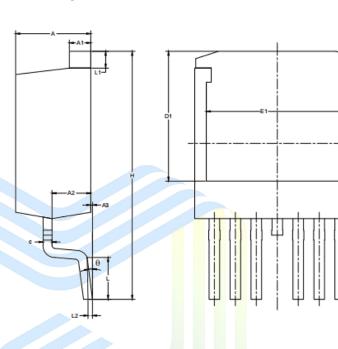




Mechanical Dimensions

TO-263-6L Package Information





COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	MAX			
А	4.25	4.64			
A1	1.20	1.44			
A2	2.25	2.55			
A3	0.00	0.25			
р	0.50	0.74			
С	0.40	0.64			
D	9.00	9.45			
D1	6.90	8.05			
н	14.65	15.35			
E	9.80	10.20			
E1	7.25	8.80			
е	1.27BSC				
L	2.34	3.00			
L1	0.80	1.20			
L2	0.25BSC				
в	2°	8°			



VFTW010R016NA

NOTICE

Hangzhou VMD Semiconductor Co., Ltd (VMD) reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to VMD's terms and conditions supplied at the time of order acknowledgement.

VMD, its affiliates, agents, and employees, and all persons acting on its or their behalf, disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

VMD disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify VMD's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

VMD warrants performance of its hardware products to the specifications at the time of sale, testing, reliability and quality control are used to the extent VMD deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

VMD does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using VMD's components. To minimize risk, customers must provide adequate design and operating safeguards.

VMD does not warrant or convey any license to any intellectual property rights either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in VMD's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice.

VMD is not responsible or liable for such altered documentation. Resale of VMD's products with statements different from or beyond the parameters stated by VMD for that product or service voids all express or implied warrantees for the associated VMD product or service and is an unfair and deceptive business practice.

All Rights Reserved.





Via-Media Semiconductor Limited Company

http://www.vmdsemi.com

Main Sites:

- Headquarters

Hangzhou Via-Media Semiconductor Co., LTD. 1305-1306, Building 71, No. 90, Wensan Road, Xihu District, Hangzhou, Zhejiang Province, P.R. China Tel: +86-0571-8515 0563

- Shanghai

Shanghai R&D Center. 1506~1508, Xinyin Building, 888 Yishan Road, Shanghai, P.R of China

Tel: +86-021-54201999

- Xi'an

Xi'an R&D Center.

Room 10504, Building 2, Central Plaza, Jinye Road, High tech Zone, Xi'an City, Shanxi Province, R.P. of China

- Chengdu Office

Chengdu Winhi Semiconductor Co., LTD. Floor 15, Building 5, No. 171, Hele 2nd Street, Chengdu, Sichuan Province, P.R. China Tel: +86-028-8505 0771

- Shenzhen

Shenzhen Sales office .

Room 4A15, Block AB, Tianxiang Building,
Chegongmiao, Futian District, Shenzhen, P.R of
China

Tel: +86-0755-82570682