



VMDSEMI

VFTV010R020NB

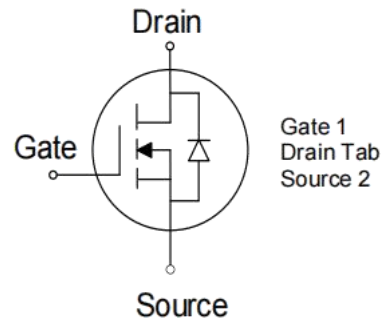
Datasheet



VMDSEMI

2mΩ, 100V, N-Channel Power MOSFET
VFTV010R020NB
Description

$V_{(BR)DSS}$	$R_{DS(ON)_{max}}$	I_D
100V	2mΩ@10V	283A

Symbol


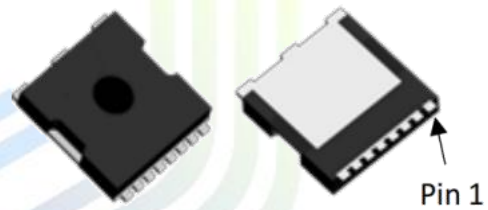
Symbol of VFTV010R020NB

Features

- Low $R_{DS(ON)}$
- RoHS compliant Note 1
- Halogen-free Note 1
- 100% UIS tested
- Wettable Flank for Improved Optical Inspection

Application

- Battery Management System
- DC-DC Converter

Package Type


Package Type of VFTV010R020NB

Ordering Information

Product Name	Package
VFTV010R020NB	TOLL

Absolute Maximum Ratings ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Value	Units
Drain-Source Voltage	V_{DS}	100	V
Drain Current - Continuous ($T_C = 25^\circ\text{C}$) ^{Note 1}	I_D	283	A
Drain Current - Continuous ($T_C = 100^\circ\text{C}$)		179	A
Drain Current - Pulsed ^{Note 2}	I_{DM}	1132	A
Gate-Source Voltage	V_{GS}	± 20	V
Single Pulsed Avalanche Energy ^{Note 3}	E_{AS}	702	mJ
Power Dissipation ($T_C = 25^\circ\text{C}$)	P_D	312.5	W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Value	Units
Thermal Resistance, Junction-to-Case, Steady-State	$R_{\theta JC}$	0.4	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient, Steady State ^{Note 4}	$R_{\theta JA}$	50	$^\circ\text{C/W}$

Notes:

1. The max drain current rating is silicon limited
2. Repetitive Rating: Pulse width limited by maximum junction temperature
3. $L = 0.5\text{ mH}$, $V_{DD} = 50\text{ V}$, $I_{AS} = 53\text{ A}$, $R_g = 50\Omega$, Starting $T_J = 25^\circ\text{C}$
4. Mount on minimum PCB layout

Electrical Characteristics ($T_J = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
Static Characteristics						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	100	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	μA
Gate Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$	-	-	± 100	nA
Gate Threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2	2.9	4	V
Drain-Source on-state resistance	$R_{DS(on)}$	$V_{GS} = 10\text{ V}, I_D = 20\text{ A}$	-	1.5	2	mΩ
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS} = 50\text{ V}, V_{GS} = 0\text{ V},$ $f = 1\text{ MHz}$	-	9325	-	pF
Output Capacitance	C_{oss}		-	3065	-	pF
Reverse Transfer Capacitance	C_{rss}		-	105	-	pF
Gate Resistance	R_g	$f = 1\text{ MHz}$	-	1.8	-	Ω
Switching Characteristics						
Turn On Delay Time	$T_{D(on)}$	$V_{DD} = 50\text{ V}, R_L = 1\text{ }\Omega,$ $V_{GS} = 10\text{ V}, R_G = 2.2\text{ }\Omega$	-	33	-	ns
Rise Time	t_r		-	60	-	ns
Turn Off Delay Time	$t_{D(off)}$		-	70	-	ns
Fall Time	T_f		-	55	-	ns
Total Gate Charge	Q_g		$V_{DD} = 50\text{ V}, I_D = 50\text{ A},$ $V_{GS} = 10\text{ V}$	-	147	-
Gate-Source Charge	Q_{gs}		-	42	-	nC
Gate-Drain Charge	Q_{gd}		-	39	-	nC
Drain-Source Diode Characteristics and Maximum Ratings						
Maximum Continuous Body-Diode Forward Current	I_S		-	-	283	A
Maximum Pulsed Body-Diode Forward Current ^{Note1}	I_{SM}		-	-	1132	A
Diode Forward Voltage	V_{SD}	$V_{GS} = 0\text{ V}, I_S = 1\text{ A}$	-	0.66	-	V
Reverse recovery time	t_{rr}	$V_{DD} = 50\text{ V}, I_D = 50\text{ A},$ $di/dt = 100\text{ A}/\mu\text{S}$	-	85	-	ns
Reverse recovery charge	Q_{rr}		-	169	-	nC
Peak Reverse Recovery Current	I_{rrm}		-	3.5	-	A

Electrical Characteristics Diagrams

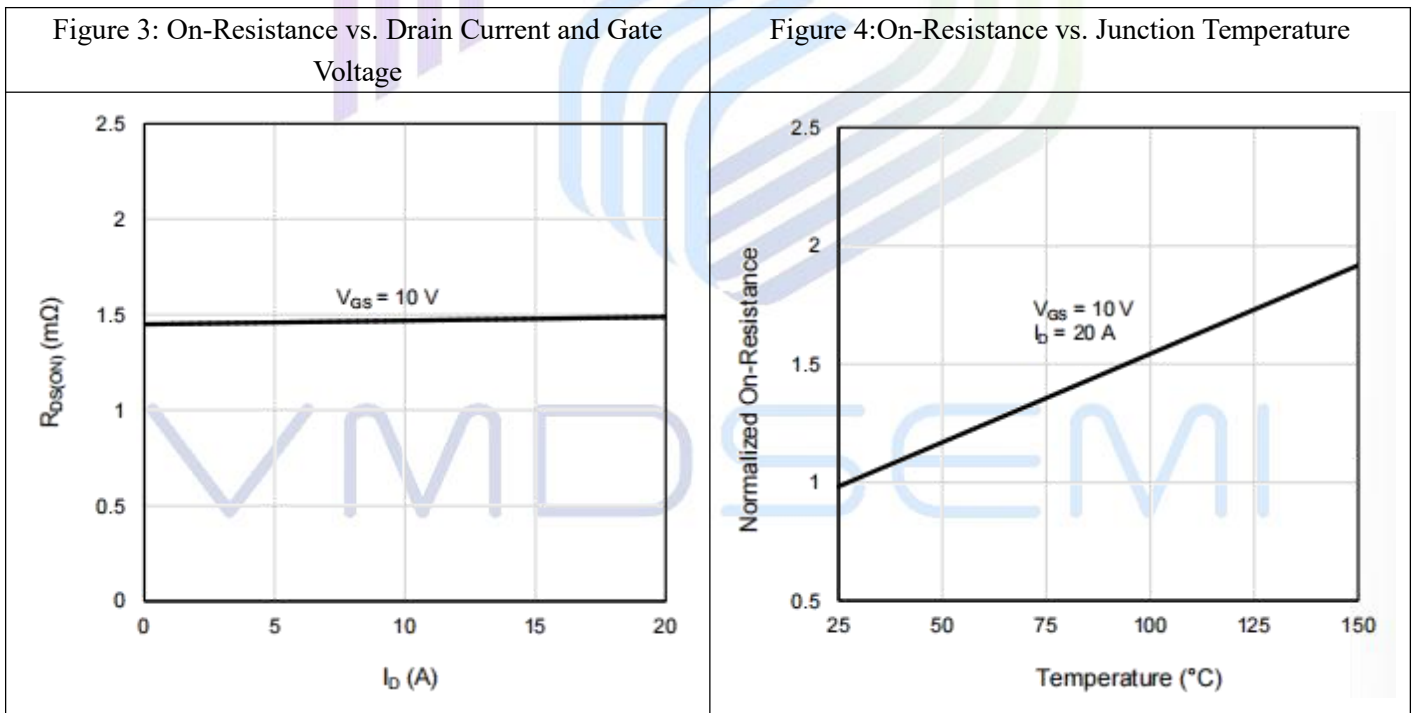
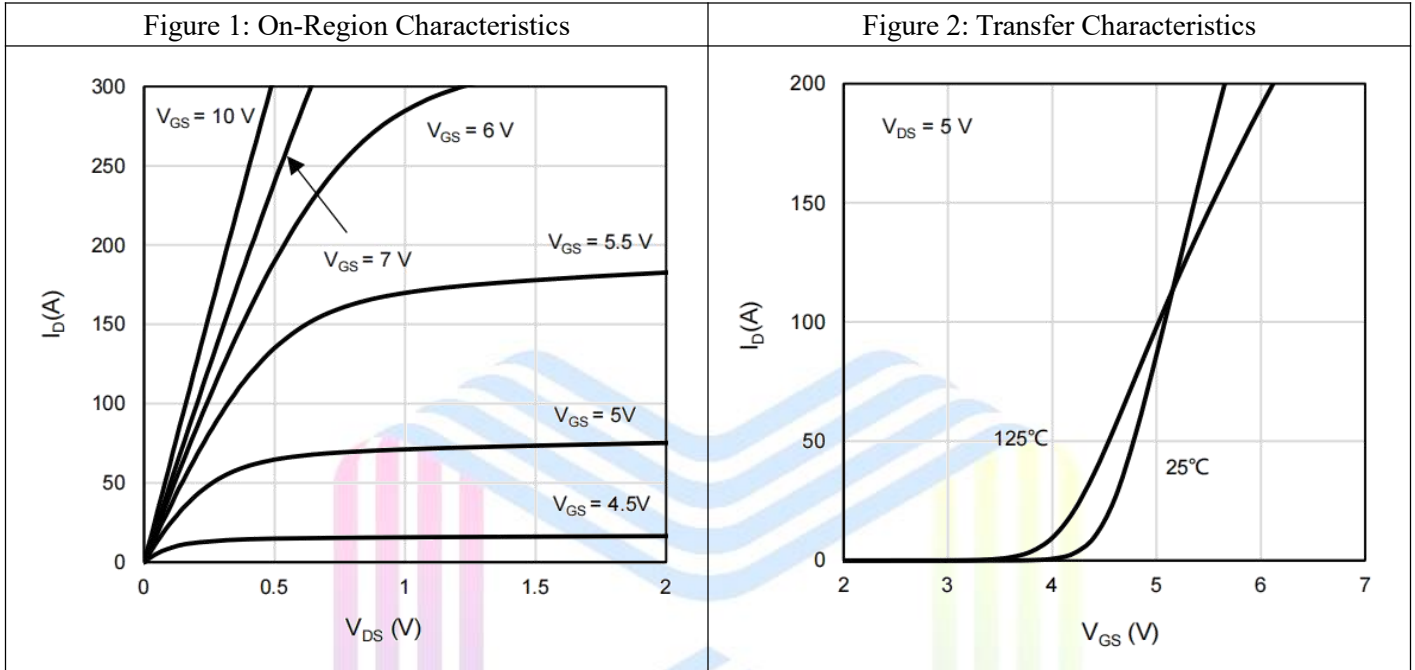


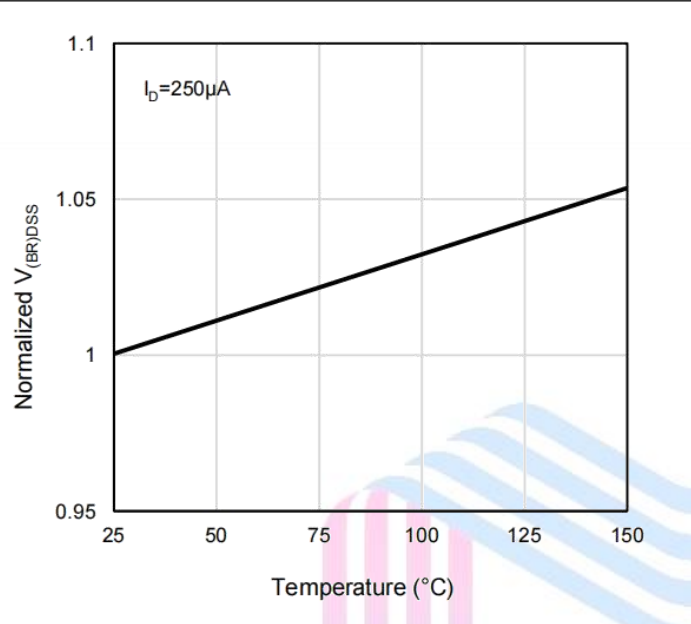
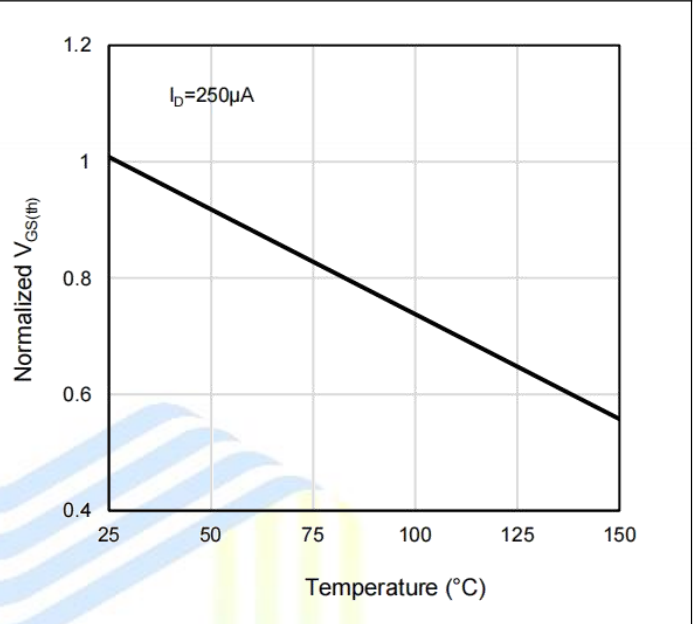
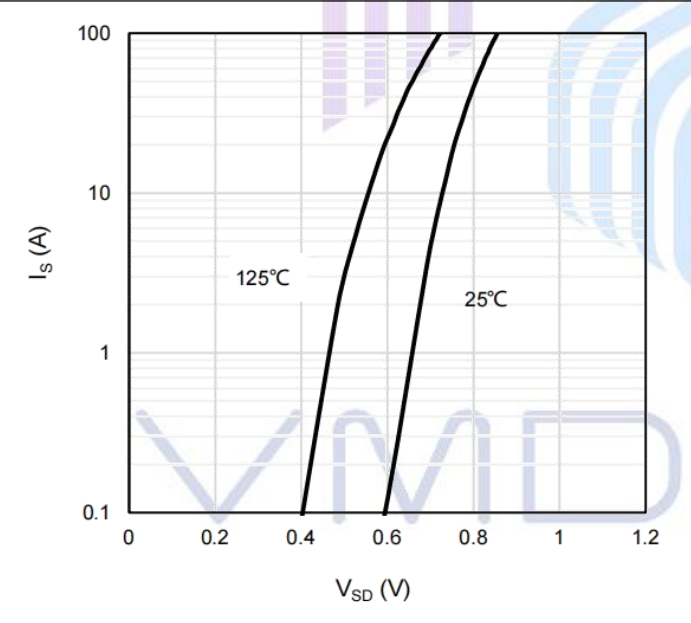
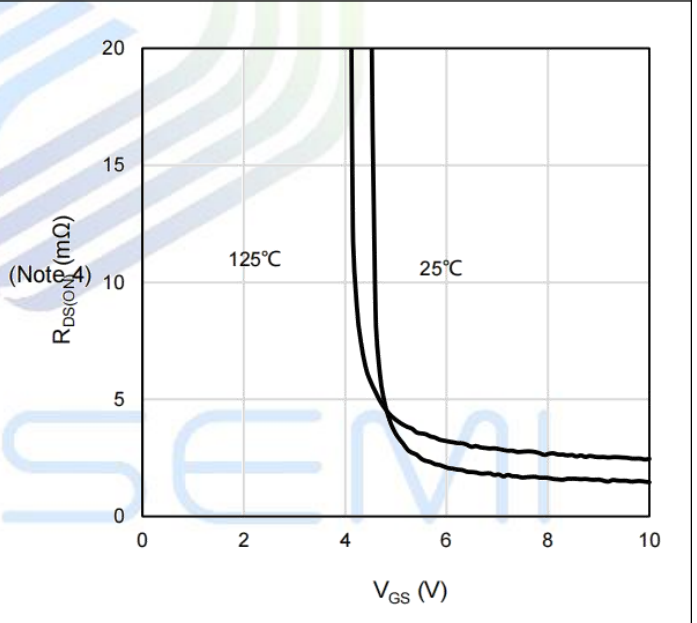
Figure 5: Breakdown Voltage vs. Junction Temperature

Figure 6: Threshold Voltage vs. Junction Temperature

Figure 7: Body-Diode Characteristics

Figure 8: On-Resistance vs. Gate-Source Voltage


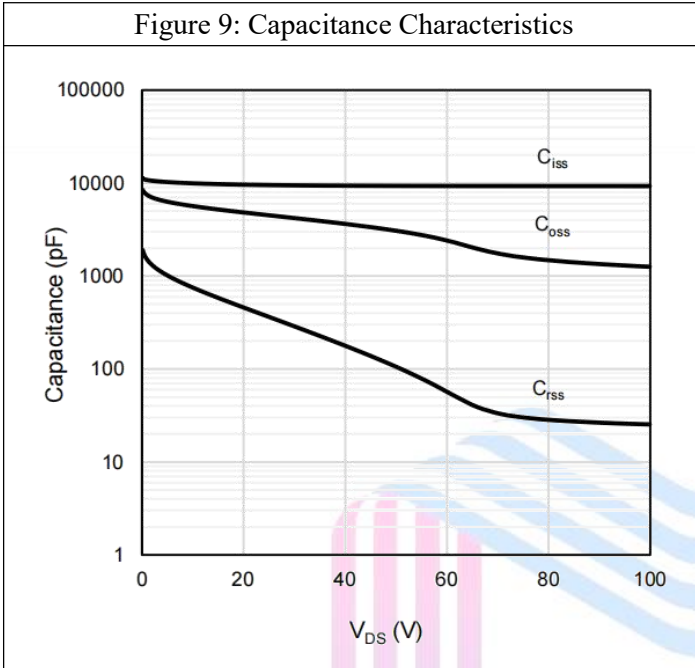
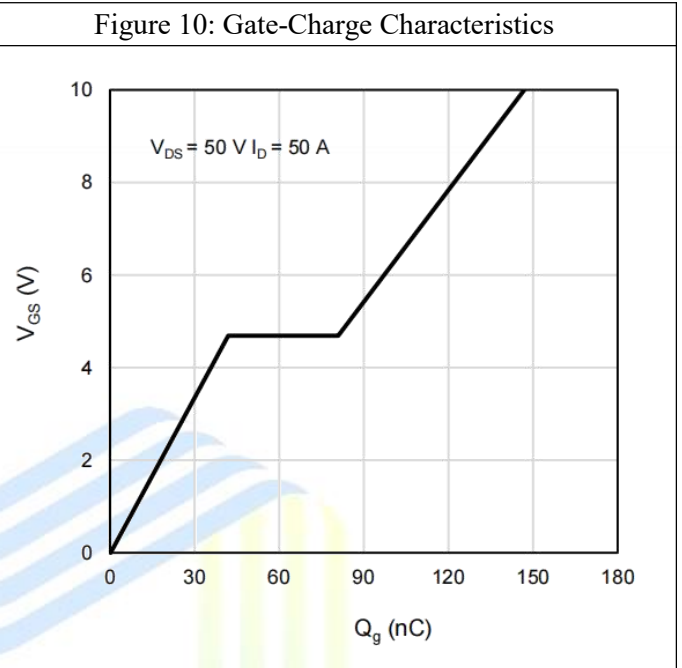
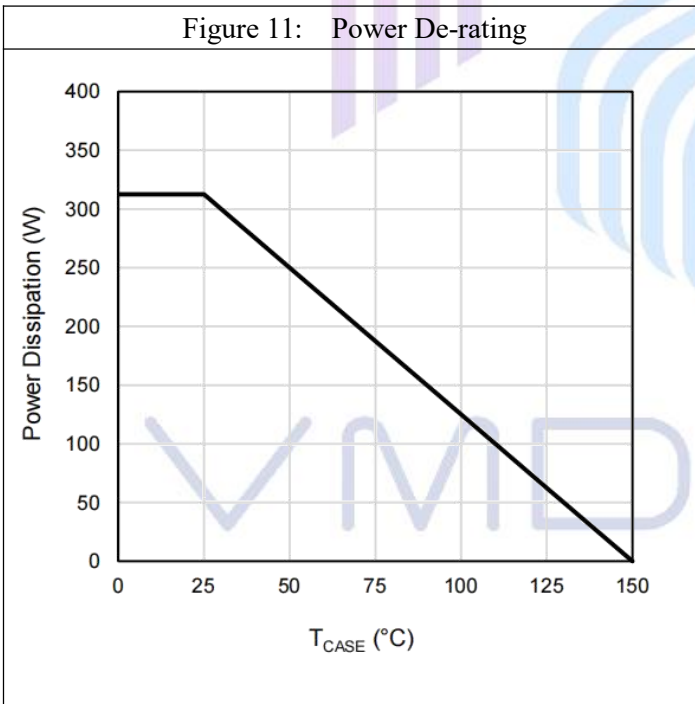
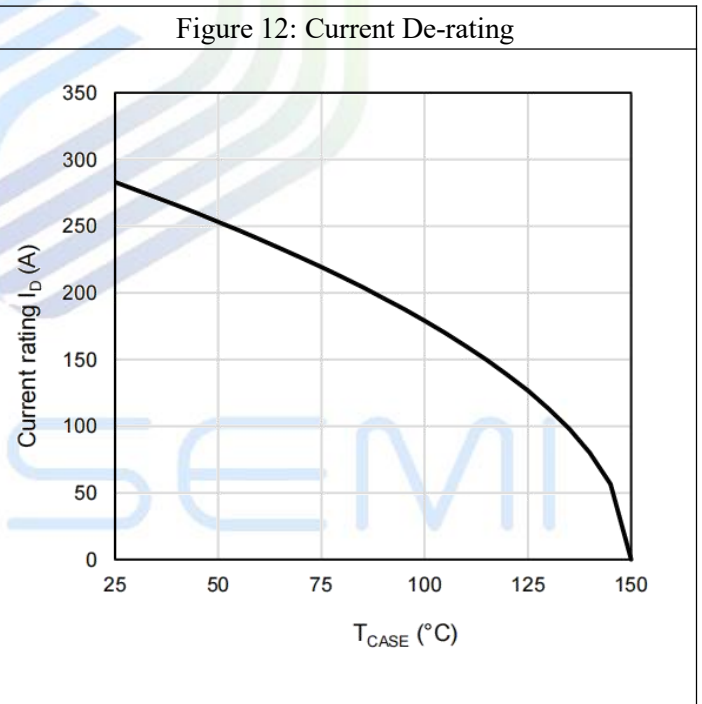
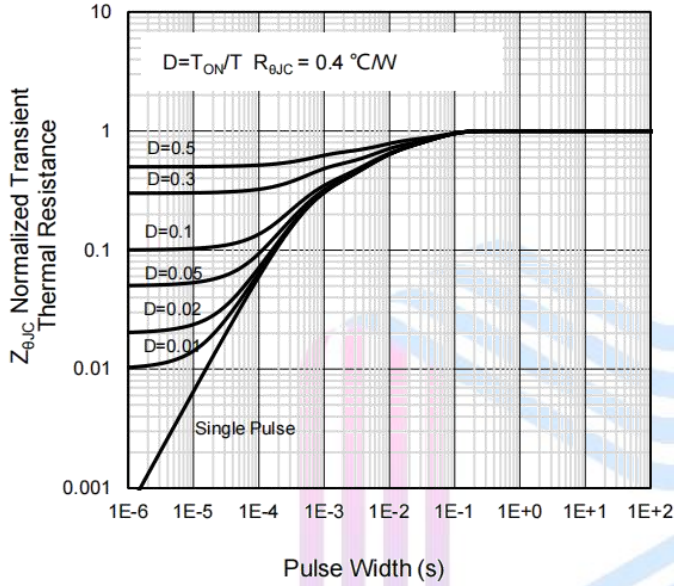
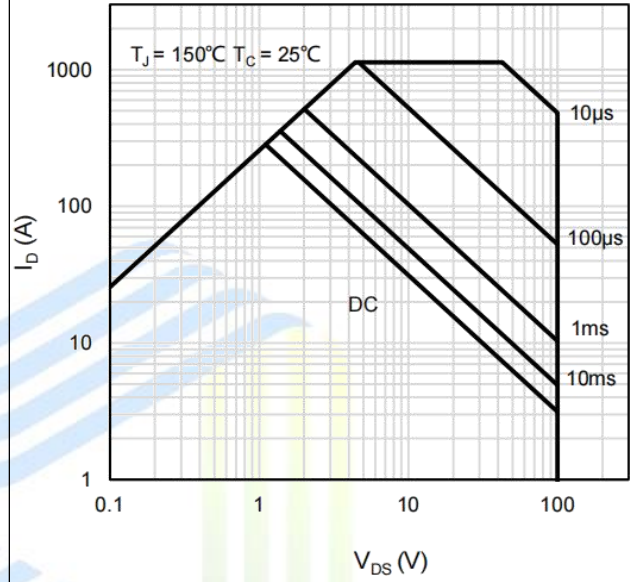
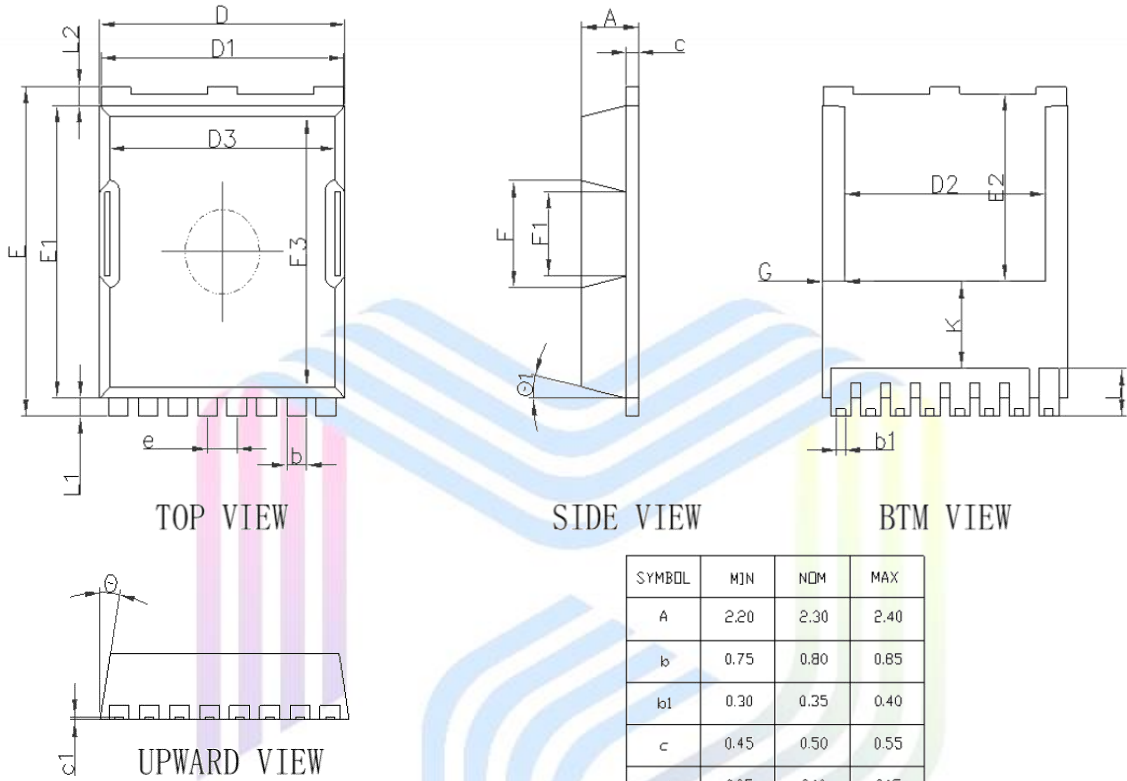
Figure 9: Capacitance Characteristics

Figure 10: Gate-Charge Characteristics

Figure 11: Power De-rating

Figure 12: Current De-rating


Figure 13: Normalized Maximum Transient Thermal Impedance

Figure 14: Maximum Forward Biased Safe Operating Area


Mechanical Dimensions

TOLL Package Information



SYMBOL	MIN	NOM	MAX
A	2.20	2.30	2.40
b	0.75	0.80	0.85
b1	0.30	0.35	0.40
c	0.45	0.50	0.55
c1	0.05	0.10	0.15
D	9.80	9.90	10.00
D1	9.70	9.80	9.90
D2	8.00	8.10	8.30
D3	9.10 TYP.		
e	1.20 BSC		
E	11.60	11.70	11.80
E1	10.30	10.40	10.50
E2	6.55	6.65	6.75
E3	9.60 TYP.		
F	3.80 TYP.		
F1	3.00 TYP.		
G	0.80	0.90	1.00
K	3.00	3.10	3.20
L	1.59	1.69	1.79
L1	0.54	0.64	0.74
L2	0.56	0.66	0.76
θ	8°	10°	12°
θ 1	10°	12°	14°

(UNITS OF MEASURE = MILLIMETER)

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