

# VFPB004R009NA

**Datasheet** 





### 0.9mΩ, 40V, N-Channel MOSFET

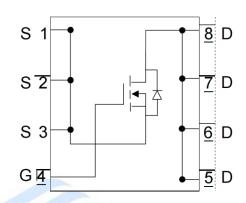
#### VFPB004R009NA

### **Description**

- 40V N-channel SGT MOSFET
- It has been designed to very low on-state resistance and superior UIS performance, especial for BMS application
- Untral Low Rdson and Fast reverse recovery for synchronous rectification

V <sub>(BR)DSS</sub>	R <sub>DS(ON)_max</sub>	$I_D$
40V	0.9mΩ@10V	370A

## **Symbol**



Symbol of VFPB004R009NA

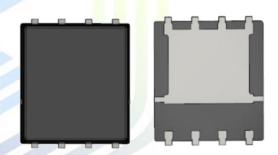
## Package Type

### **Features**

- Low R<sub>DS(ON)</sub>
- Fast reverse recovery
- 100% UIS tested
- Fast switching

### **Application**

- Battery management system
- DC-DC converter
- Synchronous rectification



Package Type of VFPB004R009NA

# **Ordering Information**

<b>Product Name</b>	Package
VFPB004R009NA	PDFN5×6



#### VFPB004R009NA

## Absolute Maximum Ratings (T<sub>J</sub>= 25 °C, unless otherwise specified)

Parameter	Symbol	Value	Units
Drain-Source Voltage	$V_{DS}$	40	V
Drain Current - Continuous (T <sub>C</sub> = 25°C VGS=10V) Note1	т	370	A
Drain Current - Continuous (T <sub>C</sub> = 100°C VGS=10V)	$I_{D}$	220	A
Drain Current - Pulsed Note 2	$I_{DM}$	900	A
Gate-Source Voltage	$V_{GS}$	± 20	V
Single Pulsed Avalanche Energy Note 3	E <sub>AS</sub>	550	mJ
Repetitive Avalanche Energy Note 3	E <sub>AR</sub>	0.2	mJ
Power Dissipation (TC = 25°C)	$P_D$	125	W
Operating and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 to +150	°C

### **Thermal Resistance**

Parameter	Sy <mark>m</mark> bol	Value	Units
Thermal Resistance, Junction-to-Case, Steady-State	$R_{ heta JC}$	1.0	°C/W
Thermal Resistance, Junction-to-Ambient, Steady State Note 4	$R_{\theta JA}$	54	°C/W

#### Notes:

- 1. The max drain current rating is package limited
- 2. Repetitive Rating: Pulse width limited by maximum junction temperature
- 3.  $L = 0.5 \text{ mH}, V_{DD} = 30 \text{ V}, Rg = 10 \Omega, Starting T_J = 25 ^{\circ}C$
- 4. Mount on minimum PCB layout



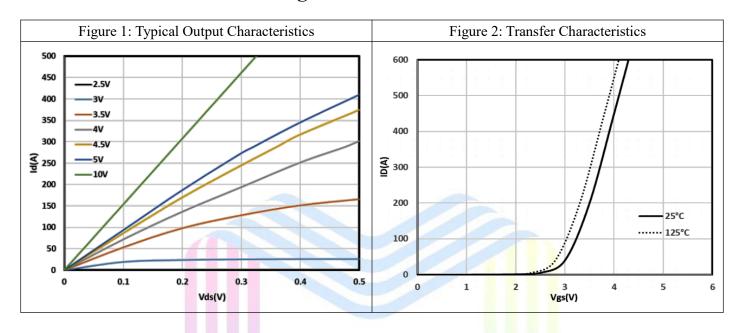


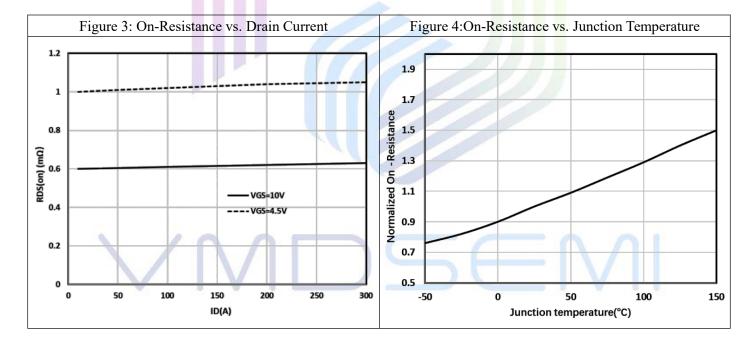
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## Electrical Characteristics(T<sub>J</sub>= 25 °C, unless otherwise specified)

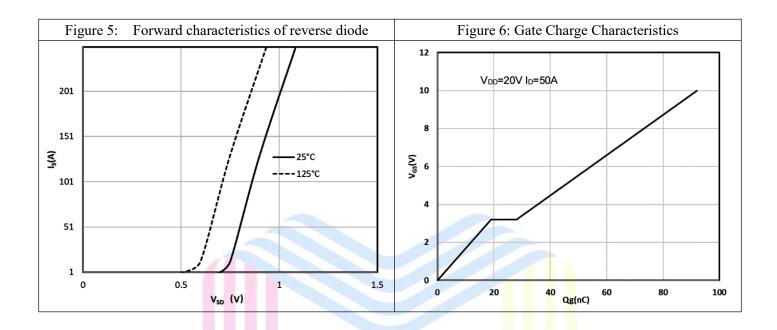
Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
StaticCharacteristics	1					
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \text{ uA}$	40	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	10	uA
Gate Leakage Current	$I_{GSS}$	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	±100	nA
Gate Threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \text{ uA}$	1.2	1.6	2	V
·	R <sub>DS(ON)</sub>	$V_{GS} = 10 \text{ V}, I_D = 30 \text{ A}$	-	0.6	0.9	mΩ
Drain-Source on-state resistance		$V_{GS} = 5 \text{ V}, I_D = 30 \text{ A}$	-	0.9	1.2	mΩ
Dynamic Characteristics						
Input Capacitance	Ciss	V - 20 V V - 0 V	<b>-</b>	7020	-	pF
Output Capacitance	Coss	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$ f = 1 MHz	-	2435	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	50	-	pF
Gate Resistance	$R_{g}$	f=1 MHz	-	2	-	Ω
Switching Characteristics						
Turn On Delay Time	T <sub>D(on)</sub>		-	12	-	ns
Rise Time	Tr	$V_{DD} = 32V , I_D = 50A$	-	41	-	ns
Turn Off Delay Time	T <sub>D(off)</sub>	$V_{GS} = 10V, R_G = 2.5 \Omega$	-	29	-	ns
Fall Time	$T_{\mathrm{f}}$		//	65	-	ns
Total Gate Charge	Qg		/ -	92	-	пC
Gate-Source Charge	Qgs	$V_{DD} = 20 \text{ V}, I_D = 50 \text{ A}$	-	19	-	пC
Gate-Drain Charge	$Q_{\mathrm{gd}}$	$V_{GS} = 0 \text{ to } 10 \text{ V}$	-	9	-	пC
Gate plateau voltage	Vplateau		ı	3.2	-	V
<b>Drain-Source Diode Characteristics and Ma</b>	aximum Rati	ings				
Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0 \text{ V}, I_{S} = 50 \text{A}$	-	0.79	1.2	V
Reverse recovery time	$T_{rr}$		1	80	-	ns
Reverse recovery charge	Qrr	I <sub>S</sub> =50A,di/dt=100A/us	F	138	-	пC
Peak Reverse Recovery Current	$I_{rrm}$		- 1	4.5	-	A

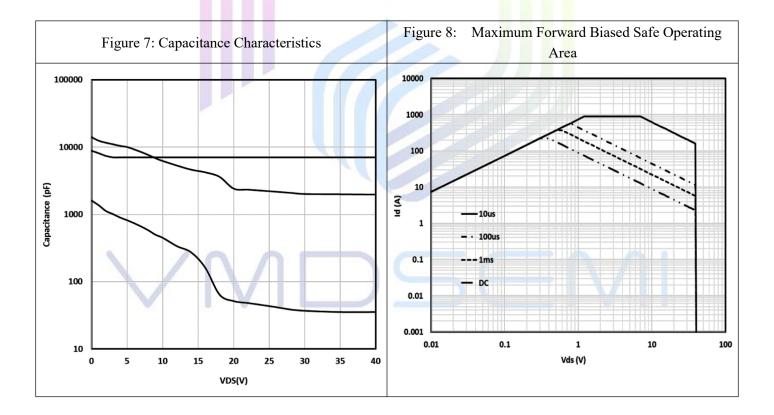
## **Electrical Characteristics Diagrams**



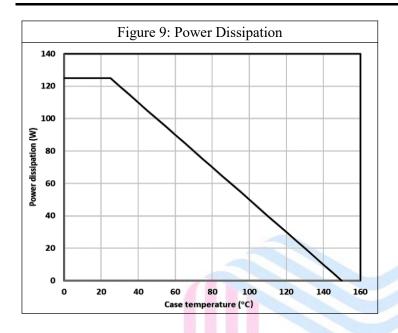


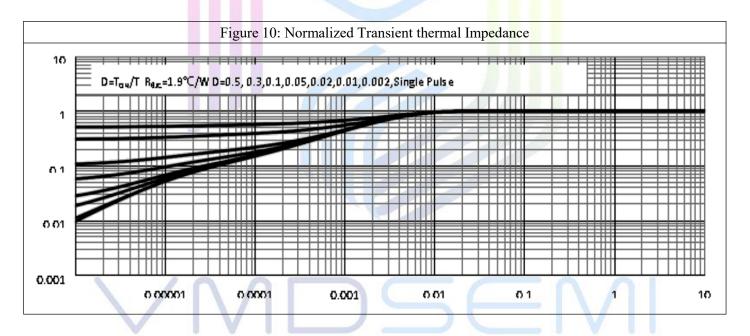
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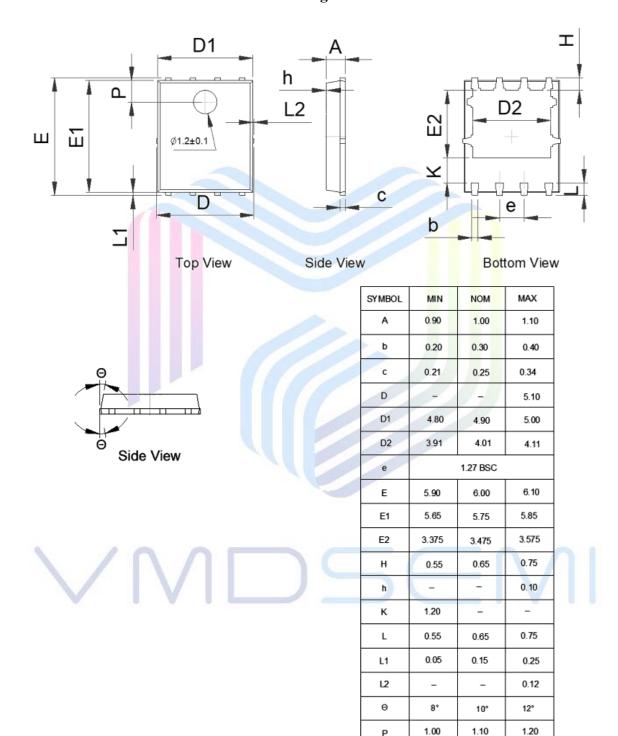






### **Mechanical Dimensions**

#### PDFN5x6 Package Information



Unit in mm

#### VFPB004R009NA

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