



VMDSEMI

**VFPB010R082NA**

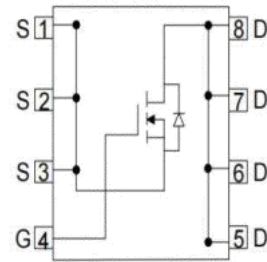
**Datasheet**



VMDSEMI

**8.2mΩ, 100V, N-Channel Power MOSFET**
**VFPB010R082NA**
**General Description**

$V_{(BR)DSS}$	$R_{DS(ON)_{max}}$	$I_D$
100V	8.2mΩ@10V	60A

**Symbol**


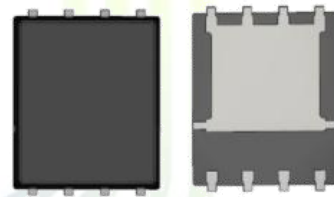
Symbol of VFPB010R082NA

**Features**

- Low RDS(on)
- RoHS compliant<sup>Note 1</sup>
- Halogen-free<sup>Note 1</sup>

**Application**

- Battery Management System
- Motor Drivers
- DC-DC Converter

**Package Type**


Package Type of VFPB010R082NA

**Ordering Information**

Product Name	Package
VFPB010R082NA	PDFN5X6

**Absolute Maximum Ratings** ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

Parameter	Symbol	Value	Units
Drain-Source Voltage	$V_{DS}$	100	V
Drain Current - Continuous ( $T_C = 25\text{ }^\circ\text{C}$ ) (Note 1)	$I_D$	70	A
Drain Current - Continuous ( $T_C = 25\text{ }^\circ\text{C}$ ) (Note 2)		60	A
Drain Current - Continuous ( $T_C = 100\text{ }^\circ\text{C}$ )		44	A
Drain Current - Pulsed (Note 3)	$I_{DM}$	240	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Single Pulsed Avalanche Energy (Note 4)	$E_{AS}$	144	mJ
Power Dissipation ( $T_C = 25\text{ }^\circ\text{C}$ )	$P_D$	73	W
Operating and Storage Temperature Range	$T_J, T_{STG}$	-55 to +150	$^\circ\text{C}$

**Thermal Resistance**

Parameter	Symbol	Value	Units
Thermal Resistance, Junction-to-Case, Steady-State	$R_{\theta JC}$	1.7	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient, Steady State <sup>Note 5</sup>	$R_{\theta JA}$	45	$^\circ\text{C}/\text{W}$

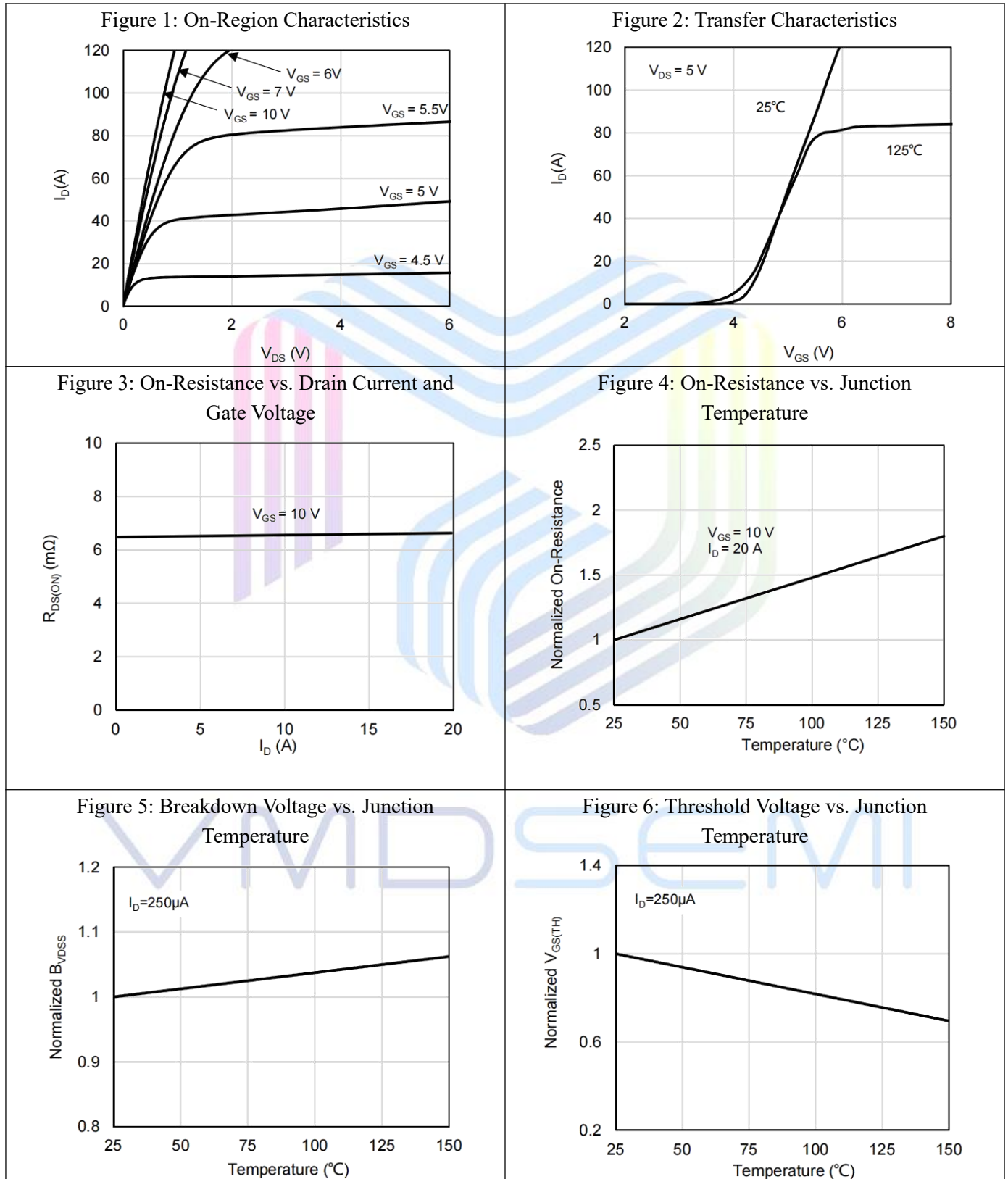
**Notes:**

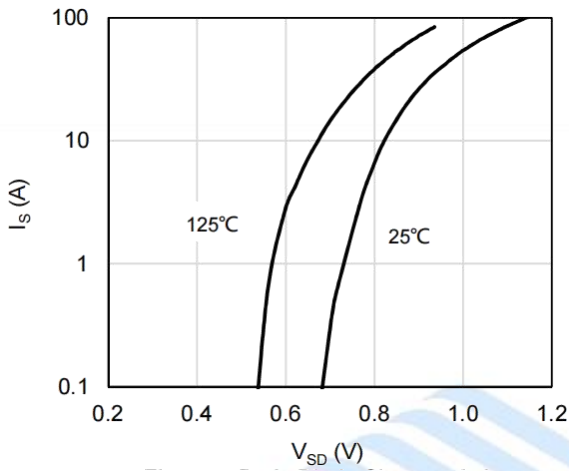
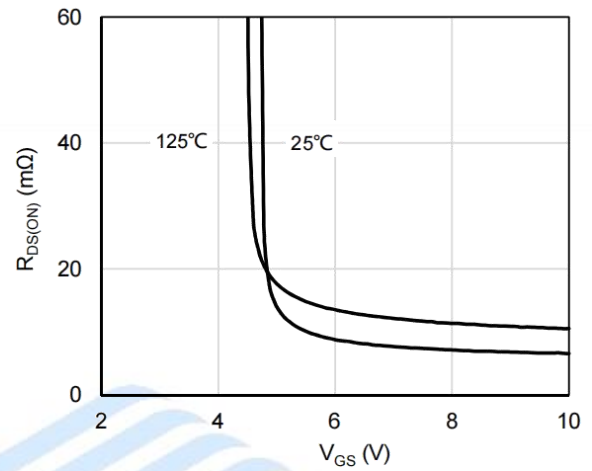
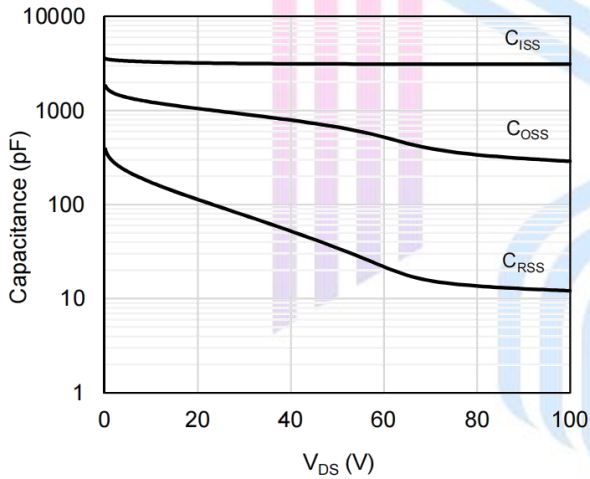
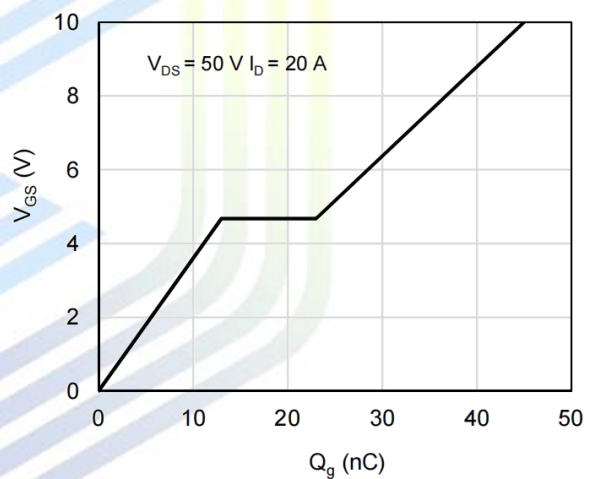
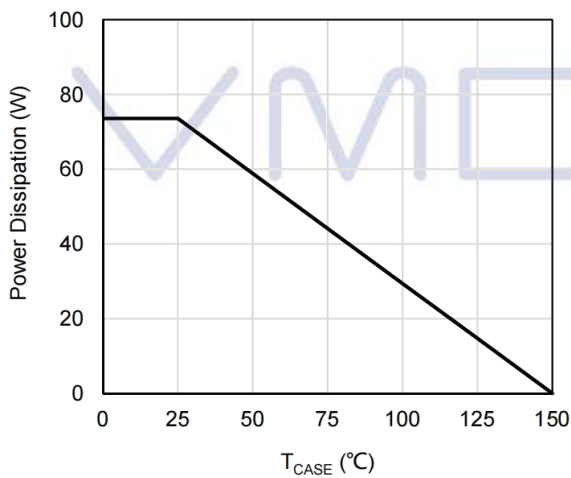
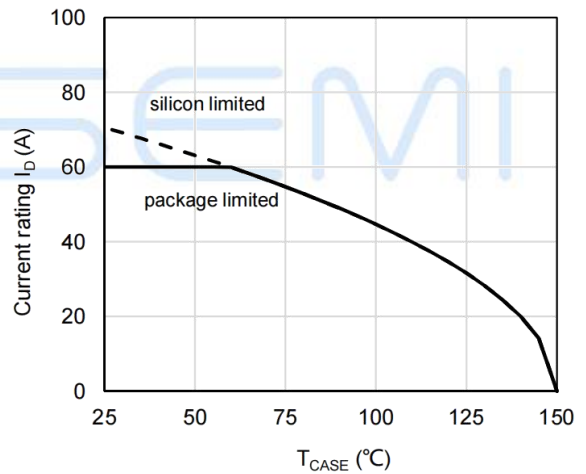
1. The max drain current rating is silicon limited
2. The max drain current rating is package limited
3. Repetitive Rating: Pulse width limited by maximum junction temperature
4.  $L = 0.5\text{ mH}$ ,  $V_{DD} = 50\text{ V}$ ,  $I_{AS} = 24\text{ A}$ ,  $R_G = 25\text{ }\Omega$ , Starting  $T_J = 25\text{ }^\circ\text{C}$
5. Mount on minimum PCB layout

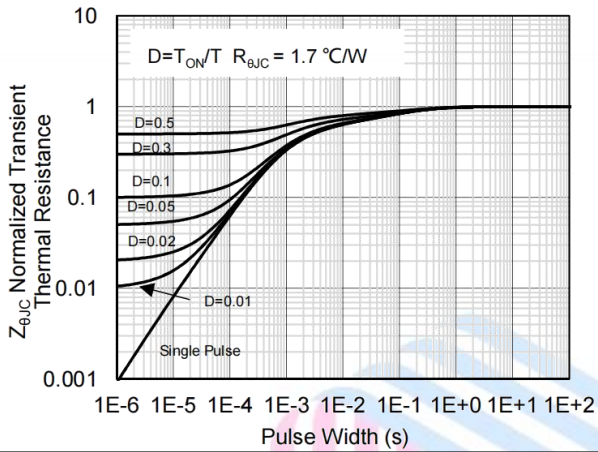
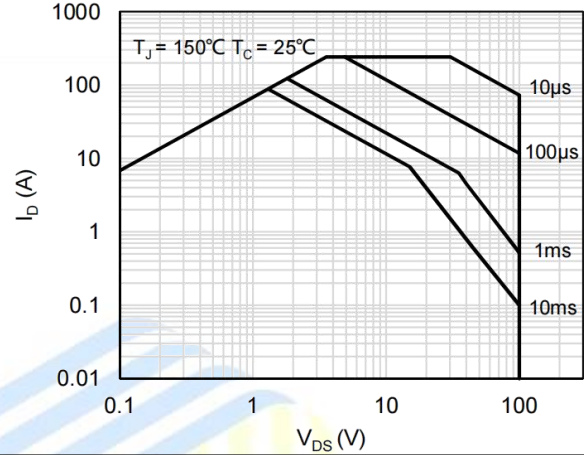
**Electrical Characteristics** ( $T_J = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Units
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0\text{ V}, I_D = 250\text{ }\mu\text{A}$	100	-	-	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 100\text{ V}, V_{GS} = 0\text{ V}$	-	-	1	$\mu\text{A}$
Gate Leakage Current	$I_{GSS}$	$V_{GS} = \pm 20\text{ V}, V_{DS} = 0\text{ V}$	-	-	$\pm 100$	nA
Gate Threshold voltage	$V_{GS(TH)}$	$V_{DS} = V_{GS}, I_D = 250\text{ }\mu\text{A}$	2	3.2	4	V
Drain-Source on-state resistance	$R_{DS(ON)}$	$V_{GS} = 10\text{ V}, I_D = 20\text{ A}$	-	6.7	8.2	mΩ
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{ISS}$	$V_{DS} = 50\text{ V},$	-	3120	-	pF
Output Capacitance	$C_{OSS}$	$V_{GS} = 0\text{ V},$	-	660	-	pF
Reverse Transfer Capacitance	$C_{RSS}$	$F = 1\text{ MHz}$	-	34	-	pF
Gate Resistance	$R_G$	$F = 1\text{ MHz}$	-	1.2	-	Ω
<b>Switching Characteristics</b>						
Turn On Delay Time	$T_{D(ON)}$	$V_{DD} = 50\text{ V},$ $R_L = 2.5\text{ }\Omega, V_{GS} = 10\text{ V},$ $R_G = 6.8\text{ }\Omega$	-	18	-	nS
Rise Time	$T_R$		-	24	-	nS
Turn Off Delay Time	$T_{D(OFF)}$		-	37	-	nS
Fall Time	$T_F$		-	16	-	nS
Total Gate Charge	$Q_G$		$V_{DD} = 50\text{ V},$	-	45	-
Gate-Source Charge	$Q_{GS}$	$I_D = 20\text{ A},$	-	13	-	nC
Gate-Drain Charge	$Q_{GD}$	$V_{GS} = 10\text{ V}$	-	10	-	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
Maximum Continuous Body-Diode Forward	$I_S$		-	-	60	A
Maximum Pulsed Body-Diode Forward Current <sup>NOTE 1</sup>	$I_{SM}$		-	-	240	A
Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0\text{ V}, I_S = 50\text{ A}$	-	0.98	-	V
Reverse Recovery Time	$T_{RR}$	$V_{DD} = 50\text{ V},$	-	58	-	nS
Reverse Recovery Charge	$Q_{RR}$	$I_D = 15\text{ A},$	-	101	-	nC
Peak Reverse Recovery Current	$I_{RRM}$	$di/dt = 100\text{ A}/\mu\text{S}$	-	3	-	A

## Electrical Characteristics Diagrams

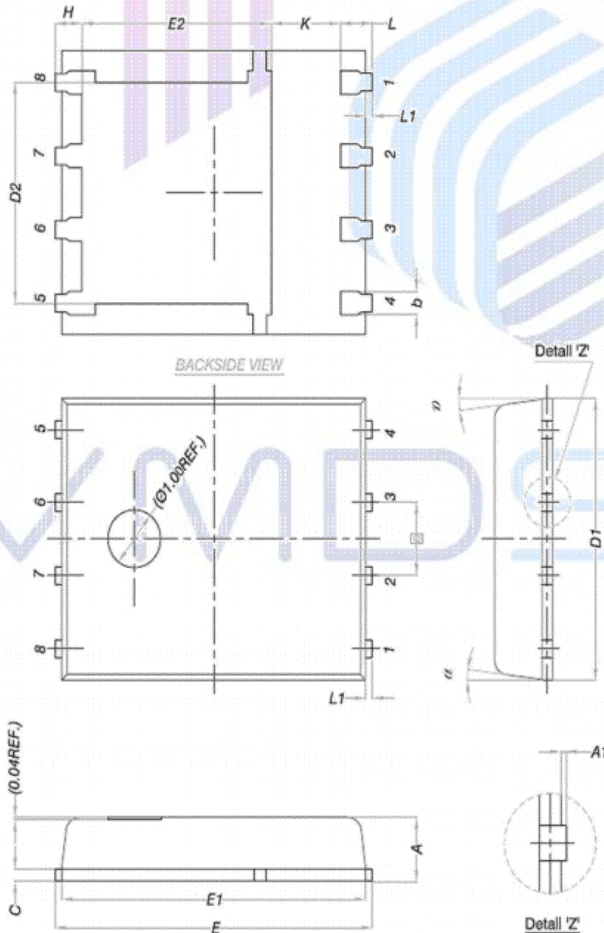


**Figure 7: Body-Diode Characteristics**

**Figure 8: On-Resistance vs. Gate-Source Voltage**

**Figure 9: Capacitance Characteristics**

**Figure 10: Gate-Charge Characteristics**

**Figure 11: Power De-rating**

**Figure 12: Current De-rating**


**Figure 13: Normalized Maximum Transient Thermal Impedance**

**Figure 14: Maximum Forward Biased Safe Operating Area**


## Mechanical Dimensions

### PDFN5X6 Package Information



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
A1	0	-	0.05
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
$\square$	1.27 BSC		
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
$\alpha$	0°	-	12°

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