

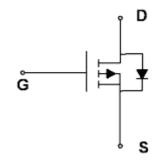


# VNDSEMI



## Description

V <sub>(BR)DSS</sub>	R <sub>DS(ON)_max</sub>	ID	
2017	34mΩ@-4.5V	5 7 4	
-20V	45mΩ@-2.5V	-5.7A	



Symbol of P2305



Symbol

#### Features

- Excellent package for good heat dissipation
- Advanced Trench technology
- Low Gate Charge

#### Application

- PWM applications
- Load switch
- Power Management

SOT - 23

Package Type of P2305

#### **Ordering Information**

$\checkmark$	Product Name	Package
	P2305	SOT - 23



#### Absolute Maximum Ratings(T<sub>A</sub>= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage		V <sub>DS</sub>	-20	V
Gate-Source Voltage		V <sub>GS</sub>	±12	V
Continuous Drain Current <sup>Note 1</sup>	T <sub>C</sub> =25°C	ID	-5.7	A
Pulsed Drain Current <sup>Note 2</sup>	T <sub>C</sub> =25°C	I <sub>DM</sub>	-28.5	A
Max Power Dissipation Note 3	T <sub>C</sub> =25°C	PD	1.15	W
Avalanche Energy, Single Pulse Note 4	·	E <sub>AS</sub>	14.4	mJ
Operating and Storage Temperature Range		T <sub>J</sub> ,T <sub>SGT</sub>	-55 to 150	°C

#### **Thermal Resistance**

Parameter	Symbol	Min	<mark>Ту</mark> р	Max	Unit
Thermal Resistance, Junction-to-Case	R <sub>0JC</sub>	-	1 <mark>08</mark> .7	-	°C/W

Notes:

1) Calculated continuous current based on maximum allowable junction temperature.

- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P<sub>D</sub> is based on max. junction temperature, using junction-case thermal resistance.
- 4)  $V_{DD}$ = -20V,  $V_{GS}$ = -4.5 V, L=0.1 mH, starting T<sub>J</sub>=25 °C.

## VMDSEMI



#### P2305

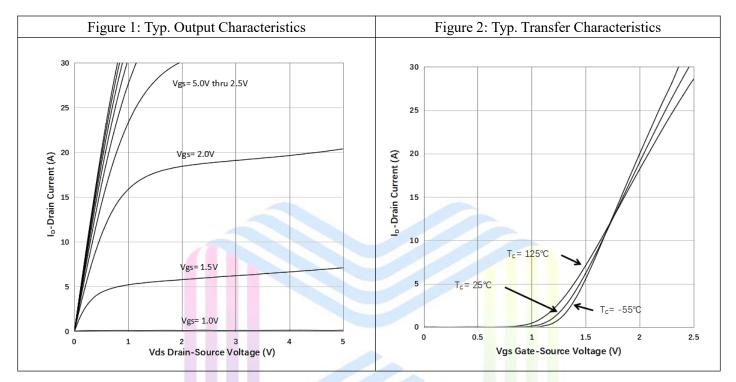
Parameter	Parameter Symbol Test Conditions		Min	Тур	Max	Unit
Statistic Characteristics	-	•				
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS}=0V, I_{D}=-250uA$	-20	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = -20V, $V_{GS}$ =0V	-	-	-1	uA
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	±100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_D=-250uA$	-0.45	-0.7	-1.0	V
Statia Durin Sauna On Davistanaa	р	$V_{GS}$ = -4.5V, $I_D$ = -4.1A	-	25.8	34	mΩ
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ = -2.5V, $I_D$ = -3A	-	33.0	45	
Gate Resistance	Rg	f=1MHz, Open Drain	-	7.0	-	Ω
Forward Transconductance	gfs	$V_{DS}$ = -5.0V, $I_D$ = -4.1A	-	19.7	-	S
Dynamic Characteristics						
Input Capacitance	Ciss	V <sub>GS</sub> =0V	-	1046	-	pF
Output Capacitance	Coss	$V_{DS}$ = -10V	-	105	-	pF
Reverse Transfer Capacitance	Crss	f=1MHz	-	96	-	pF
Turn-on Delay Time	t <sub>d(on)</sub>	$V_{DD}$ = -10V	-	8.2	-	
Rise Time	tr	$V_{GS} = -4.5V$	-	38.4	-	
Turn-off Delay Time	t <sub>d(off)</sub>	I <sub>D</sub> = -4.1A	-	35	-	ns
Fall Time	tf	$R_G=1.0\Omega$	-	48	-	
Gate Charge Characteristics						
Total Gate Charge	Qg	$V_{GS}$ = -4.5V	0-19	11.2	-	
Gate to Source Charge	Q <sub>gs</sub>	$V_{DS}$ = -10V	-	2.94	-	nC
Gate to Drain Charge	Qgd	$I_{D} = -4.1A$	-	2.09	-	
Reverse Diode Characteristics	1		•			
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	$V_{GS}=0V, I_{S}=-4.1A$	-	-0.77	-1.2	V
Reverse recovery time	trr L L L L L L L L L		-	9.2	-	ns
Reverse recovery charge	Qrr	Isd= $-4.1A$ , dI/dt= $80A$ /us	-	2.4	-	nC

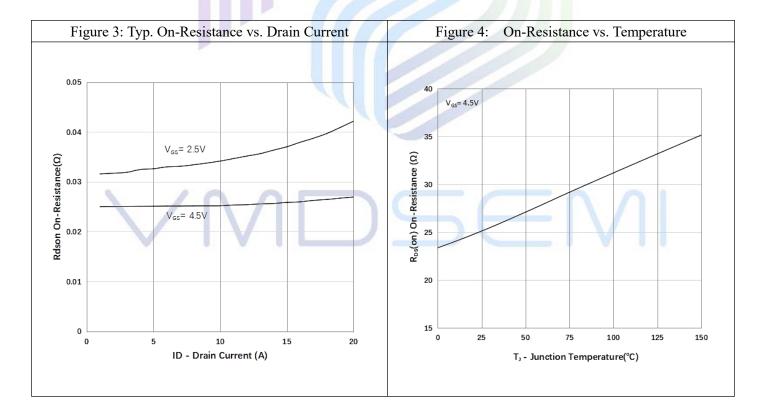
VMDSEMI

### Electrical Characteristics(T<sub>A</sub>= 25 °C, unless otherwise specified)



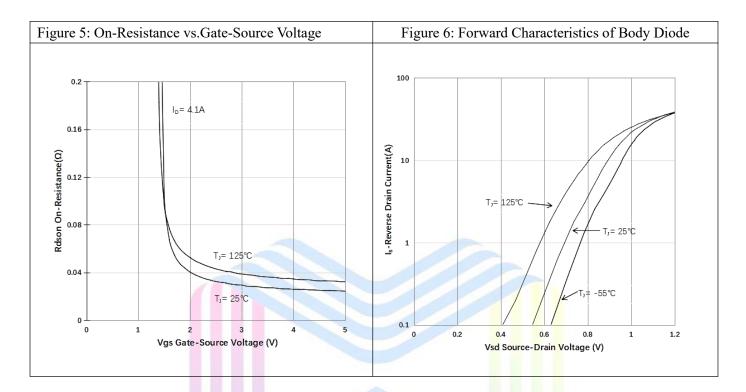
## **Typical Performance Characteristics**

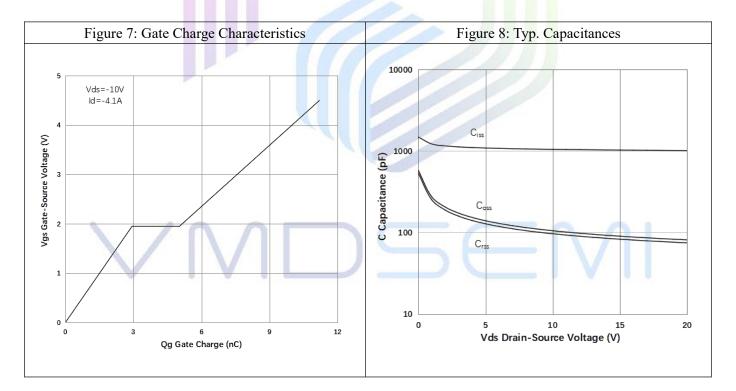




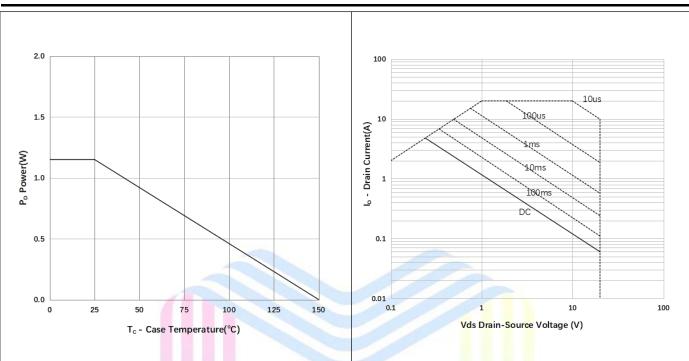


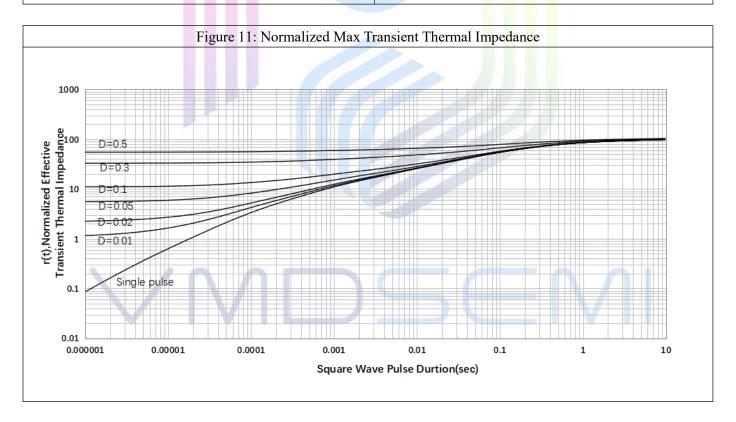
P2305











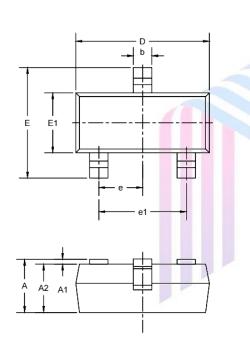
P2305



## **Mechanical Dimensions**

#### **SOT-23 Package Information**

COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)



MIN	MAX		
0.9	1.15		
0	0.1		
0.9	1.05		
<mark>0.</mark> 3	0.5		
<mark>0.</mark> 08	0.15		
<mark>2.</mark> 8	3		
<mark>2.</mark> 25	2.55		
1.2	1.4		
0. 95TYP			
1.8	2		
0.3	0.5		
0°	8°		
	0.9 0 0.9 0.3 0.08 2.8 2.25 1.2 0.95 1.8 0.3		

## VMDSEMI



#### NOTICE

Hangzhou VMD Semiconductor Co., Ltd (VMD) reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to VMD's terms and conditions supplied at the time of order acknowledgement.

VMD, its affiliates, agents, and employees, and all persons acting on its or their behalf, disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

VMD disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify VMD's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

VMD warrants performance of its hardware products to the specifications at the time of sale, testing, reliability and quality control are used to the extent VMD deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

VMD does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using VMD's components. To minimize risk, customers must provide adequate design and operating safeguards.

VMD does not warrant or convey any license to any intellectual property rights either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in VMD's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice.

VMD is not responsible or liable for such altered documentation. Resale of VMD's products with statements different from or beyond the parameters stated by VMD for that product or service voids all express or implied warrantees for the associated VMD product or service and is an unfair and deceptive business practice.

All Rights Reserved.

## VMDSEMI



## Via-Media Semiconductor Limited Company

#### http://www.vmdsemi.com

#### Main Sites:

#### - Headquarters

Hangzhou Via-Media Semiconductor Co., LTD. 1305-1306, Building 71, No. 90, Wensan Road, Xihu District, Hangzhou, Zhejiang Province, P.R. China Tel: +86-0571-8515 0563

#### - Shanghai

Shanghai R&D Center. 1506~1508, Xinyin Building, 888 Yishan Road, Shanghai, P.R of China

Tel: +86-021-54201999

#### - Xi'an

Xi'an R&D Center Room 10504, Building 2, Central Plaza, Jinye Road, High tech Zone, Xi'an City, Shanxi Province, R.P. of China

#### - Chengdu Office

Chengdu Winhi Semiconductor Co., LTD. Floor 15, Building 5, No. 171, Hele 2<sup>nd</sup> Street, Chengdu, Sichuan Province, P.R. China Tel: +86-028-8505 0771

#### Shenzhen

Shenzhen Sales office . Room 4A15, Block AB, Tianxiang Building, Chegongmiao, Futian District, Shenzhen, P.R of China Tel: +86-0755-82570682