



VMDSEMI

VUTL004R100PA

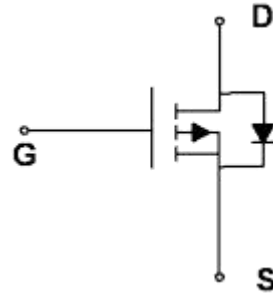
Datasheet



VMDSEMI

General Description
Symbol

$V_{(BR)DSS}$	$R_{DS(ON)_{max}}$	I_D
-40V	10mΩ@-10V	-81A
	14mΩ@-4.5V	



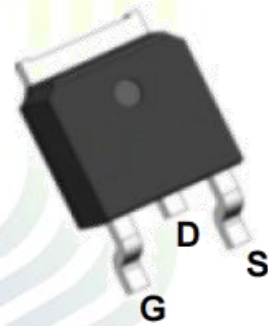
Symbol of VUTL004R100PA

Features

- Excellent package for good heat dissipation
- Advanced Trench technology
- Power Management Switches

Application

- Power switch
- Load switch in high current applications
- DC-DC converter

Package Type


TO-252

Package Type of VUTL004R100PA

Ordering Information

Product Name	Package
VUTL004R100PA	TO-252

Absolute Maximum Ratings($T_A=25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DS}	-40	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ^{Note 1}	$T_C=25^\circ\text{C}$	I_D	-81	A
Pulsed Drain Current ^{Note 2}	$T_C=25^\circ\text{C}$	I_{DM}	-240	A
Max Power Dissipation ^{Note 3}	$T_C=25^\circ\text{C}$	P_D	87	W
Avalanche Energy, Single Pulse ^{Note 4}		E_{AS}	378	mJ
Operation Junction temperature		T_J, T_{SGT}	-55 to 150	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	-	1.43	-	$^\circ\text{C/W}$

Notes:

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_D is based on max. junction temperature, using junction-case thermal resistance.
- 4) $V_{DS} = -20\text{V}, V_{GS} = -10\text{V}, L = 1\text{mH}, R_G = 25\Omega$, starting $T_J = 25\text{ }^\circ\text{C}$.

Electrical Characteristics($T_A=25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=-250\mu A$	-40	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-40V, V_{GS}=0V$	-	-	-1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1.0	-1.7	-2.5	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-10V, I_D=-20A$	-	8.1	10	mΩ
		$V_{GS}=-4.5V, I_D=-10A$	-	10.1	14	
Forward Transconductance	gfs	$V_{DS}=-10V, I_D=-20A$	-	33	-	S
Dynamic Characteristics						
Input Capacitance	C_{ISS}	$V_{GS}=0V$	-	4272	-	pF
Output Capacitance	C_{OSS}	$V_{DS}=-20V$	-	328	-	pF
Reverse Transfer Capacitance	C_{RSS}	f=1MHz	-	242	-	pF
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=-20V$	-	9	-	ns
Rise Time	t_r	$V_{GS}=-10V$	-	3.7	-	
Turn-off Delay Time	$t_{d(off)}$	$R_L=2\Omega$	-	52.4	-	
Fall Time	t_f	$R_G=1\Omega$	-	38.9	-	
Gate Charge Characteristics						
Total Gate Charge	Q_g	$V_{GS}=-10V$	-	85	-	nC
Gate to Source Charge	Q_{gs}	$V_{DS}=-20V$	-	14	-	
Gate to Drain Charge	Q_{gd}	$I_D=-20A$	-	17	-	
Reverse Diode Characteristics						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=-20A$	-	-0.84	-1.2	V

Typical Performance Characteristics

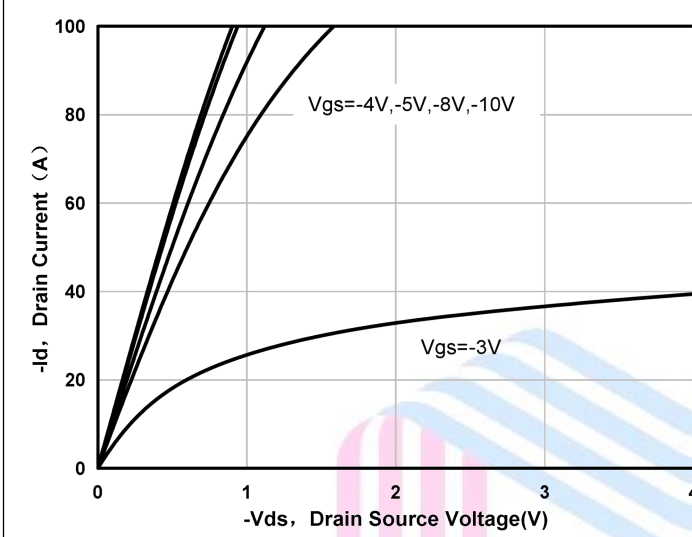
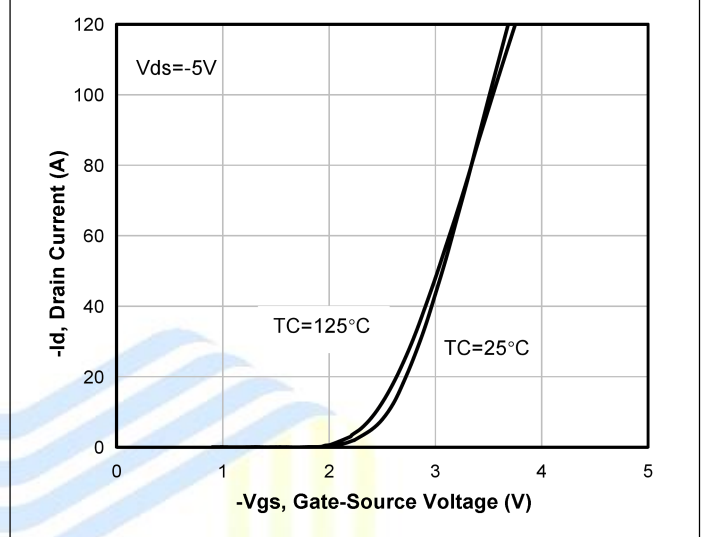
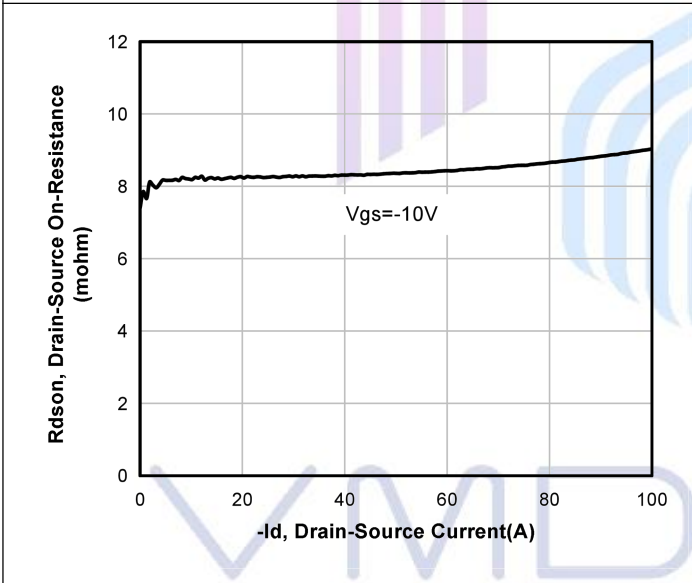
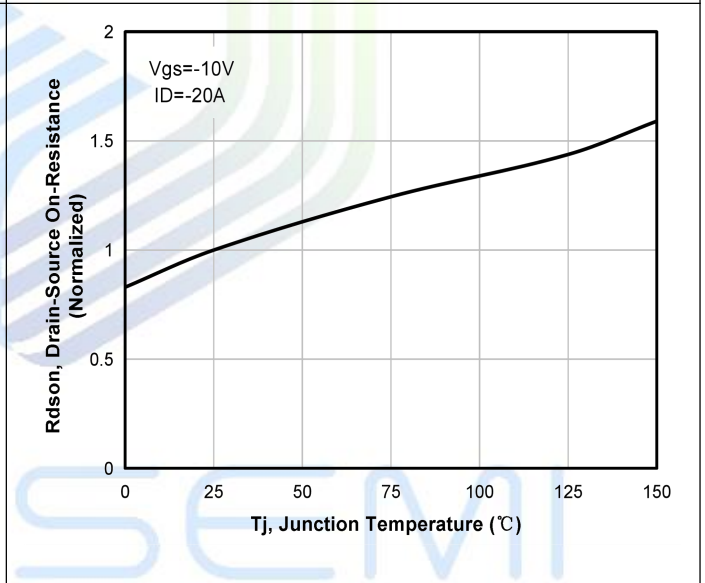
Figure 1: Typ. Output Characteristics

Figure 2: Typ. Transfer Characteristics

Figure 3: Typ. On-Resistance vs. Drain Current

Figure 4: On-Resistance vs. Temperature


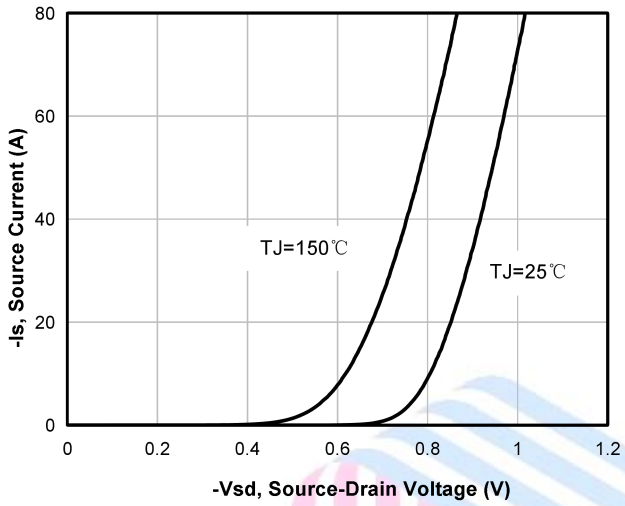
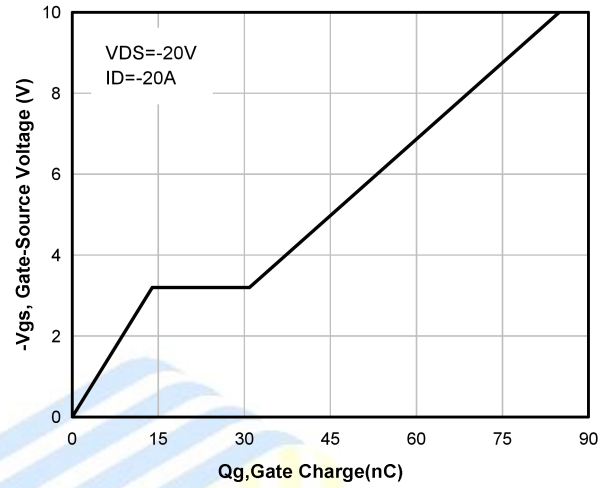
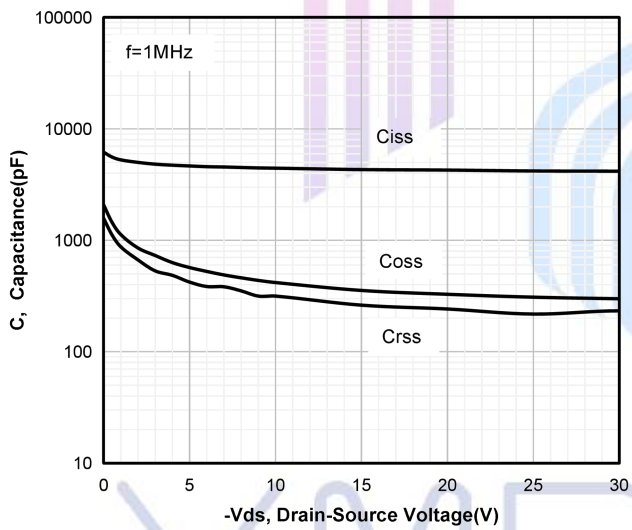
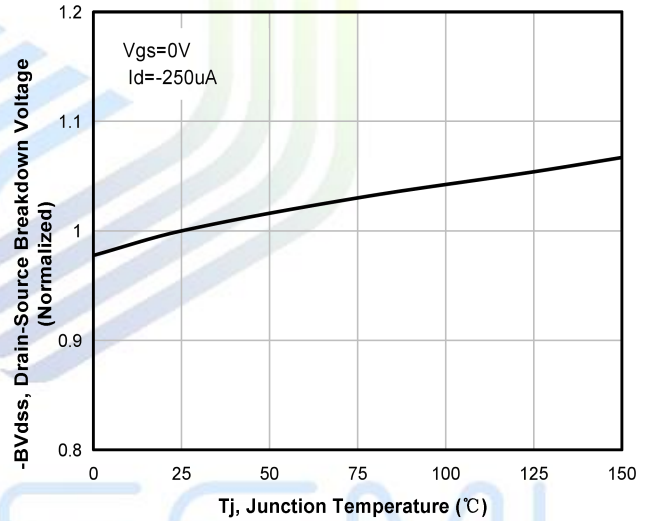
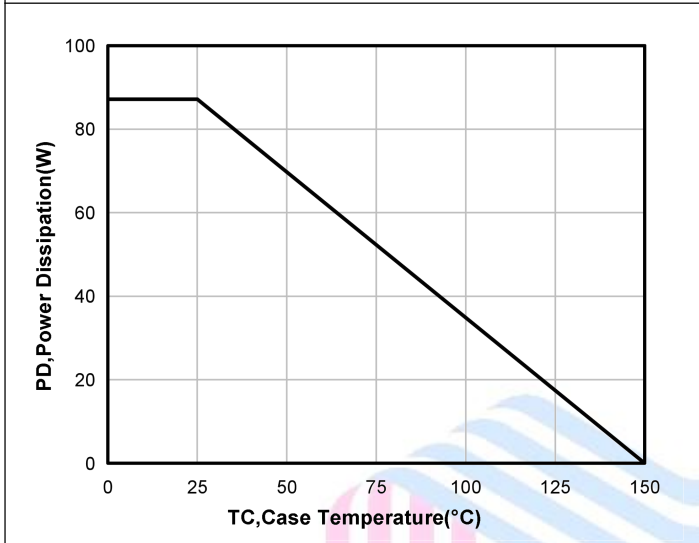
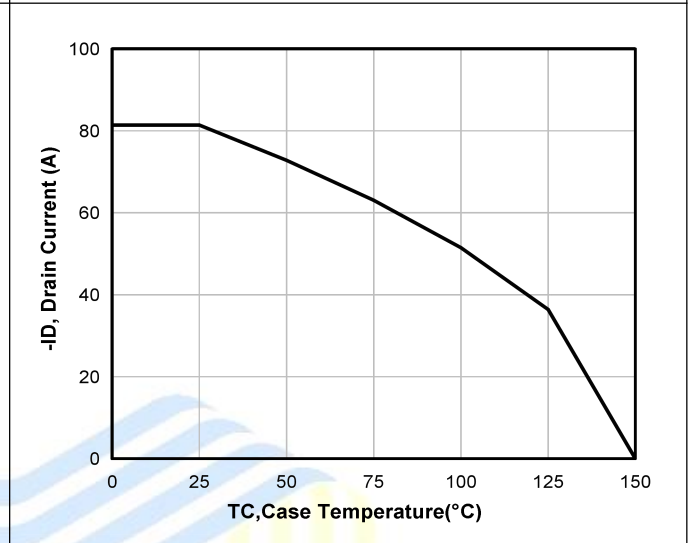
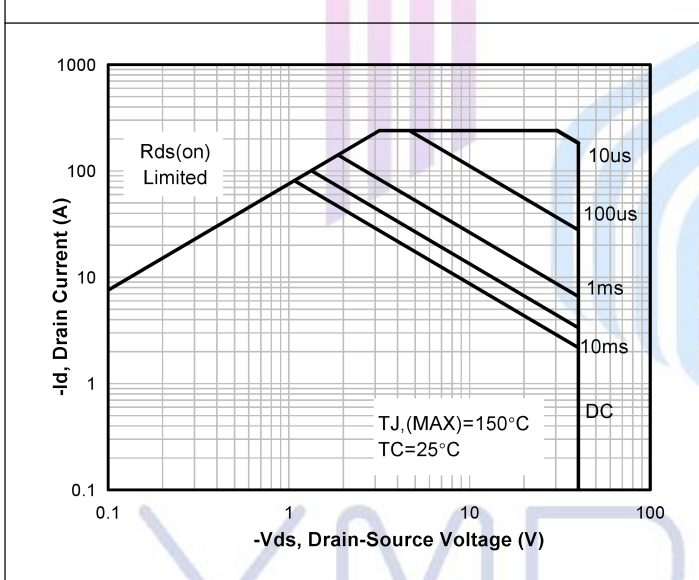
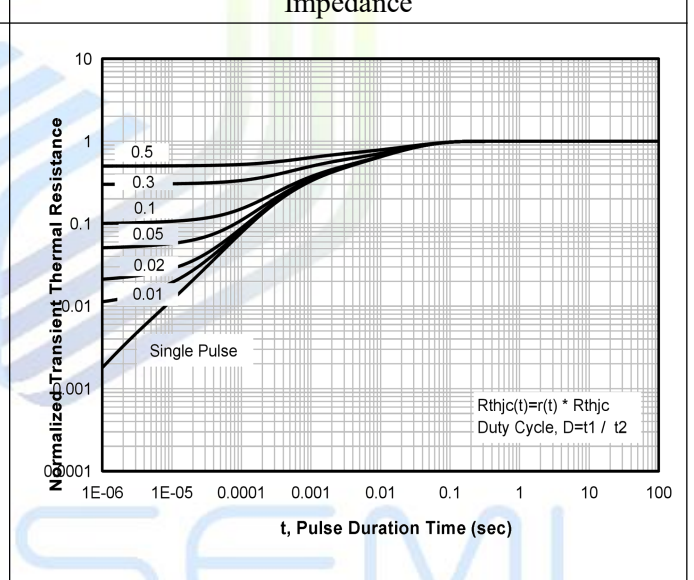
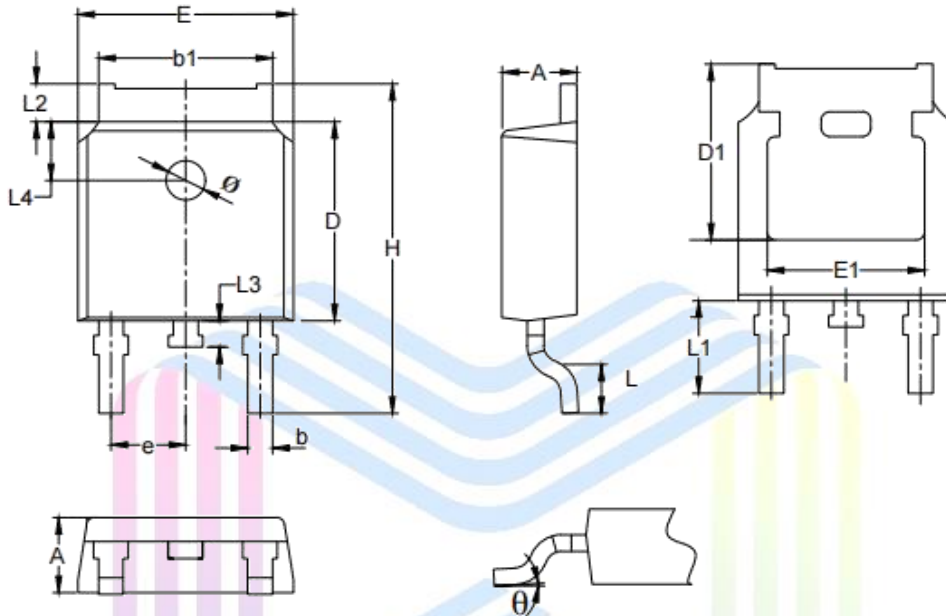
Figure 5: Forward Characteristics of Body Diode

Figure 6: Gate Charge Characteristics

Figure 7: Typ. Capacitances

Figure 8: BV_{DSS} vs. Temperature


Figure 9: Power Dissipation

Figure 10: ID Current Derating

Figure 11: Safe Operating Area

Figure 12: Normalized Max Transient Thermal Impedance


Mechanical Dimensions

TO-252 Package Information



SYMBOL	MILLIMETERS	
	MIN	MAX
A	2.2	2.4
A1	0	0.127
A2	-	-
b	0.66	0.9
b1	5.1	5.5
c	0.43	0.61
D	5.95	6.22
D1	5.3REF	
E	6.4	6.75
E1	4.8REF	
e	2.286BSC	
H	9.4	10.5
L	1.38	2
L1	2.9REF	
L2	0.88	1.28
L3	0.5	1
L4	1.8REF	
θ	0°	8°

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