



VMDSEMI

**VGTD065N150NA**

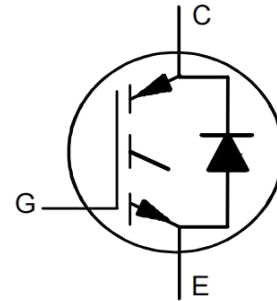
**Datasheet**



VMDSEMI

**General Description**
**Symbol**

$V_{CE}$	650	V
$I_C$	15	A
$V_{CEsat, Typ}$ $T_{vj} = 25\text{ }^\circ\text{C}$	1.65	V
$T_{jmax}$	150	$^\circ\text{C}$



Symbol of VGTD065N150NA

**Features**

- Offers high breakdown voltage to 650V
- Low  $V_{CEsat}$
- Easy parallel switching capability due to positive temperature coefficient in  $V_{CEsat}$
- IGBT copacked with fast and soft antiparallel diode
- Qualified according to JEDEC for target applications
- Pb-Free lead plating; RoHS Product
- Halogen and Antimony Free. "Green" Device

**Package Type**


Package Type of VGTD065N150NA

**Application**

- UPS
- Air Condition
- Motor Drives
- PFC

**Ordering Information**

Product Name	Package
VGTD065N150NA	TO-220F

**Absolute Maximum Ratings** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

Parameter	Symbol	Rating	Unit
Collector-emitter voltage	$V_{CE}$	650	V
DC collector current, limited by $T_{vj\text{max}}$	$T_c = 25\text{ }^\circ\text{C}$	30	A
	$T_c = 100\text{ }^\circ\text{C}$	15	
Pulsed collector current, $t_p$ limited by $T_{vj\text{max}}$	$I_{C,pulse}$	60	A
Diode forward current, limited by $T_{vj\text{max}}$	$T_c = 25\text{ }^\circ\text{C}$	30	A
	$T_c = 100\text{ }^\circ\text{C}$	15	
Diode pulsed current, $t_p$ limited by $T_{vj\text{max}}$	$I_{F,pulse}$	60	A
Gate-emitter voltage	$V_{GE}$	$\pm 20$	V
Short circuit withstand time $V_{GE} = 15\text{V}$ , $V_{CC} \leq 360\text{V}$ , Allowed number of short circuits < 1000, Times between short circuits: $\geq 1.0\text{s}$ , $T_j \leq 25\text{ }^\circ\text{C}$	tsc	12	us
Power dissipation	$T_c = 25\text{ }^\circ\text{C}$	91	W
Operating junction temperature	$T_{stg}$	-55 to 150	$^\circ\text{C}$
Soldering temperature, wave soldering 1.6mm (0.063in.) from case for 10s	$T_L$	260	$^\circ\text{C}$

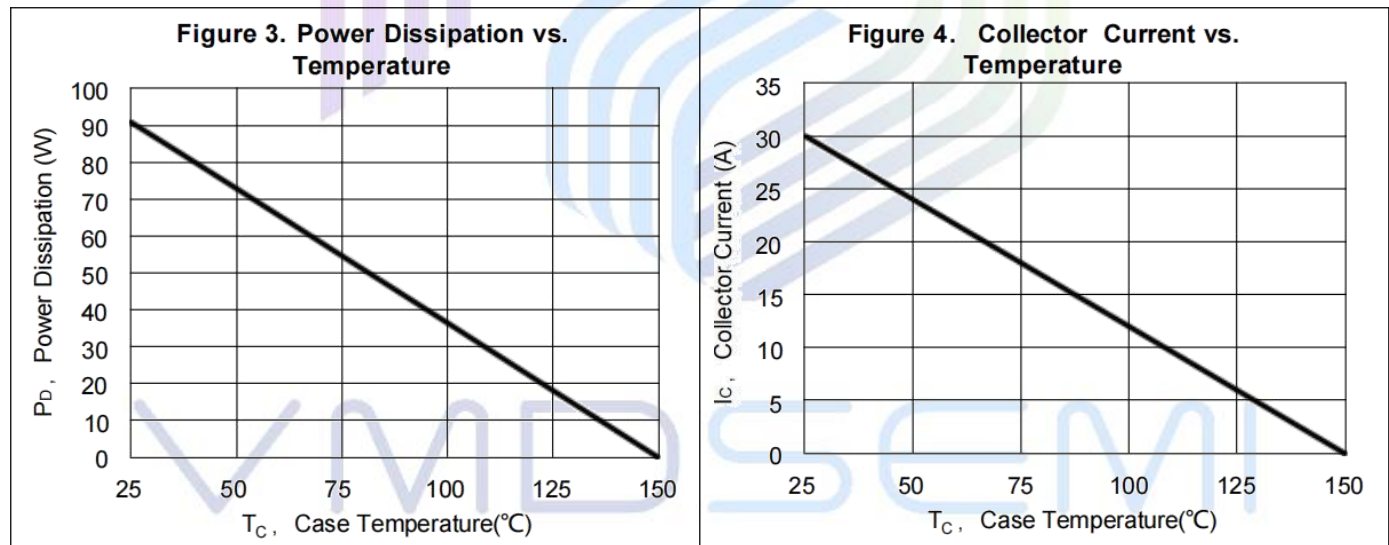
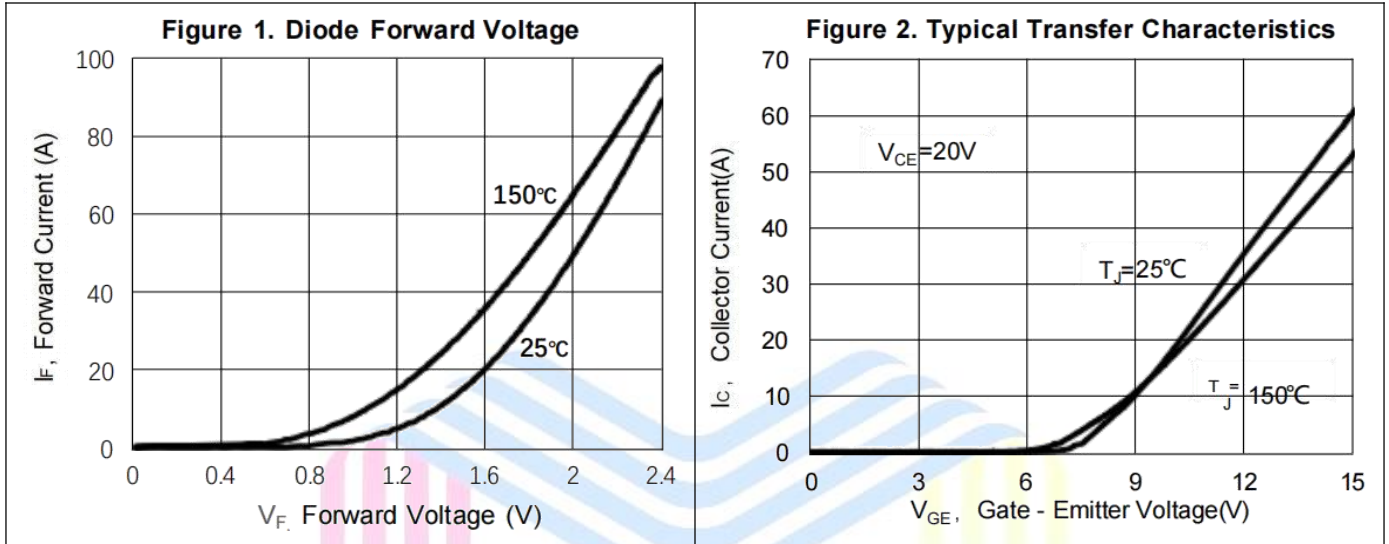
**Thermal Resistance**

Parameter	Symbol	Min	Typ	Max	Unit
IGBT Thermal Resistance, Junction to Case max.	$R_{\theta JC}$	-	1.37	-	$^\circ\text{C/W}$
Diode Thermal Resistance, Junction to Case max.	$R_{\theta JC}$	-	1.98	-	
Thermal Resistance, Junction to Ambient max.	$R_{\theta JA}$	-	39.2	-	



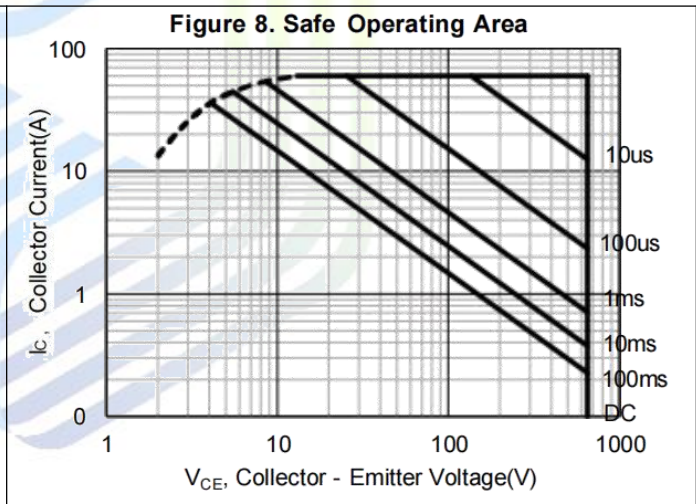
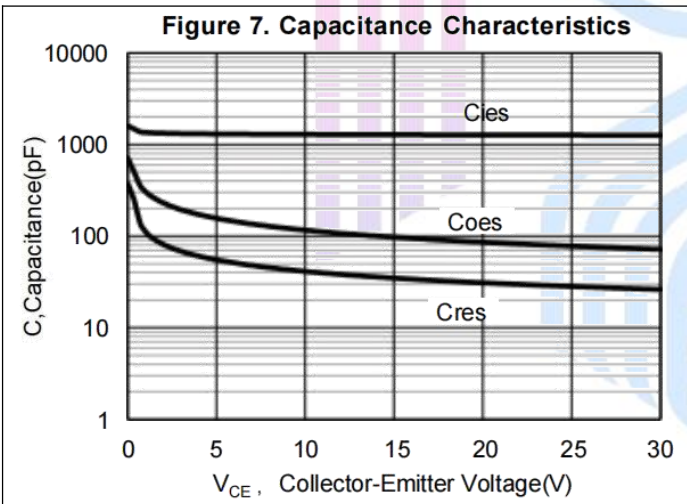
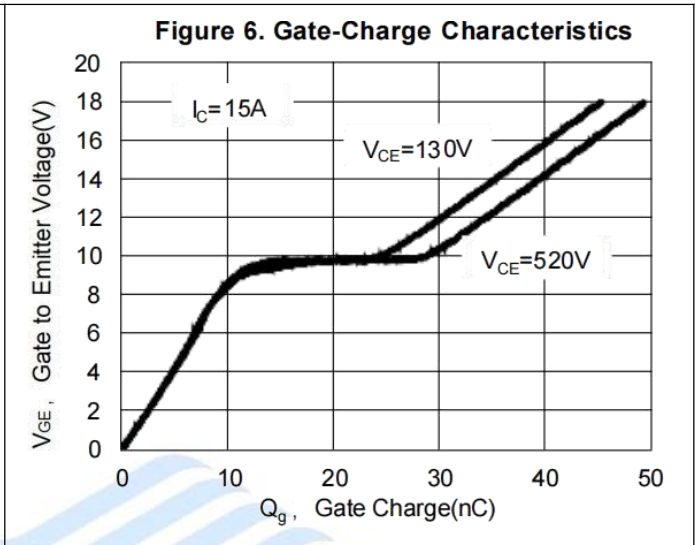
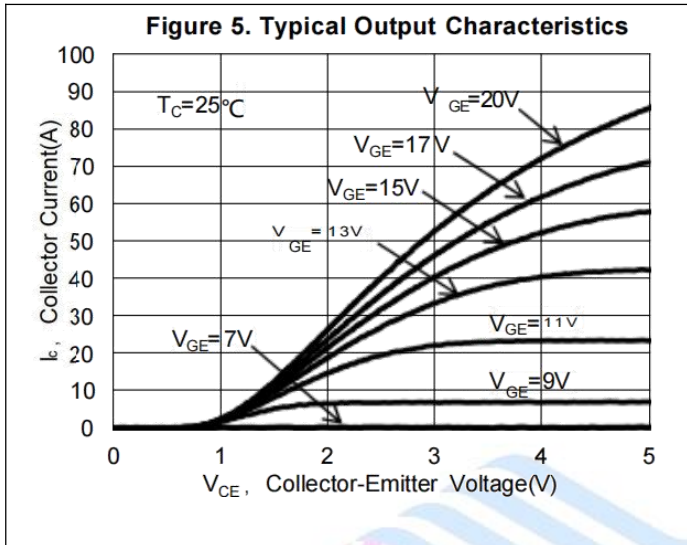
**15A, 650V, Insulated Gate Bipolar Transistor**
**VGTD065N150NA**
**Electrical Characteristics** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

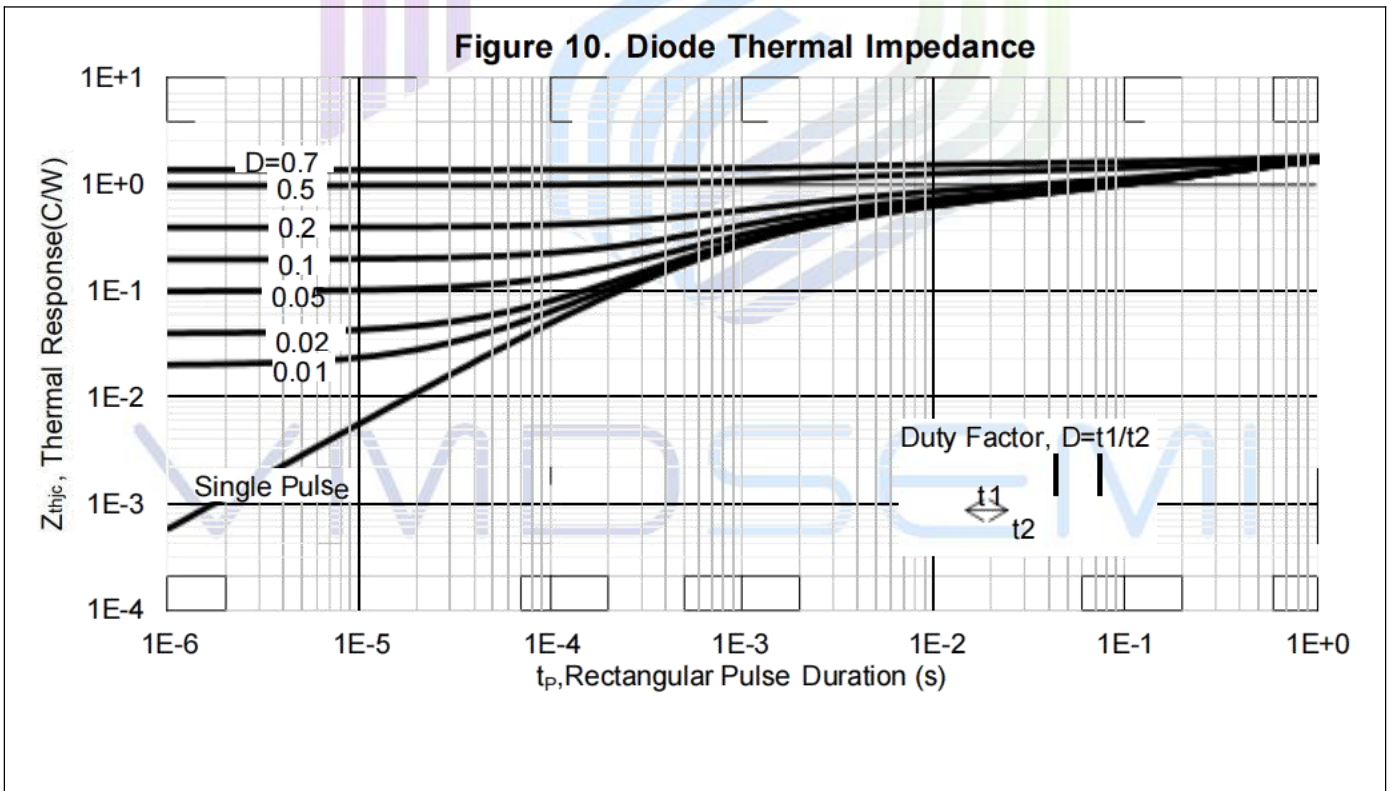
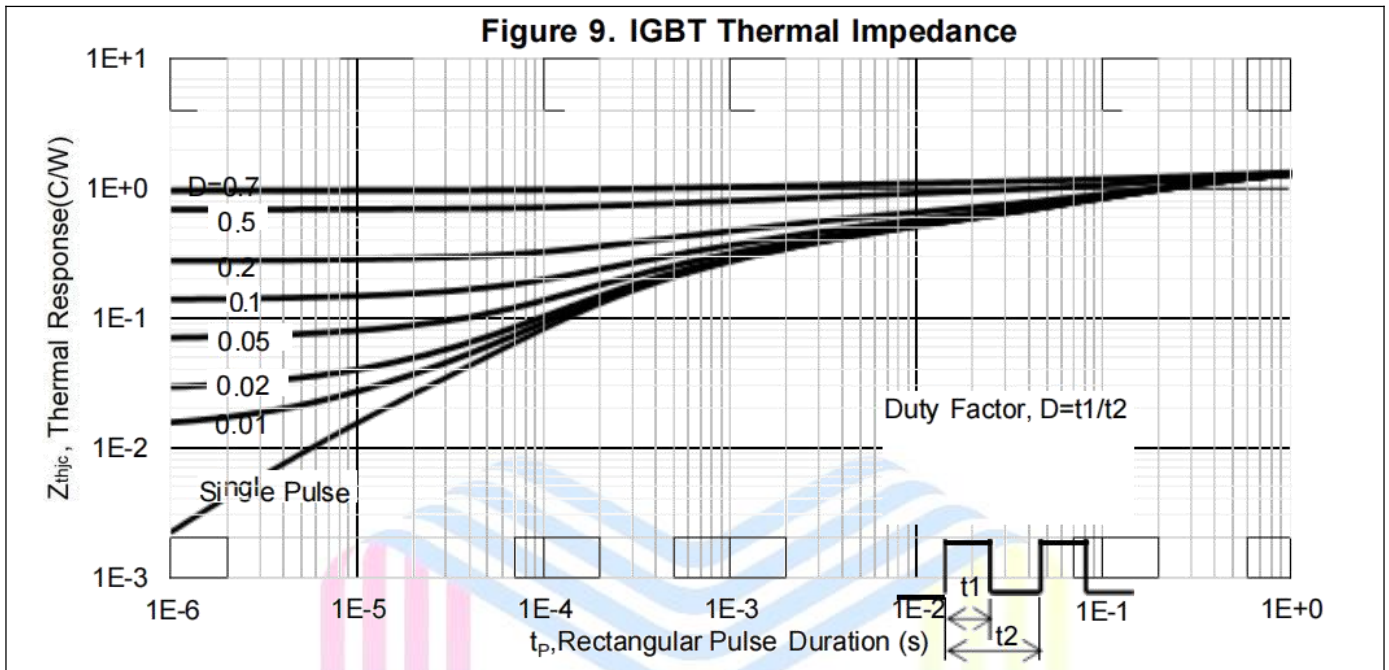
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Statistic Characteristics</b>						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{CE}=0V, I_C=250\mu A$	650	-	-	V
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{GE}=15V, I_C=15A, T_{vj}=25^\circ C$	-	1.65	1.9	V
Diode forward voltage	$V_F$	$V_{GE}=0V, I_C=15A, T_{vj}=25^\circ C$	-	1.45	1.7	V
Gate-emitter threshold voltage	$V_{GE(th)}$	$V_{GE}=V_{CE}, I_C=250\mu A$	4.5	-	6.5	V
Zero voltage gate collector current	$I_{CES}$	$V_{CE}=650V, V_{GE}=0V, T_{vj}=25^\circ C$	-	-	1	$\mu A$
Gate-emitter leakage current	$I_{GES}$	$V_{GE}=\pm 20V, V_{CE}=0V$	-	-	100	nA
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{ies}$	$V_{CE}=25V$	-	1270.3	-	pF
Output Capacitance	$C_{oes}$	$V_{GE}=0V$	-	78.0	-	pF
Reverse Transfer Capacitance	$C_{res}$	$f=1MHz$	-	28.3	-	pF
Gate total charge	$Q_G$	$V_{CE}=520V$	-	42	-	nC
Gate-Emitter charge	$Q_{GE}$	$V_{GE}=15V$	-	10.7	-	
Gate-Collector charge	$Q_{GC}$	$I_C=15A$	-	18.3	-	
Short circuit collector current Max.1000 short circuits, Times between short circuits: $\geq 1.0s$	$I_{SC}$	$V_{CC} \leq 360V$ $V_{GE} = 15V$ $t_{SC} \leq 8\mu s, T_J \leq 25^\circ C$	-	100	-	A
<b>Switching Characteristic, Inductive Load IGBT Characteristic, @<math>T_{vj} = 25^\circ C</math></b>						
Turn-on delay time	$t_{d(on)}$	$T_{vj} = 25^\circ C$ $V_{CE}=400V$ $V_{GE}=10V$ $I_C=15A$ $R_G=10\Omega$	-	12	-	ns
Rise time	$t_r$		-	26	-	ns
Turn-off delay time	$t_{d(off)}$		-	30	-	ns
Fall time	$t_f$		-	244	-	ns
Turn-on energy	$E_{on}$		-	0.24	-	mJ
Turn-off energy	$E_{off}$		-	0.57	-	mJ
<b>Diode Characteristics @<math>T_{vj} = 25^\circ C</math></b>						
Diode reverse recovery time	$t_{rr}$	$T_{vj} = 25^\circ C$	-	36	-	ns
Diode reverse recovery charge	$Q_{rr}$	$V_{CE}=400V$	-	85	-	nC
Diode peak reverse recovery current	$I_{rrm}$	$I_F=15A,$ $di/dt=200A/\mu s$	-	4.6	-	A

**Typical Performance Characteristics**


# 15A, 650V, Insulated Gate Bipolar Transistor

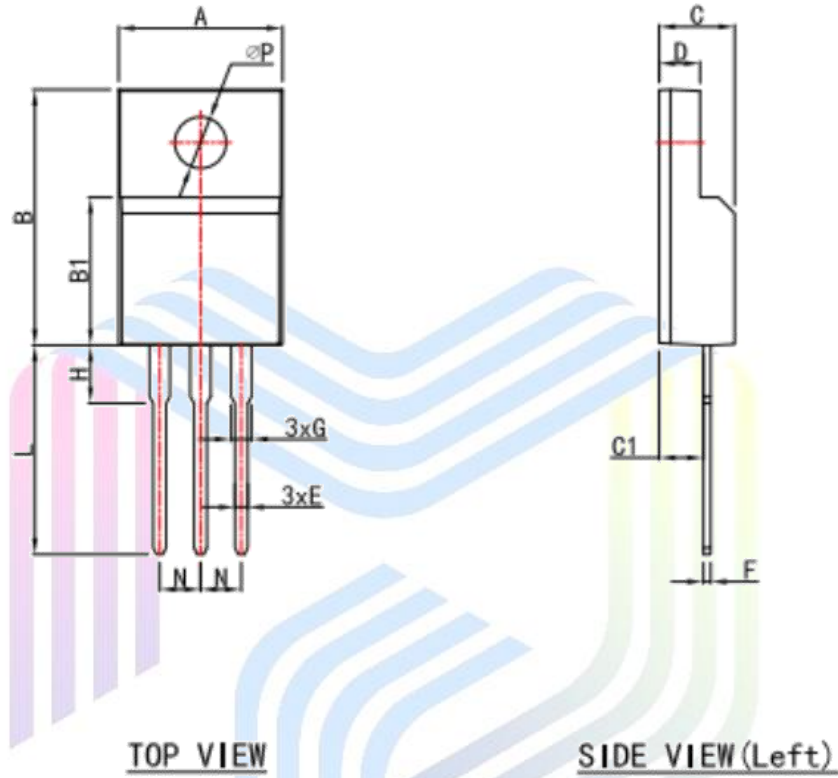
# VGTD065N150NA





## Mechanical Dimensions

### TO220F Package Information



SYMBOL	MIN	TYP	MAX
A	9.60	10.00	10.40
B	15.40	15.80	16.20
B1	8.90	9.20	9.50
C	4.30	4.60	4.90
C1	2.10	2.55	3.00
D	2.40	2.70	3.00
E	0.60	0.80	1.00
F	0.3	0.45	0.6
G	1.12	1.27	1.42
H	3.40	-	3.80
	2.40	-	2.90
L	12.00	13.00	14.00
N	2.34	2.54	2.74
Q	3.15	3.35	3.55
ΦP	2.90	3.10	3.30
All Dimensions in mm			



## NOTICE

Hangzhou VMD Semiconductor Co., Ltd (VMD) reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to VMD's terms and conditions supplied at the time of order acknowledgement.

VMD, its affiliates, agents, and employees, and all persons acting on its or their behalf, disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

VMD disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify VMD's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

VMD warrants performance of its hardware products to the specifications at the time of sale, testing, reliability and quality control are used to the extent VMD deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

VMD does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using VMD's components. To minimize risk, customers must provide adequate design and operating safeguards.

VMD does not warrant or convey any license to any intellectual property rights either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in VMD's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice.

VMD is not responsible or liable for such altered documentation. Resale of VMD's products with statements different from or beyond the parameters stated by VMD for that product or service voids all express or implied warranties for the associated VMD product or service and is an unfair and deceptive business practice.

All Rights Reserved.

VMDSEMI



## Via-Media Semiconductor Limited Company

<http://www.vmdsemi.com>

### Main Sites:

#### - Headquarters

Hangzhou Via-Media Semiconductor Co., LTD.  
1305-1306, Building 71, No. 90, Wensan Road, Xihu  
District, Hangzhou, Zhejiang Province, P.R. China  
Tel: +86-0571-8515 0563

#### - Chengdu Office

Chengdu Winhi Semiconductor Co., LTD.  
Floor 15, Building 5, No. 171, Hele 2<sup>nd</sup> Street,  
Chengdu, Sichuan Province, P.R. China  
Tel: +86-028-8505 0771

#### - Shanghai

Shanghai R&D Center.  
1506~1508, Xinyin Building, 888 Yishan Road,  
Shanghai, P.R of China  
Tel: +86- 021-54201999

#### - Shenzhen

Shenzhen Sales Center.  
17B, No.1 Phoenix Building, 2008 Shennan Road,  
Shenzhen, P.R of China  
Tel: +86-0755- 82570682

#### - Xi'an

Xi'an R&D Center  
1703B, Building A, Greenland Center, Jinye Road,  
High-Tech Zone, Xi'an, Shaanxi, P.R of China