

WinhiSemi

WR811X

Datasheet

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Features

- Precision voltage monitor for 3V, 3.3V or 5V power supplies
- Reset remains valid with V_{CC} as low as 1V
- 140ms minimum reset pulse width available
- 3 μ A typical supply current
- Available in 5-pin SOT23-5L package

Applications

- Computer
- Controller
- Intelligent Instruments
- Critical uP and uC Power Monitoring
- Portable/Battery-Powered Equipment

Description

The WR811X is a low-power microprocessor (μ P) supervisory circuit used to monitor power supplies in microprocessor and digital systems. Low supply current makes the WR811 ideal for use in portable equipment. The device comes in a 5-pin SOT23-5L package.

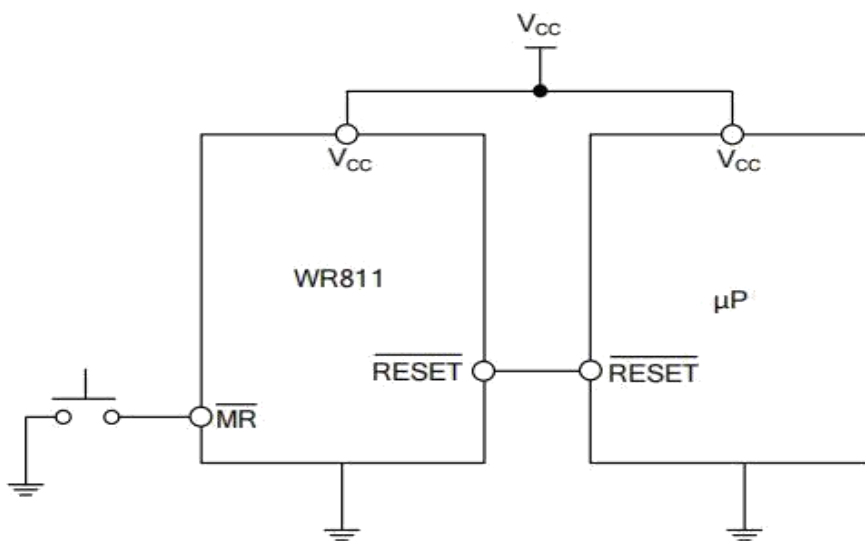
The WR811X provides excellent circuit reliability and low BOM cost by eliminating external components and adjustments when used with 5V-powered or 3V-powered circuits. The WR811X also provides a debounced manual reset input.

The function of this device is to assert a reset if either the power supply drops below a designed

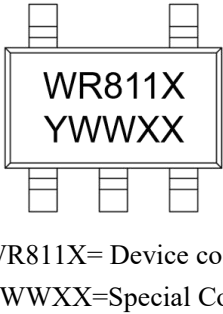
reset threshold level or \overline{MR} is forced low. The reset comparator is designed to ignore fast transients on V_{CC} . Reset thresholds are available for operation with a variety of supply voltages.



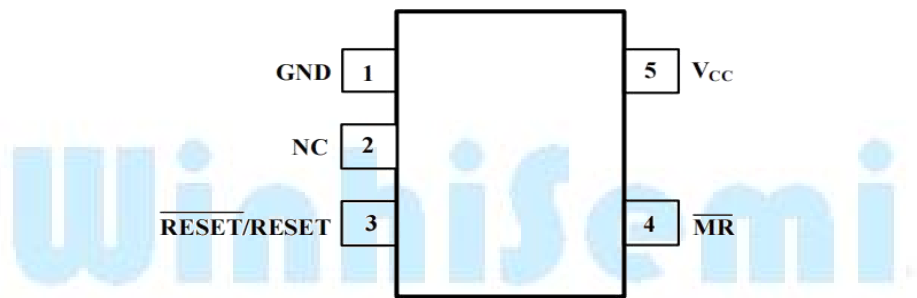
Typical Application Circuit



Ordering and Marking Information

Part Number	Marking Code	package
WR811	 <p>WR811X= Device code YWWXX=Special Code</p>	SOT23-5L

Pin Configuration



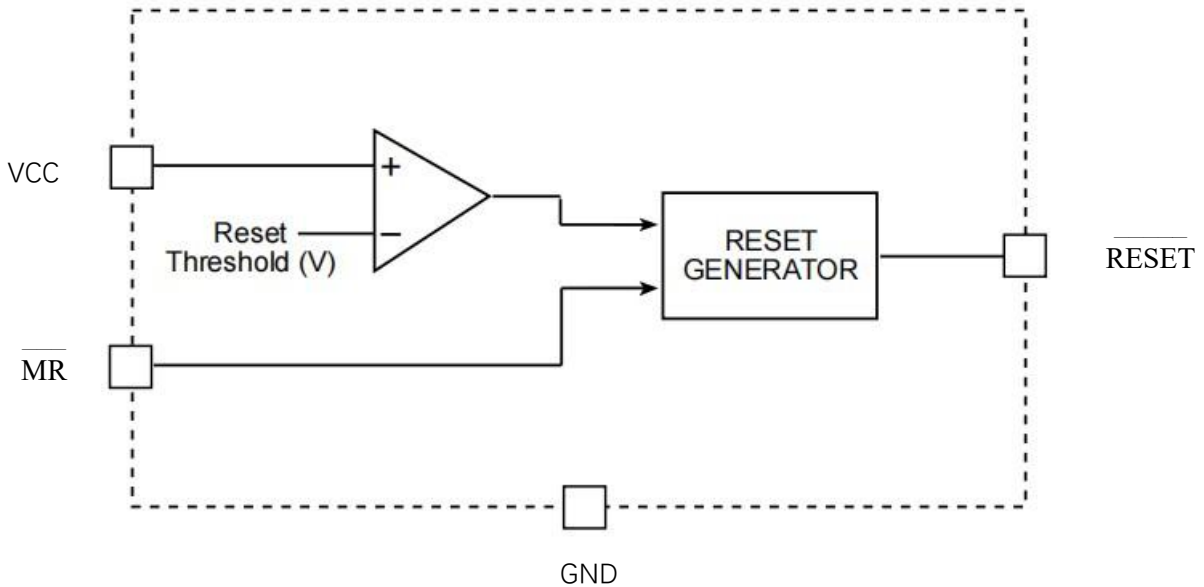
Pin Description

Pin No.	Name	Function
1	GND	IC Ground Pin.
2	NC	
3	$\overline{\text{RESET}}$	$\overline{\text{RESET}}$ goes low if V_{CC} falls below the reset threshold and remains asserted for one reset timeout period after V_{CC} exceeds the reset threshold.
4	$\overline{\text{MR}}$	Manual Reset Input. A logic low on $\overline{\text{MR}}$ forces a reset timeout period after $\overline{\text{MR}}$ goes high. This input can be shorted to ground via a switch or driven from CMOS or TTL logic. Float if unused.
5	VCC	Power Supply

Absolute Maximum Values

Parameter	Symbol	Value	Unit
Terminal Voltage(V_{CC})	V_{CC}	-0.3 to 6.0V	V
Input Current(V_{CC}, \overline{MR})	V_{CC}, \overline{MR}	20	mA
Output Current(\overline{RESET})	\overline{RESET}	20	mA
ESD Rating		3	KV
Lead Temperature(soldering,10sec)		300	°C
Junction Temperature		150	°C
Storage Temperature		-65 to 160	°C
Junction Temperature		150	°C
Storage Temperature		-65 to 150	°C
SOT23-5L Package Thermal Resistance	$R_{\theta JA}$	250	°C/W
SOT23-5L Package Thermal Resistance	$R_{\theta JC}$	115	°C/W

Functional Diagram



Electrical Characteristics

WR811-T(3.08) (TA = +25°C unless otherwise stated, VCC = 5.0V)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Operation Voltage Range	V _{CC}	1		5.5	V	T _A =-40°C to 85°C
Supply Current	I _{CC}		3	8	μA	V _{CC} =3.3V, no load
Reset Voltage Threshold	V _{TH}	3	3.08	3.15	V	
Reset Timeout Period	t _{RST}	140		560	ms	
RESET Output Voltage	V _{OH}	0.8×V _{CC}			V	I _{SOURCE} =500μA
	V _{OL}			0.3	V	V _{CC} =V _{TH} min, I _{SINK} =1.2mA
				0.3	V	V _{CC} >1V, I _{SINK} =50μA, T _A =-40°C to 85°C
MR Minimum Pulse Width		10			μs	
MR to Reset Delay			0.5		μs	
MR Input Threshold	V _{IH}	0.7×V _{CC}			V	
	V _{IL}			0.25×V _{CC}	V	
MR Pull-Up resistance		10	20	30	kΩ	
MR Glitch Immunity			100		ns	

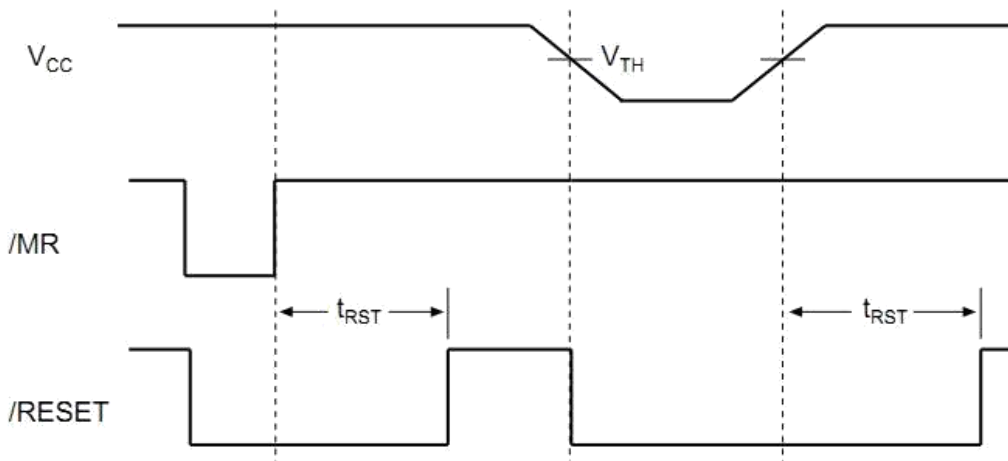
WR811-L(4.63) (TA = +25°C unless otherwise stated, VCC = 5.0V)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Operation Voltage Range	V _{CC}	1		5.5	V	T _A =-40°C to 85°C
Supply Current	I _{CC}		3	8	μA	V _{CC} =3.3V, no load
Reset Voltage Threshold	V _{TH}	4.5	4.63	4.8	V	
Reset Timeout Period	t _{RST}	140		560	ms	
RESET Output Voltage	V _{OH}	0.8×V _{CC}			V	I _{SOURCE} =500μA
	V _{OL}			0.3	V	V _{CC} =V _{TH} min, I _{SINK} =1.2mA
				0.3	V	V _{CC} >1V, I _{SINK} =50μA, T _A =-40°C to 85°C
MR Minimum Pulse		10			μs	
MR to Reset Delay			0.5		μs	
MR Input Threshold	V _{IH}	0.7×V _{CC}			V	
	V _{IL}			0.25×V _{CC}	V	
MR Pull-Up resistance		10	20	30	kΩ	
MR Glitch Immunity			100		ns	

WR811-R(2.63) (TA = +25°C unless otherwise stated, VCC = 5.0V)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Condition
Operation Voltage Range	V _{CC}	1		5.5	V	T _A =-40°C to 85°C
Supply Current	I _{CC}		3	8	μA	V _{CC} =3.3V, no load
Reset Voltage Threshold	V _{TH}	2.58	2.63	2.68	V	
Reset Timeout Period	t _{RST}	140		560	ms	
RESET Output Voltage	V _{OH}	0.8×V _{CC}			V	I _{SOURCE} =500μA
	V _{OL}			0.3	V	V _{CC} =V _{TH} min, I _{SINK} =1.2mA
				0.3	V	V _{CC} >1V, I _{SINK} =50μA, T _A =-40°C to 85°C
MR Minimum Pulse Width		10			μs	
MR to Reset Delay			0.5		μs	
MR Input Threshold	V _{IH}	0.7×V _{CC}			V	
	V _{IL}			0.25×V _{CC}	V	
MR Pull-Up resistance		10	20	30	kΩ	
MR Glitch Immunity			100		ns	

Timing Diagram



Reset Timing Diagram

Application Information

Microprocessor Reset

The $\overline{\text{RESET}}$ pin is asserted whenever V_{CC} falls below the reset threshold voltage. The $\overline{\text{RESET}}$ pin remains asserted for a period of 140ms after V_{CC} has risen above the reset threshold voltage. The reset and powers up in a known condition after a power failure. $\overline{\text{RESET}}$ will remain valid with V_{CC} as low as 1V.

VCC Transients

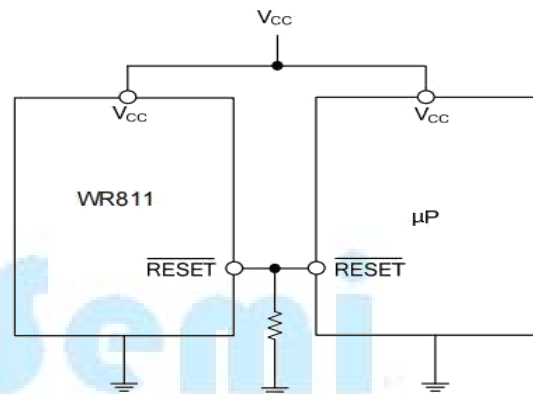
The WR811 is relatively immune to negative-going VCC glitches below the reset threshold. Typically, a negative-going transient 125mV below the reset threshold with a duration of 20 μ s or less will not cause a reset.

Interfacing to Bidirectional Reset Pins

The WR811 can interface with μ Ps with bidirectional reset pins by connecting a 4.7k Ω resistor in series with the WR811 output and the μ P reset pin.

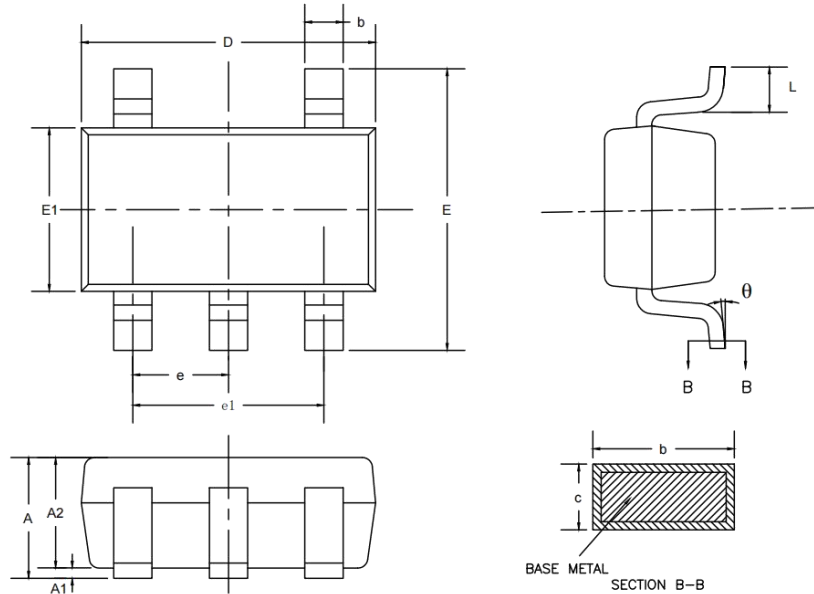
RESET Valid at Low Voltage

A resistor can be added from the $\overline{\text{RESET}}$ pin to ground to ensure the $\overline{\text{RESET}}$ output remains low with VCC down to 0V. A 100k Ω resistor connected from the $\overline{\text{RESET}}$ to ground is recommended. The size of the resistor should be large enough not to load the output excessively and small enough to pull-down any stray leakage currents.



Mechanical Dimensions

SOT23-5L Package Information



COMMON DIMENSIONS

UNITS OF MEASURE=MILLIMETER

SYMBOL	MIN	MAX
A	0.9	1.45
A1	0	0.15
A2	0.9	1.3
b	0.28	0.5
c	0.1	0.23
D	2.82	3.05
E	2.6	3
E1	1.5	1.75
e	0.95BSC	

e1	1.8	2
L	0.3	0.6
θ	0°	8°

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