



VMDSEMI

**VSDH065R20ANA**

**Datasheet**

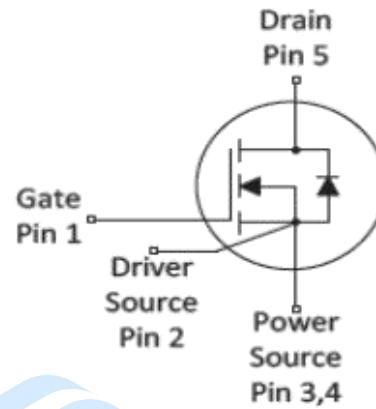


VMDSEMI

### General Description

### Symbol

$V_{(BR)DSS}$	$R_{DS(ON)_{max}}$	$I_D$
650V	200mΩ@10V	20A

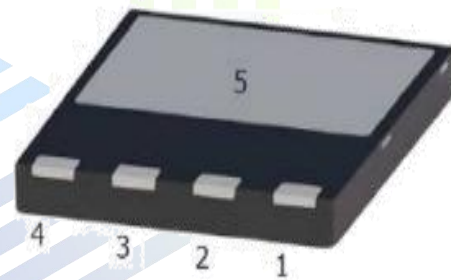


Symbol of VSDH065R20ANA

### Features

- Low  $R_{DS(on)}$  & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Ultra-fast and robust body diode

### Package Type



DFN8\*8

Package Type of VSDH065R20ANA

### Application

- PC power
- Telecom power
- Server power
- EV Charger
- Motor driver

### Ordering Information

Product Name	Package	Marking
VSDH065R20ANA	DFN8*8	VSDH065R20ANA

**Absolute Maximum Ratings** ( $T_A=25\text{ }^\circ\text{C}$ , unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	650	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Continuous Drain Current <sup>Note 1</sup>	$I_D$	20	A
Pulsed Drain Current <sup>Note 2</sup>	$I_{D, pulse}$	60	A
Continuous Diode Forward Current <sup>Note 1</sup>	$I_S$	20	A
Diode Pulsed Current <sup>Note 2</sup>	$I_{S, pulse}$	60	A
Max Power Dissipation <sup>Note 3</sup>	$P_D$	240	W
Avalanche Current, Single Pulse <sup>Note 4</sup>	$I_{AS}$	12	A
Avalanche Energy, Single Pulse <sup>Note 4</sup>	$E_{AS}$	778	mJ
MOSFET dv/dt ruggedness, $V_{DS}=0\sim 480\text{V}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\sim 480\text{V}$ , $I_{SD}\leq I_D$	dv/dt	50	V/ns
Operation and storage temperature	$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

**Thermal Resistance**

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	-	0.52	-	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient <sup>Note 5</sup>	$R_{\theta JA}$	-	62	-	

**Notes:**

Note1: Calculated continuous current based on maximum allowable junction temperature.

Note2: Pulse width limited by safe operating area.

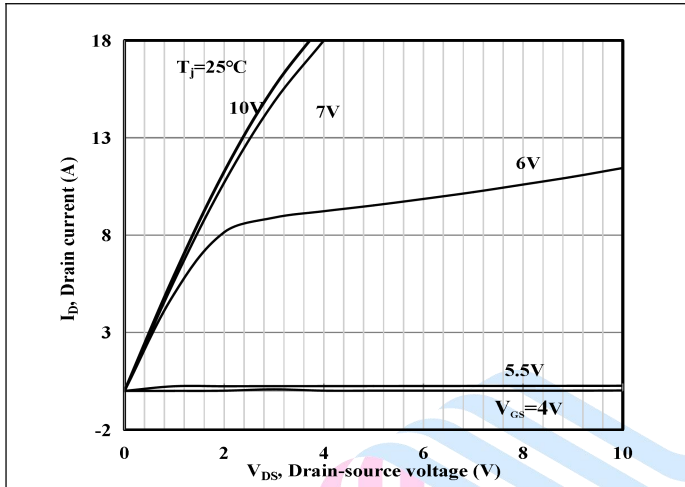
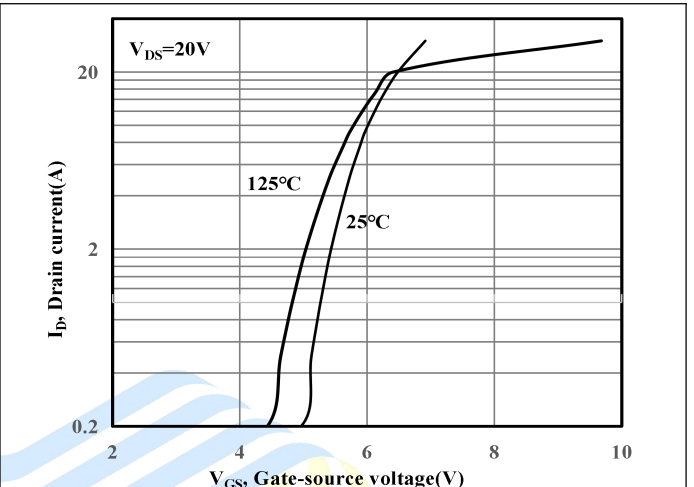
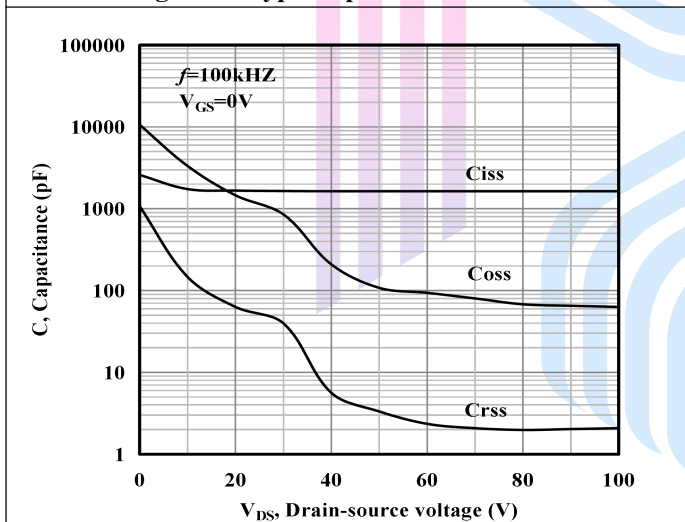
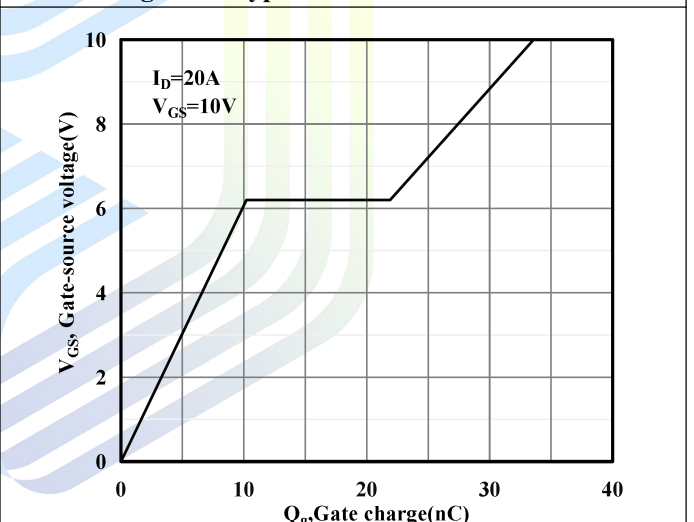
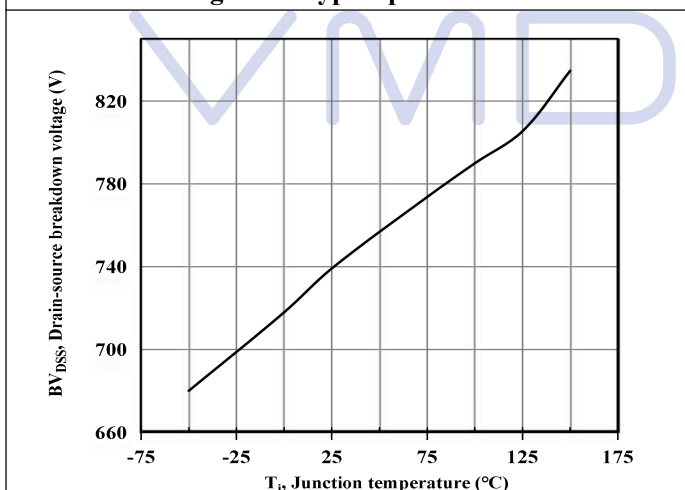
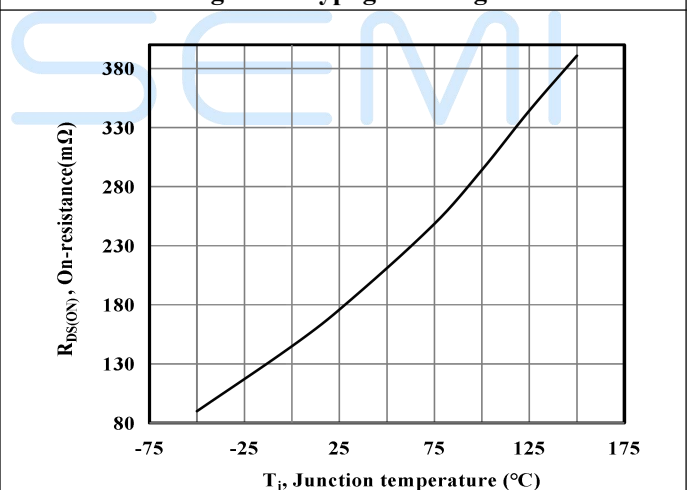
Note3: Based on max. junction temperature, using junction-case thermal resistance.

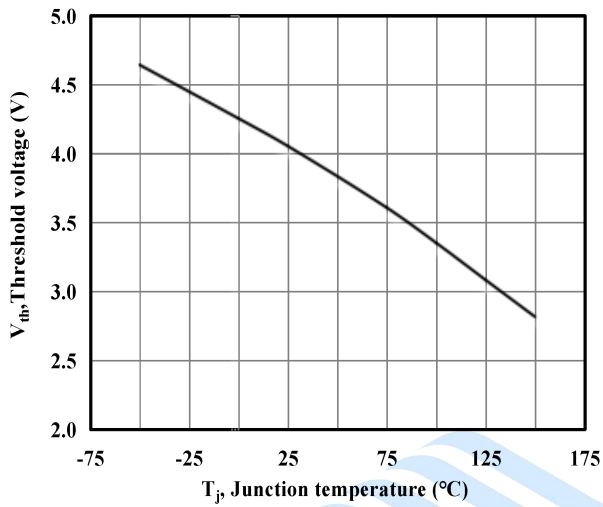
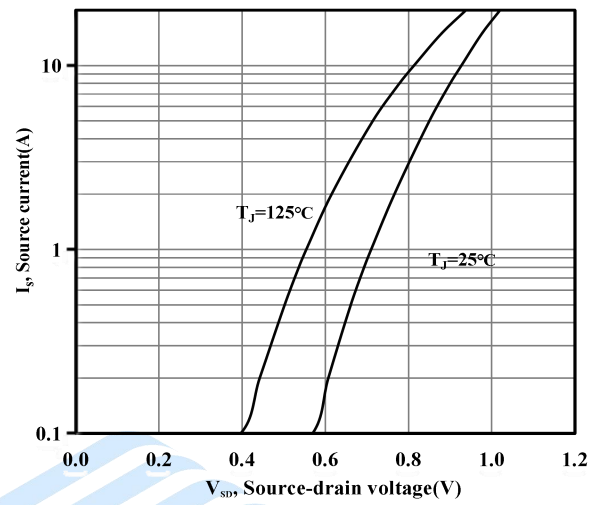
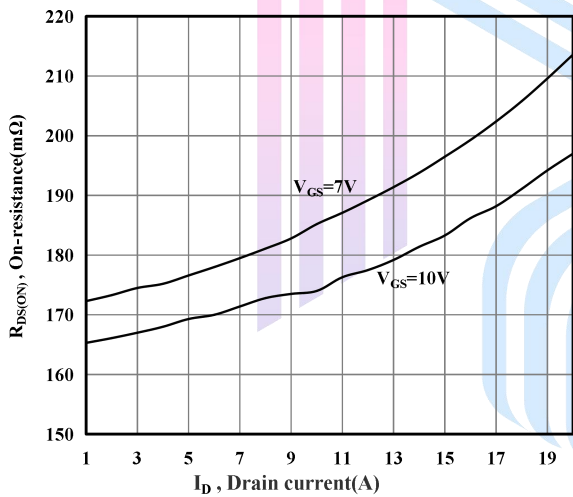
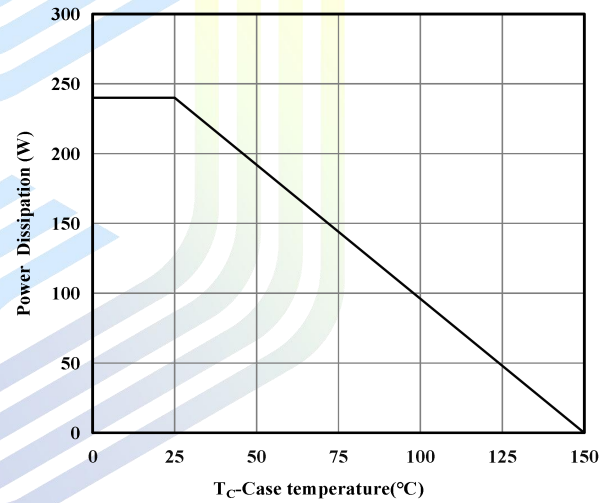
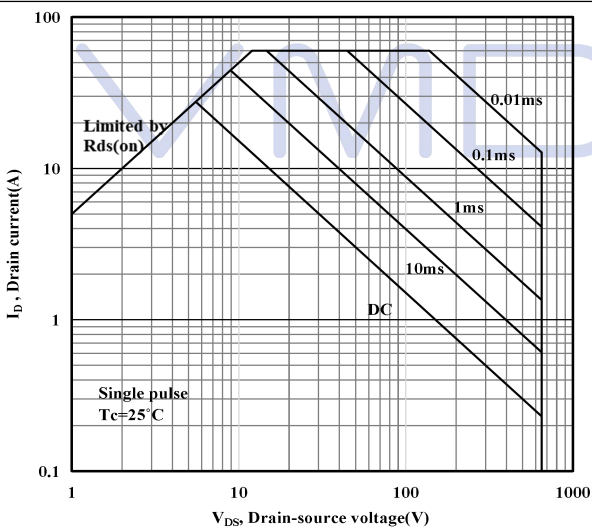
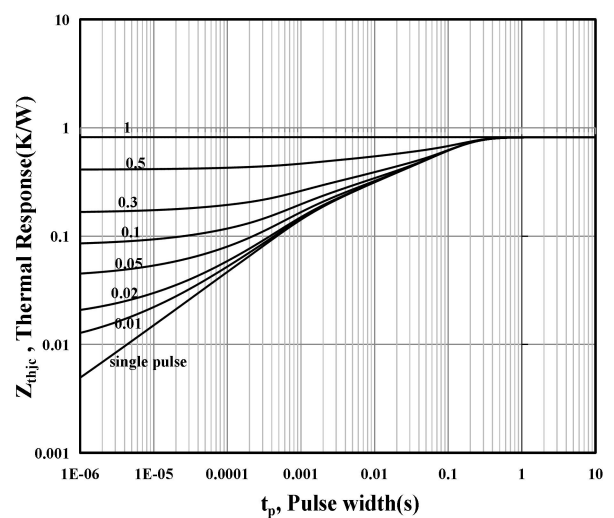
Note4:  $V_{DD}=150\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=10.8\text{mH}$ , starting  $T_A=25\text{ }^\circ\text{C}$ .

Note5: When mounted on 1 inch square copper board,  $t\leq 10\text{sec}$ . The value in any given application depends on the user's specific board design.

**Electrical Characteristics** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

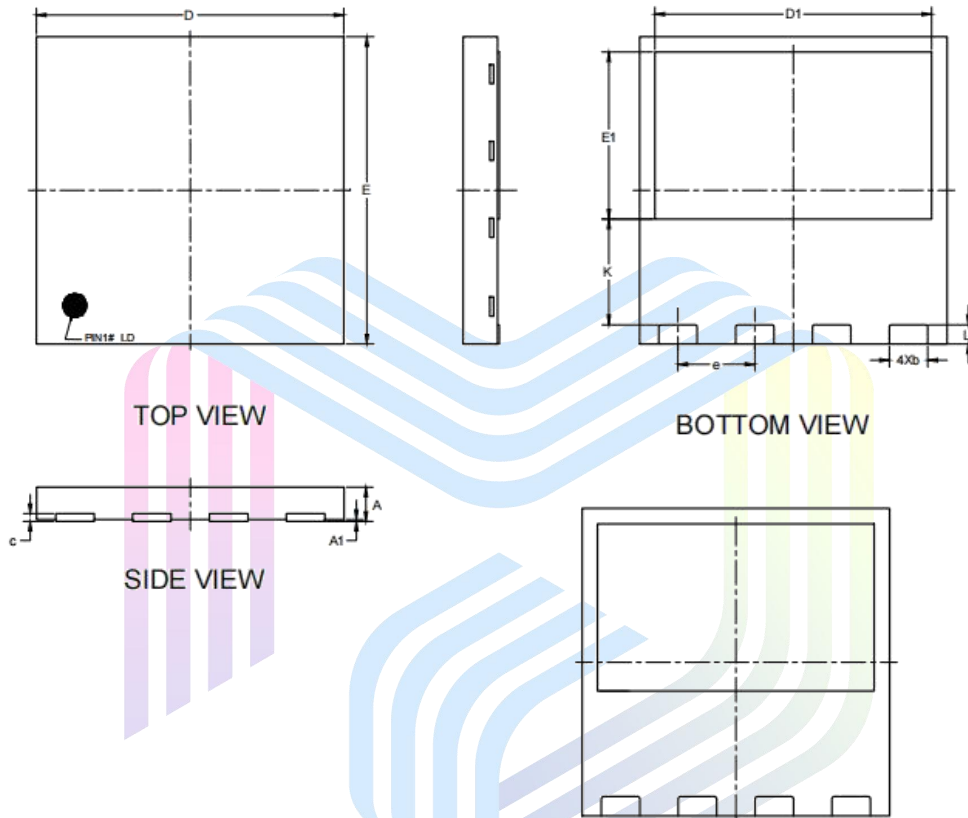
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Statistic Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	650	-	-	V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=650V, V_{GS}=0V$	-	-	5	$\mu A$
Gate-Source Leakage Current	Forward	$I_{GSSF}, V_{GS}=30V, V_{DS}=0V$	-	-	100	nA
	Reverse	$I_{GSSR}, V_{GS}=-30V, V_{DS}=0V$	-	-	-100	
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	3.0	4.0	5.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=10A$	-	172	200	mΩ
Gate Resistance	$R_G$	$F=1MHz, \text{Open Drain}$	-	4.56	-	Ω
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS}=50V$	-	1640	-	pF
Output Capacitance	$C_{oss}$	$V_{GS}=0V$	-	108	-	pF
Reverse Transfer Capacitance	$C_{rss}$	$f=100kHz$	-	3.33	-	pF
Turn-on Delay Time	$t_{d(on)}$	$V_{DS}=520V$	-	35.3	-	ns
Rise Time	$t_r$	$I_D=20A$	-	23.9	-	
Turn-off Delay Time	$t_{d(off)}$	$R_G=25\Omega$	-	86.9	-	
Fall Time	$t_f$	$V_{GS}=10V$	-	16	-	
<b>Gate Charge Characteristics</b>						
Gate to Source Charge	$Q_{gs}$	$V_{DS}=520V$ $I_D=10A$ $V_{GS}=0 \text{ to } 10V$	-	10.21	-	nC
Gate to Drain Charge	$Q_{gd}$		-	11.69	-	
Gate Charge Total	$Q_g$		-	33.58	-	
Gate Plateau Voltage	$V_{plateau}$		-	6.2	-	V
<b>Reverse Diode Characteristics</b>						
Drain-Source Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=1A$	-	0.7	1.4	V
Reverse Recovery Time	$t_{rr}$	$V_R=520V$	-	179	-	ns
Reverse Recovery Charge	$Q_{rr}$	$I_S=20A$	-	965	-	nC
Peak Reverse Recovery Current	$I_{rrm}$	$di/dt=100A/\mu s$	-	10.6	-	A

**Electrical Characteristics Diagrams**

**Figure 1. Typ. output characteristics**

**Figure 2. Typ. transfer characteristics**

**Figure 3. Typ. capacitances**

**Figure 4. Typ. gate charge**

**Figure 5. Drain-source breakdown voltage**

**Figure 6. Drain-source on-state resistance**


**Figure 7. Threshold voltage**

**Figure 8. Forward characteristic of body diode**

**Figure 9. Drain-source on-state resistance**

**Figure 10. Power dissipation**

**Figure 11. Safe operation area T<sub>c</sub>=25°C**

**Figure 12. Max. transient thermal impedance**

## Mechanical Dimensions

### DFN8\*8 Package Information



COMMON DIMENSIONS  
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	MAX
A	0.75	0.95
A1	0.00	0.05
b	0.90	1.10
c	0.203REF	
D	7.90	8.10
D1	7.10	7.30
e	2.0BSC	
E	7.90	8.10
E1	4.25	4.45
K	2.75BSC	
L	0.40	0.60

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