

# VGTF120N400NA

Datasheet



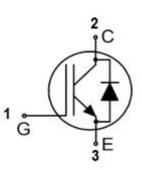
#### VGTF120N400NA

# **General Description**

V <sub>CE</sub>	1200	V
Ic	40	А
$V_{CEsat.}$ Typ T <sub>vj</sub> = 25 °C	2.2	V
T <sub>jmax</sub>	175	°C

### **Symbol**

**Package Type** 



Symbol of VGTF120N400NA

# Features

- Offers high breakdown voltage to 1200V for improved reliability
- Low V<sub>CEsat</sub>
- Easy parallel switching capability due to positive temperature coefficient in V<sub>CEsat</sub>
- Powerful monolithic body diode with low forward voltage designed for soft commutation only
- Very tight parameter distribution
- Qualified according to JEDEC fortarget applications RoHS product
- Halogen and antimony free. "Green" Device

# Application

- Solar converters
- Uninterruptible power supplies
- Welding converters
- Mid to high range switching frequency convertesrs

# **Product Validation**

Qualified for industrial applications according to the relevant tests of JESD-022

# **Ordering Information**

Product Name	Package
VGTF120N400NA	TO-247

с Б ТО-247

Package Type of VGTF120N400NA



#### VGTF120N400NA

# Absolute Maximum Ratings (T<sub>A</sub>= 25 °C, unless otherwise specified)

Parameter			Rating	Unit
Collector-emitter voltage			1200	V
DC collector exercit limited by T	Tc = 25  °C	т	80	•
DC collector current, limited by $T_{vj max}$	$Tc = 100 \ ^{\circ}C$	Ic	40	A
Pulsed collector current, tp limited by $T_{vj max}$		I <sub>C,pulse</sub>	120	Α
Turn off safe operating area $V_{CE} \le 1200V$ , $Tvj \le 175^{\circ}C$		-	120	Α
Diede formund aurment limited by T	Tc = 25  °C	т	80	•
Diode forward current, limited by $T_{vjmax}$ $Tc = 100 \ ^{\circ}C$		$I_{\rm F}$	40	A
Diode pulsed current, tp limited by T <sub>vjmax</sub>		I <sub>F,pulse</sub>	120	Α
Gate-emitter voltage			±30	V
Short circuit withstand time $V_{GE} = 15V$ , $V_{CC} \le 4000V$ , Allowed number of short circuits <1000, Times between short circuits: $\ge 1.0s$ , $T_i \le 25^{\circ}C$			10	us
Damen dissinction	Tc = 25  °C	- Ptot	357	W
Power dissipation	$Tc = 100 \ ^{\circ}C$		125	
Operating junction temperature			-40 to 175	°C
Soldering temperature, wave soldering 1.6mm (0.063in.) from case for 10s			260	°C

# Thermal Resistance

Parameter	Symbol	Min	Тур	Max	Unit
IGBT Thermal Resistance, Junction to Case max.	R <sub>0JC</sub>	-	0.40	-	°C/
Diode Thermal Resistance, Junction to Case max.	R <sub>0JC</sub>	-	0.80	-	W
Thermal Resistance, Junction to Ambient max.	R <sub>0JA</sub>	-	40	-	vv



#### VGTF120N400NA

# **Electrical Characteristics** (T<sub>J</sub>= 25 °C, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Statistic Characteristics		L		• • •	1	
Collector-emitter breakdown voltage	V <sub>(BR)CES</sub>	V <sub>GE</sub> =0V, I <sub>C</sub> =1mA	1200	-	-	V
	V <sub>CE(sat)</sub>	V <sub>GE</sub> =15V, I <sub>C</sub> =40A, T <sub>vj</sub> =25°C	-	2.2	2.6	V
Collector-emitter saturation voltage		$V_{GE}=15V, I_{C}=40A, T_{vj}=150^{\circ}C$	-	2.8	3.1	V
	17	$V_{GE}=0V, I_{C}=40A, T_{vj}=25^{\circ}C$	-	3.0	3.8	V
Diode forward voltage	$V_{\mathrm{F}}$	$V_{GE}=0V, I_{C}=40A, T_{vj}=150^{\circ}C$	-	2.4	3.2	V
	V	V <sub>GE</sub> =V <sub>CE</sub> , I <sub>C</sub> =1mA,T <sub>vj</sub> =25°C	5.3	5.9	6.5	V
Gate-emitter threshold voltage	V <sub>GE(th)</sub>	$V_{GE}=V_{CE},I_C=1mA,T_{vj}=150^{\circ}C$	3.2	3.8	4.6	V
Zere veltage gete collector current	L	$V_{CE}=1200V, V_{GE}=0V, T_{vj}=25^{\circ}C$	-	-	0.6	mA
Zero voltage gate collector current	I <sub>CES</sub>	$V_{CE}=1200V, V_{GE}=0V, T_{vj}=150^{\circ}C$	-	-	10.0	mA
Gate-emitter leakage current	I <sub>GES</sub>	$V_{GE}=20V, V_{CE}=0V$	-	-	200	nA
Dynamic Characteristics						
Input Capacitance	Cies	$V_{CE}=30V$	-	6010	-	pF
Output Capacitance	Coes	V <sub>GE</sub> =0V	-	150	-	pF
Reverse Transfer Capacitance	Cres	f=1MHz	-	90	-	pF
Gate total charge	QG	V <sub>CE</sub> =600V	-	235	-	
Gate-Emitter charge	Qge	V <sub>GE</sub> =15V	-	50	-	nC
Gate-Collector charge	Q <sub>GC</sub>	$I_{C}=40A$	-	110	-	
Switching Characteristic, Inductive Loa	d IGBT C	haracteristic	1		•	
Turn-on delay time	t <sub>d(on)</sub>	T 2500	-	64	-	ns
Rise time	tr	$T_{Vj} = 25^{\circ}C$	-	70	-	ns
Turn-off delay time	t <sub>d(off)</sub>	$V_{CE}=600V$	-	250	-	ns
Fall time	t <sub>f</sub>	$V_{GE}=15V$ $I_C=40A$	-	50	-	ns
Turn-on energy	Eon	$R_{G}=10\Omega$	-	1.22	-	mj
Turn-off energy	Eoff	102	0	1.32	n 1-	mj
Turn-on delay time	t <sub>d(on)</sub>	T 1500C	-	60	-	ns
Rise time	t <sub>r</sub>	$T_{\rm Vj} = 150^{\circ}\rm C$	-	66	-	ns
Turn-off delay time	$t_{d(off)}$	$V_{CE}$ =600V $V_{GE}$ =15V	-	290		ns
Fall time	t <sub>f</sub>	$V_{GE}=15V$ $I_C=40A$	-	100	-	ns
Turn-on energy	Eon	$R_{G}=10\Omega$	-	1.30	-	mj
Turn-off energy	Eoff	1012	-	1.80	-	mj
Diode Characteristic						
Diode reverse recovery time	t <sub>rr</sub>	$T_{Vj} = 25^{\circ}C$	-	180	-	ns
Diode reverse recovery charge	Q <sub>rr</sub>	$V_R=600V$	-	860	-	μC
Diode peak reverse recovery current	I <sub>rrm</sub>	$I_F$ =40A, $d_{iF}/dt$ =200A/us	-	11.4	-	А



# 40A, 1200V, Insulated Gate Bipolar Transistor VGTF120N400NA

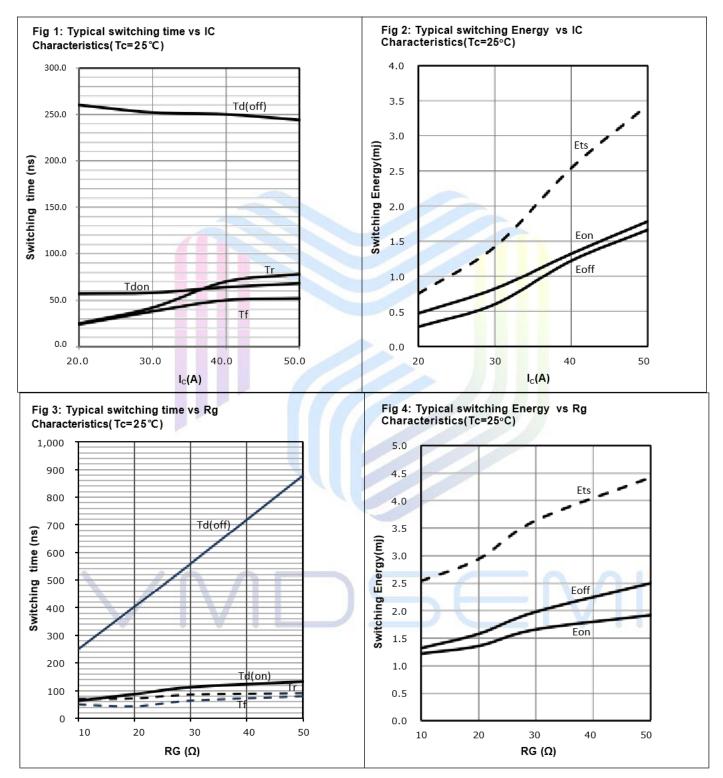
Diode peak rate of fall of reverse recovery current during tb recovery	dirr/dt		-	85.5	-	A/us
current during tb						
Diode reverse recovery time	t <sub>rr</sub>		-	280	-	ns
Diode reverse recovery charge	Qrr	$T_{\rm Vj} = 150^{\circ}{\rm C}$	-	2760	-	μC
Diode peak reverse recovery current	I <sub>rrm</sub>	$V_R=600V$	-	20.0	-	А
Diode peak rate of fall of reverse recovery current during tb recovery current during tb	dirr/dt	$I_F=40A$ , $d_{iF}/dt=200A/us$	-	123.8	-	A/us





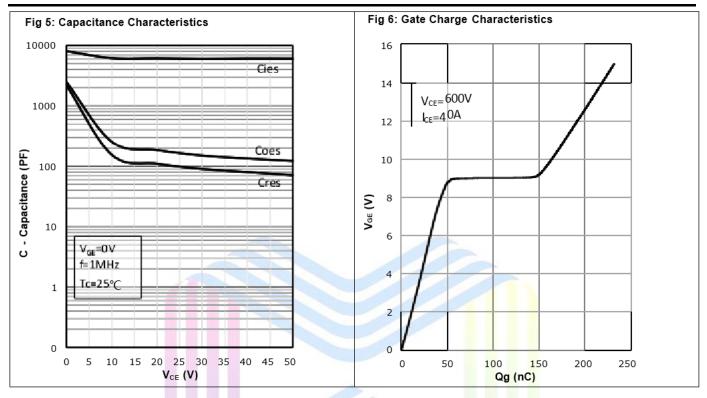
#### VGTF120N400NA

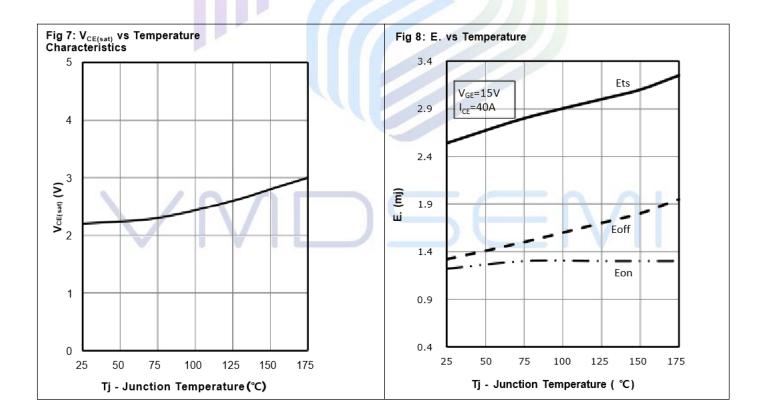
# **Typical Performance Characteristics**





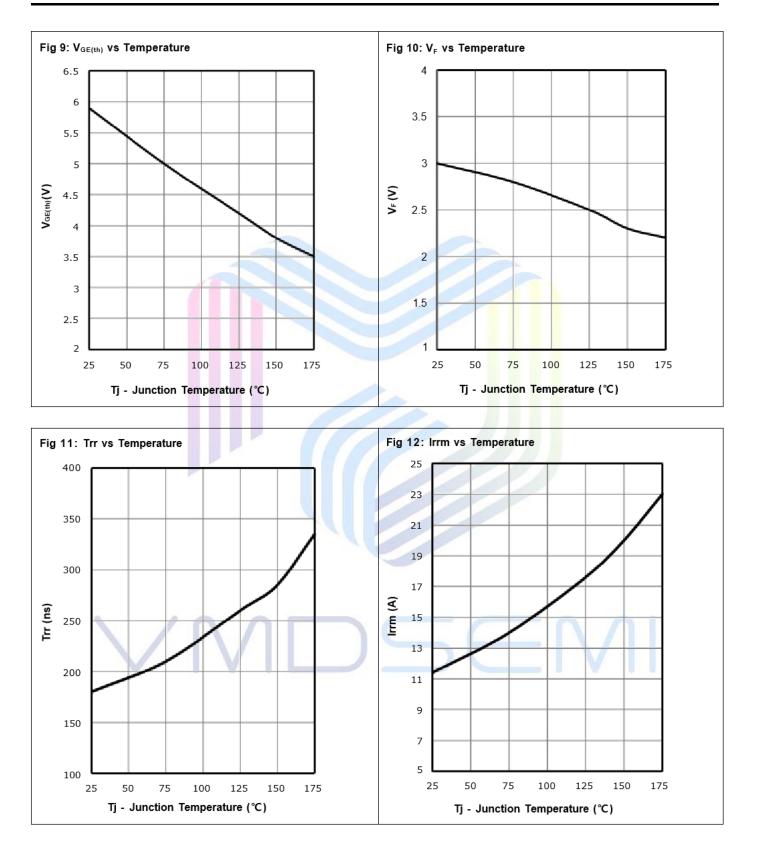
#### VGTF120N400NA







#### VGTF120N400NA

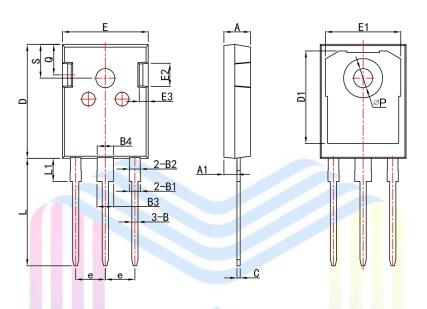




#### VGTF120N400NA

# **Mechanical Dimensions**

#### **TO-247 Package Information**



	TOP VIEW SIDE VIEW(Left)	BOTTOM VIEW		
SYMBOL	MIN	MAX		
A	4.60	5.20		
A1	2.20	2.60		
В	0.90	1.40		
B1	1.75	2.35		
B2	1.75	2.15		
B3	2.80	3.35		
B4	2.80	3.15		
С	0.50	0.70		
D	20.60	21.30		
D1	16.00	18.00		
E	15.50	16.10		
E1	13.00	14.70		
E2	3.80	5.30		
E3	0.80	2.60		
е	5.20	5.70		
L	19.00	20.50		
L1	3.90	4.60		
ФР	3.30	3.70		
Q	5.20	6.00		
S	5.80	6.60		
	All Dimensions in mm			



#### VGTF120N400NA

### NOTICE

Hangzhou VMD Semiconductor Co., Ltd (VMD) reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to VMD's terms and conditions supplied at the time of order acknowledgement.

VMD, its affiliates, agents, and employees, and all persons acting on its or their behalf, disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

VMD disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify VMD's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

VMD warrants performance of its hardware products to the specifications at the time of sale, testing, reliability and quality control are used to the extent VMD deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

VMD does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using VMD's components. To minimize risk, customers must provide adequate design and operating safeguards.

VMD does not warrant or convey any license to any intellectual property rights either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in VMD's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice.

VMD is not responsible or liable for such altered documentation. Resale of VMD's products with statements different from or beyond the parameters stated by VMD for that product or service voids all express or implied warrantees for the associated VMD product or service and is an unfair and deceptive business practice.

All Rights Reserved.



# Via-Media Semiconductor Limited Company

# http://www.vmdsemi.com

#### Main Sites:

#### - Headquarters

Hangzhou Via-Media Semiconductor Co., LTD. 1305-1306, Building 71, No. 90, Wensan Road, Xihu District, Hangzhou, Zhejiang Province, P.R. China Tel: +86-0571-8515 0563

#### - Shanghai

Shanghai R&D Center. 1506~1508, Xinyin Building, 888 Yishan Road, Shanghai, P.R of China Tel: +86- 021-54201999

#### - Xi'an

Xi'an R&D Center 1703B, Building A, Greenland Center, Jinye Road, High-Tech Zone, Xi'an, Shaanxi, P.R of China

#### Chengdu Office

Chengdu Winhi Semiconductor Co., LTD. Floor 15, Building 5, No. 171, Hele 2<sup>nd</sup> Street, Chengdu, Sichuan Province, P.R. China Tel: +86-028-8505 0771

#### - Shenzhen

Shenzhen Sales Center. 17B, No.1 Phoenix Building, 2008 Shennan Road, Shenzhen, P.R of China Tel: +86-0755- 82570682