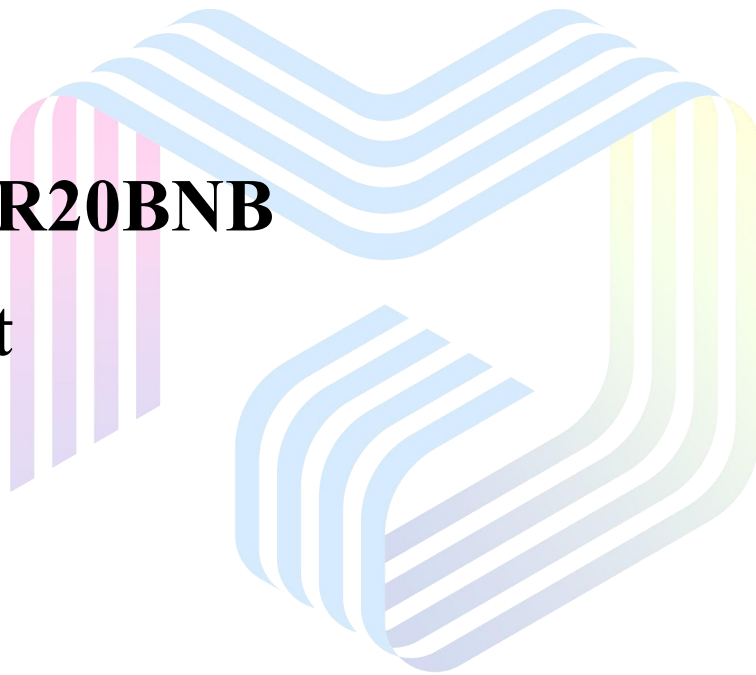




VMDSEMI

**VSTJ065R20BNB**

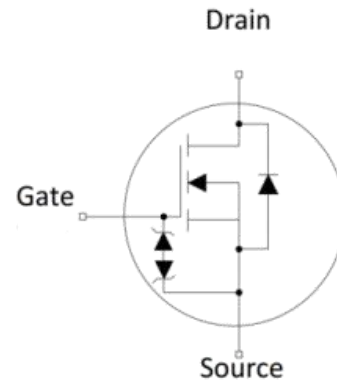
**Datasheet**



VMDSEMI

**General Description**
**Symbol**

$V_{(BR)DSS}$	$R_{DS(ON)_{max}}$	$I_D$
650V	2.0Ω@10V	3.5A



Symbol of VSTJ065R20BNB

**Features**

- Low RDS(on) & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Ultra-fast and robust body diode
- Integrated ESD protection diode

**Package Type**


TO-251

Package Type of VSTJ065R20BNB

**Application**

- PC power
- Telecom power
- Server power
- EV Charger
- Motor driver

**Ordering Information**

Product Name	Package	Marking
VSTJ065R20BNB	TO-251	STJ065R20BNB

**Absolute Maximum Ratings** ( $T_j=25\text{ }^\circ\text{C}$ , unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	650	V
Gate-Source Voltage	$V_{GS}$	$\pm 30$	V
Continuous Drain Current <sup>Note 1</sup>	$I_D$	3.5	A
Pulsed Drain Current <sup>Note 2</sup>	$I_{D, pulse}$	10.5	A
Continuous Diode Forward Current <sup>Note 1</sup>	$I_S$	3.5	A
Diode Pulsed Current <sup>Note 2</sup>	$I_{S, pulse}$	10.5	A
Max Power Dissipation <sup>Note 3</sup>	$P_D$	60	W
Avalanche Current, Single Pulse <sup>Note 4</sup>	$I_{AS}$	1.6	A
Avalanche Energy, Single Pulse <sup>Note 4</sup>	$E_{AS}$	77	mJ
Gate source ESD(HBM-C=100pF, R=1.5kΩ)	$V_{ESD(G-S)}$	Class 2	-
MOSFET dv/dt ruggedness, $V_{DS}=0\sim 480\text{V}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\sim 480\text{V}$ , $I_{SD}\leq I_D$	dv/dt	50	V/ns
Operation and storage temperature	$T_j, T_{STG}$	-55 to 150	$^\circ\text{C}$

**Thermal Resistance**

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	-	2.08	-	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient <sup>Note 5</sup>	$R_{\theta JA}$	-	62.5	-	

**Notes:**

Note1: Calculated continuous current based on maximum allowable junction temperature.

Note2: Pulse width limited by safe operating area.

Note3: Based on max. junction temperature, using junction-case thermal resistance.

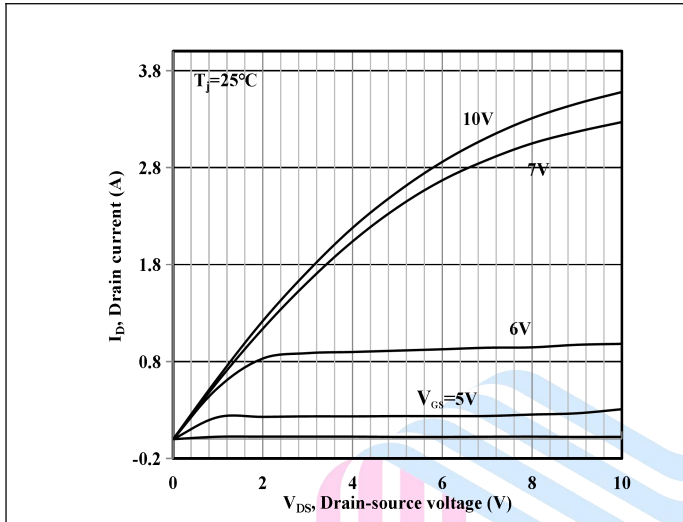
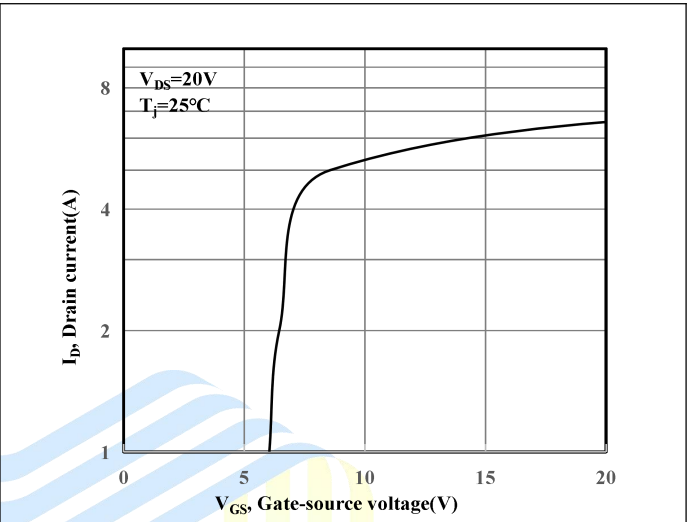
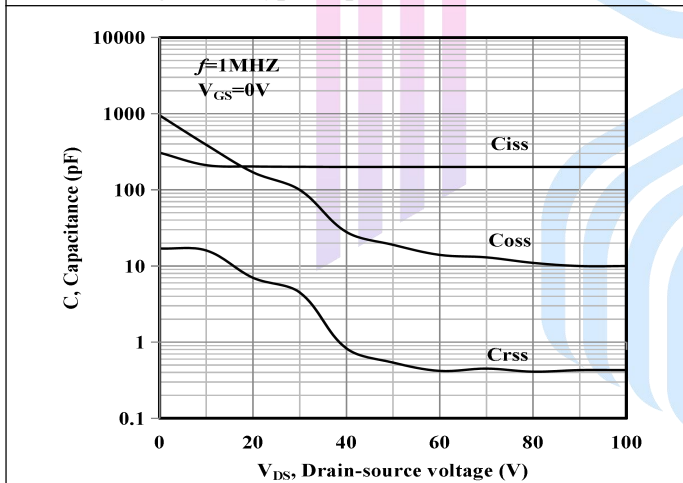
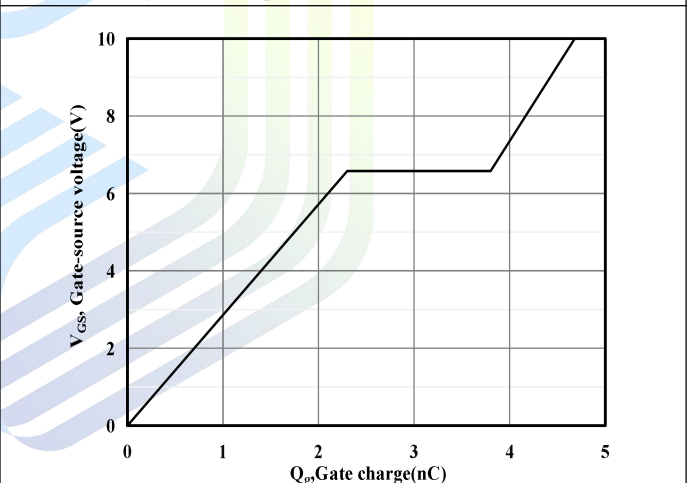
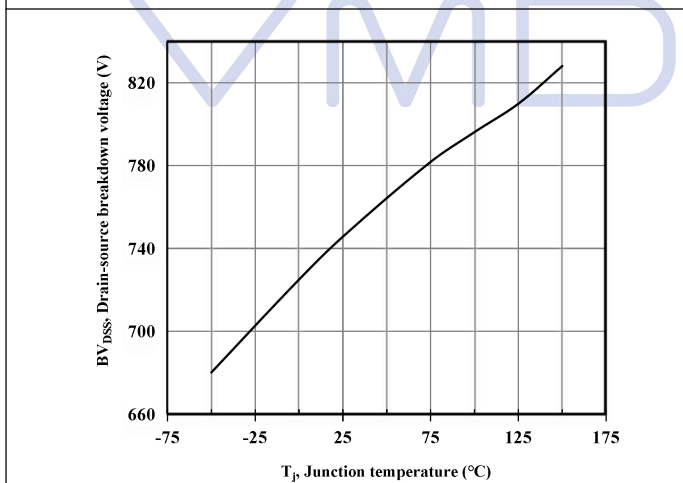
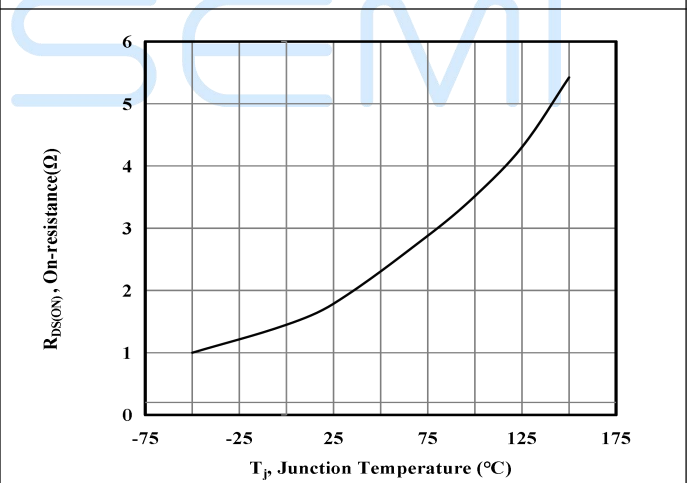
Note4:  $V_{DD}=100\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=60\text{mH}$ , starting  $T_A=25\text{ }^\circ\text{C}$ .

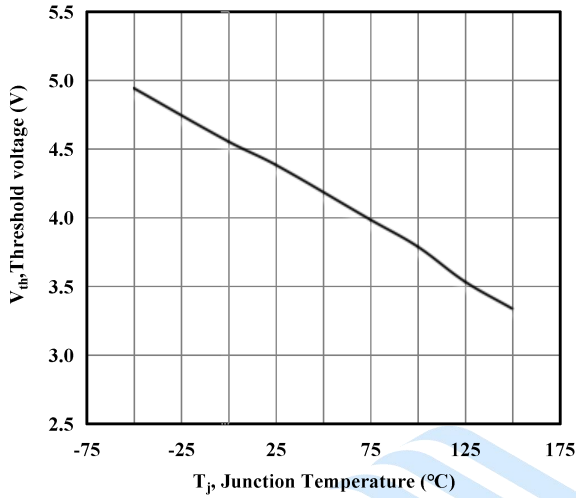
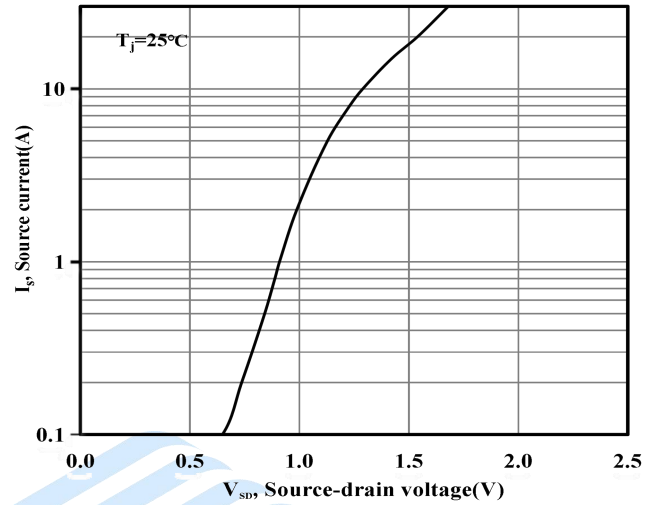
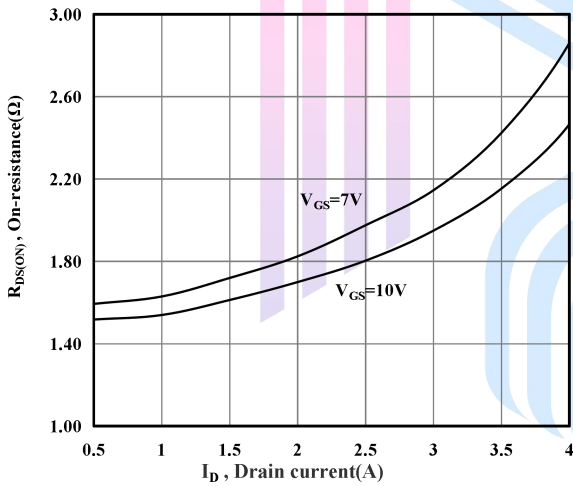
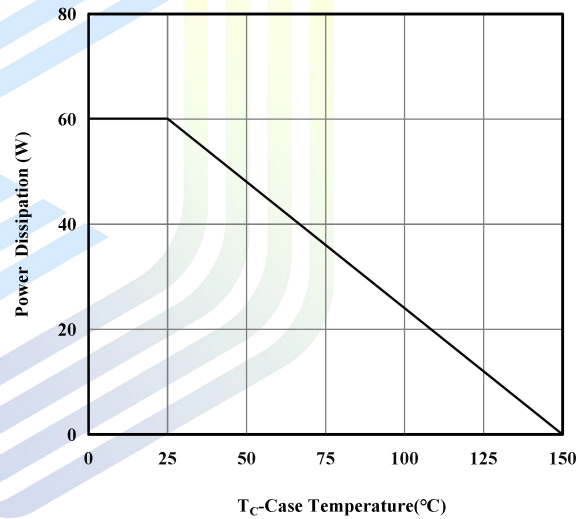
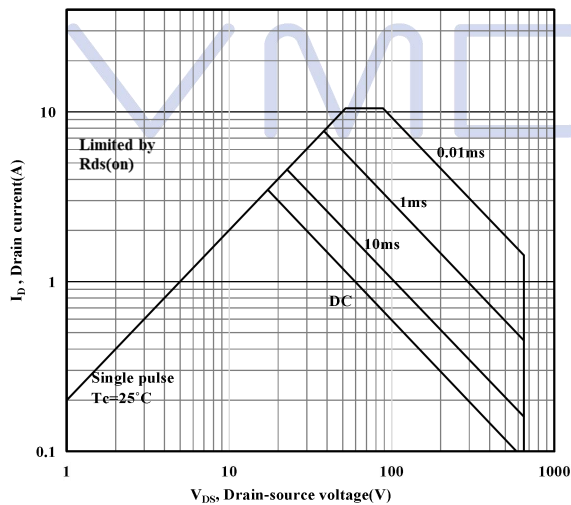
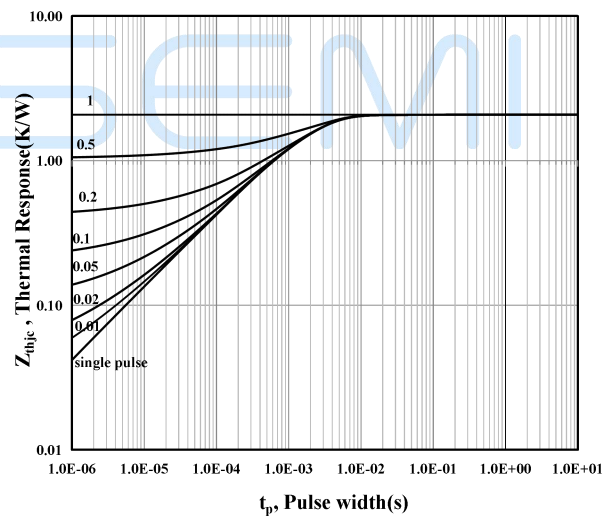
**Electrical Characteristics** ( $T_j = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

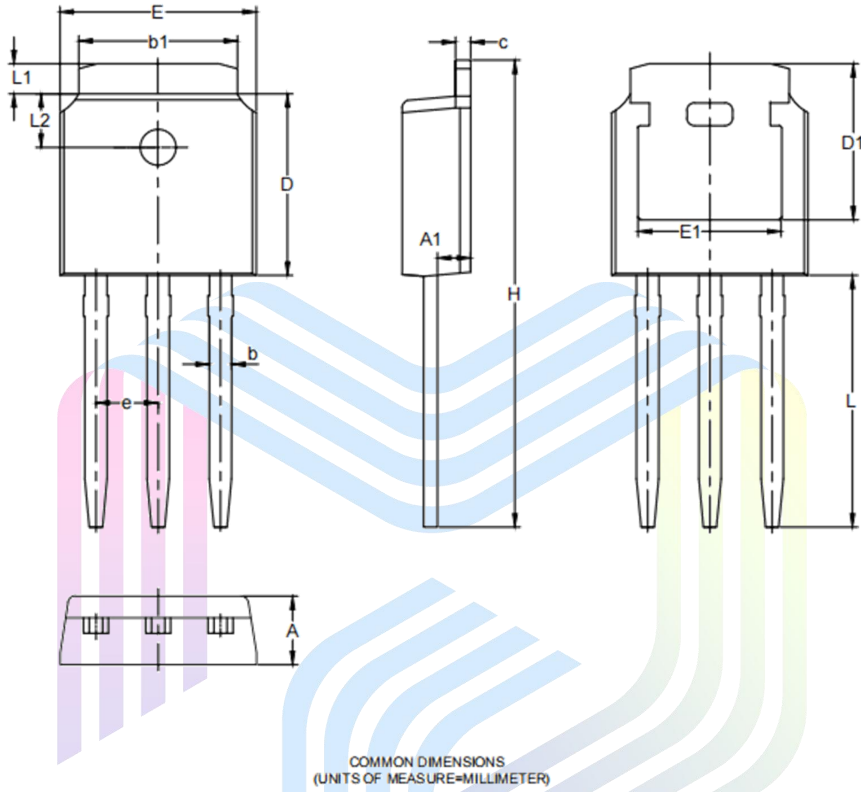
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Statistic Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	650	-	-	V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=650V, V_{GS}=0V$	-	-	5	$\mu A$
Gate-Source Leakage Current	Forward	$I_{GSSF}, V_{GS}=30V, V_{DS}=0V$	-	-	1	$\mu A$
	Reverse	$I_{GSSR}, V_{GS}=-30V, V_{DS}=0V$	-	-	-1	
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	3	4.3	5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=2.5A$	-	1.77	2.0	$\Omega$
Gate Resistance	$R_G$	$F=1MHz, \text{Open Drain}$	-	21	-	$\Omega$
<b>Dynamic Characteristics</b> <sup>Note5</sup>						
Input Capacitance	$C_{iss}$	$V_{DS}=50V$	-	200	-	pF
Output Capacitance	$C_{oss}$	$V_{GS}=0V$	-	19	-	pF
Reverse Transfer Capacitance	$C_{rss}$	$f=1MHz$	-	0.54	-	pF
Turn-on Delay Time	$t_{d(on)}$	$V_{DS}=380V$	-	7.58	-	ns
Rise Time	$t_r$	$I_D=5A$	-	17.64	-	
Turn-off Delay Time	$t_{d(off)}$	$R_G=25\Omega$	-	12.1	-	
Fall Time	$t_f$	$V_{GS}=10V$	-	12.83	-	
<b>Gate Charge Characteristics</b> <sup>Note5</sup>						
Gate to Source Charge	$Q_{gs}$	$V_{DS}=400V$ $I_D=5A$ $V_{GS}=0 \text{ to } 10V$	-	2.3	-	nC
Gate to Drain Charge	$Q_{gd}$		-	1.5	-	
Gate Charge Total	$Q_g$		-	4.68	-	
Gate Plateau Voltage	$V_{plateau}$		-	6.58	-	V
<b>Reverse Diode Characteristics</b> <sup>Note5</sup>						
Drain-Source Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=1A$	-	0.9	1.5	V
Reverse Recovery Time	$t_{rr}$	$V_R=400V$	-	56.75	-	ns
Reverse Recovery Charge	$Q_{rr}$	$I_S=5A$	-	178	-	nC
Peak Reverse Recovery Current	$I_{rrm}$	$di/dt=100A/\mu s$	-	4.8	-	A

Note5: Repetitive rating, pulse width limited by maximum junction temperature.

## Electrical Characteristics Diagrams


**Figure 1. Typ. output characteristics**

**Figure 2. Typ. transfer characteristics**

**Figure 3. Typ. capacitances**

**Figure 4. Typ. gate charge**

**Figure 5. Drain-source breakdown voltage**

**Figure 6. Drain-source on-state resistance**


**Figure 7. Threshold voltage**

**Figure 8. Forward characteristic of body diode**

**Figure 9. Drain-source on-state resistance**

**Figure 10. Power dissipation**

**Figure 11. Safe operation area T<sub>C</sub>=25°C**

**Figure 12. Max. transient thermal impedance**

**Mechanical Dimensions**
**TO-251 Package Information**


Symbol	MIN	MAX
A	2.2	2.4
A1	0.97	1.27
b	0.6	0.9
b1	5.1	5.5
c	0.43	0.61
D	5.95	6.25
D1	5.3REF	
E	6.4	6.75
E1	4.63	-
e	2.286BSC	
H	15.9	16.9
L	9	9.65
L1	0.88	1.28
L2	1.65	1.95

## NOTICE

Hangzhou VMD Semiconductor Co., Ltd (VMD) reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to VMD's terms and conditions supplied at the time of order acknowledgement.

VMD, its affiliates, agents, and employees, and all persons acting on its or their behalf, disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

VMD disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify VMD's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

VMD warrants performance of its hardware products to the specifications at the time of sale, testing, reliability and quality control are used to the extent VMD deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

VMD does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using VMD's components. To minimize risk, customers must provide adequate design and operating safeguards.

VMD does not warrant or convey any license to any intellectual property rights either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in VMD's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice.

VMD is not responsible or liable for such altered documentation. Resale of VMD's products with statements different from or beyond the parameters stated by VMD for that product or service voids all express or implied warranties for the associated VMD product or service and is an unfair and deceptive business practice.

All Rights Reserved.







## Via-Media Semiconductor Limited Company

<http://www.vmdsemi.com>

### Main Sites:

#### - Headquarters

Hangzhou Via-Media Semiconductor Co., LTD.  
1305-1306, Building 71, No. 90, Wensan Road, Xihu  
District, Hangzhou, Zhejiang Province, P.R. China  
Tel: +86-0571-8515 0563

#### - Chengdu Office

Chengdu Winhi Semiconductor Co., LTD.  
Floor 15, Building 5, No. 171, Hele 2<sup>nd</sup> Street,  
Chengdu, Sichuan Province, P.R. China  
Tel: +86-028-8505 0771

#### - Shanghai

Shanghai R&D Center.  
1506~1508, Xinyin Building, 888 Yishan Road,  
Shanghai, P.R of China

Tel: +86-021-54201999

#### - Shenzhen

Shenzhen Sales office .  
Room 4A15, Block AB, Tianxiang Building,  
Chegongmiao, Futian District, Shenzhen, P.R of  
China

Tel: +86-0755-82570682

#### - Xi'an

Xi'an R&D Center  
1703B, Building A, Greenland Center, Jinye Road,  
High-Tech Zone, Xi'an, Shaanxi, P.R of China