



VMDSEMI

VSTA065R38ANA

Datasheet

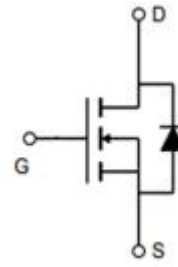


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General Description

Symbol

| | | |
|---------------|--------------------|-------|
| $V_{(BR)DSS}$ | $R_{DS(ON)_{max}}$ | I_D |
| 650V | 380mΩ@10V | 11A |

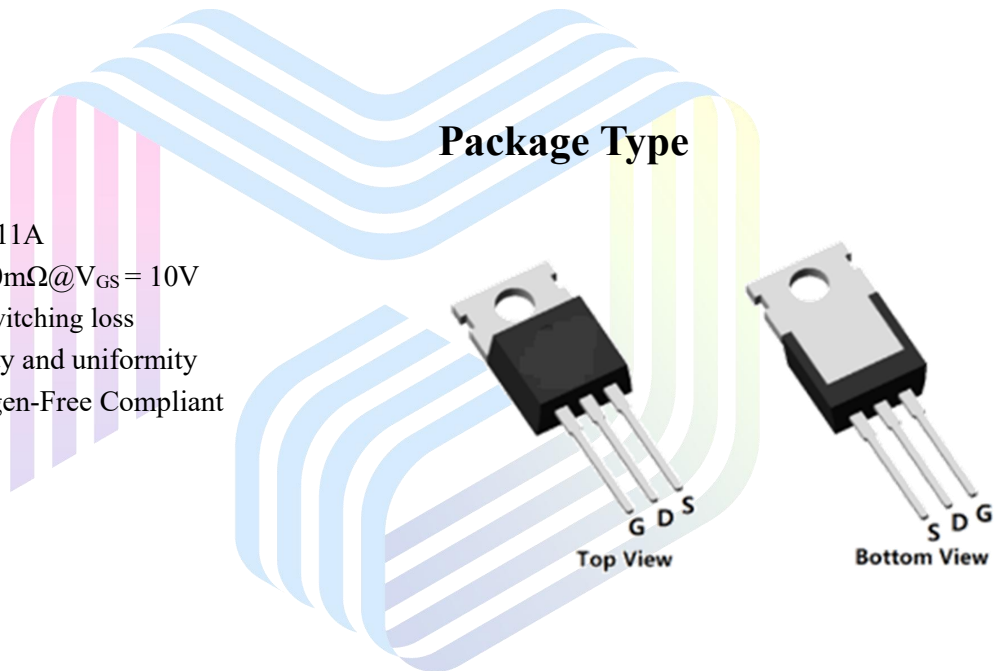


Symbol of VSTA065R38ANA

Features

- $V_{DS} = 650V, I_D = 11A$
- $R_{DS(ON)_{max}} = 380mΩ @ V_{GS} = 10V$
- Extremely low switching loss
- Excellent stability and uniformity
- RoHS and Halogen-Free Compliant

Package Type



Package Type of VSTA065R38ANA

Application

- PC power
- LED lighting
- Telecom power
- Server power
- Solar/UPS

Ordering Information

| Product Name | Package | Marking |
|---------------|---------|--------------|
| VSTA065R38ANA | TO-220 | STA065R38ANA |

Absolute Maximum Ratings

| Parameter | Symbol | Rating | Unit |
|---|-----------------------|------------|--------------------|
| Drain-Source Voltage | V_{DS} | 650 | V |
| Gate-Source Voltage | V_{GS} | ±30 | V |
| Continuous Drain Current ^{Note 1} , $T_C=25^{\circ}\text{C}$ | I_D | 11 | A |
| Pulsed Drain Current ^{Note 2} , $T_C=25^{\circ}\text{C}$ | $I_{D, \text{pulse}}$ | 33 | A |
| Continuous Diode Forward Current ^{Note 1} , $T_C=25^{\circ}\text{C}$ | I_S | 11 | A |
| Diode Pulsed Current ^{Note 2} , $T_C=25^{\circ}\text{C}$ | $I_{S, \text{pulse}}$ | 33 | A |
| Max Power Dissipation ^{Note 3} , $T_C=25^{\circ}\text{C}$ | P_D | 110 | W |
| Avalanche Current, Single Pulse ^{Note 4} | I_{AS} | 2.5 | A |
| Avalanche Energy, Single Pulse ^{Note 4} | E_{AS} | 250 | mJ |
| MOSFET dv/dt ruggedness, $V_{DS}=0\sim 480\text{V}$ | dv/dt | 50 | V/ns |
| Reverse diode dv/dt, $V_{DS}=0\sim 480\text{V}$, $I_{SD}\leq I_D$ | dv/dt | 15 | V/ns |
| Operation and storage temperature | T_J, T_{STG} | -55 to 150 | $^{\circ}\text{C}$ |

Thermal Resistance

| Parameter | Symbol | Min | Typ | Max | Unit |
|---|-----------------|-----|------|-----|-----------------------------|
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | | 1.14 | | $^{\circ}\text{C}/\text{W}$ |
| Thermal Resistance, Junction-to-Ambient ^{Note 5} | $R_{\theta JA}$ | | 62 | | |

Notes:

Note1: Calculated continuous current based on maximum allowable junction temperature.

Note2: Pulse width limited by safe operating area.

Note3: Based on max. junction temperature, using junction-case thermal resistance.

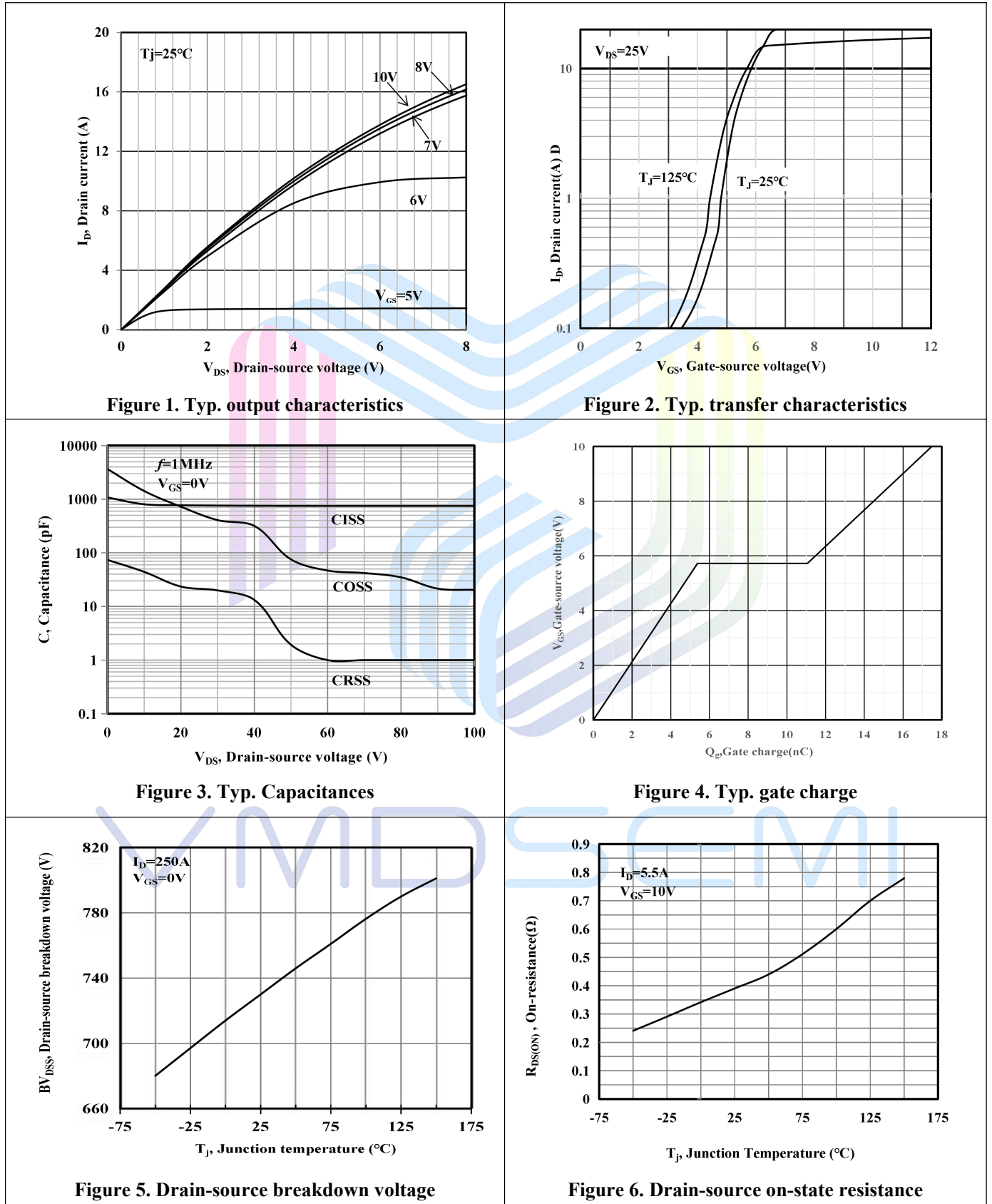
Note4: $V_{DD}=100\text{V}$, $V_{GS}=10\text{V}$, $L=80\text{mH}$, starting $T_A=25^{\circ}\text{C}$.

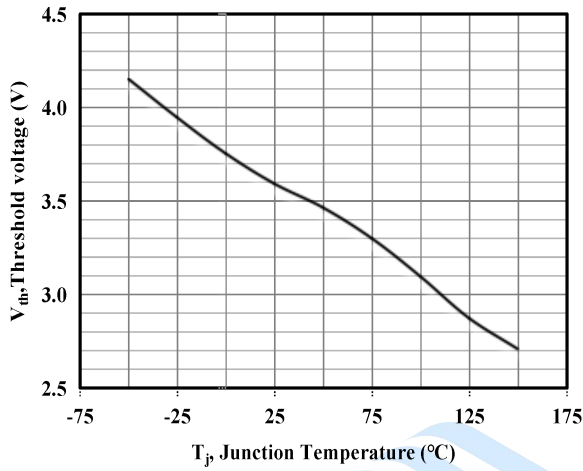
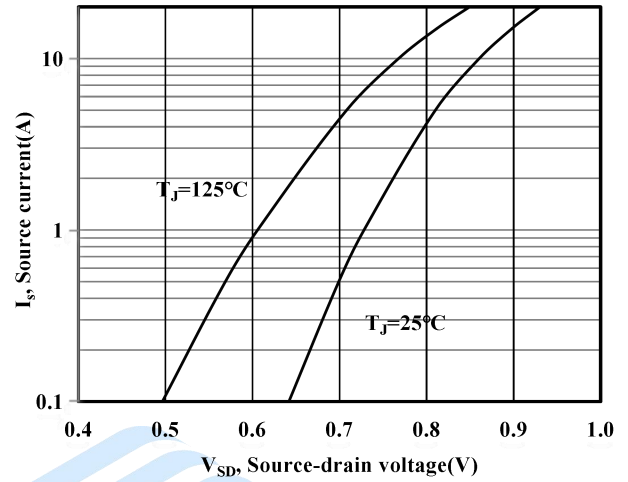
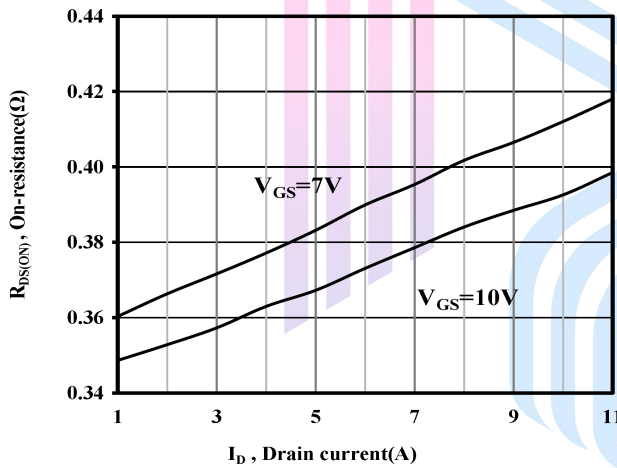
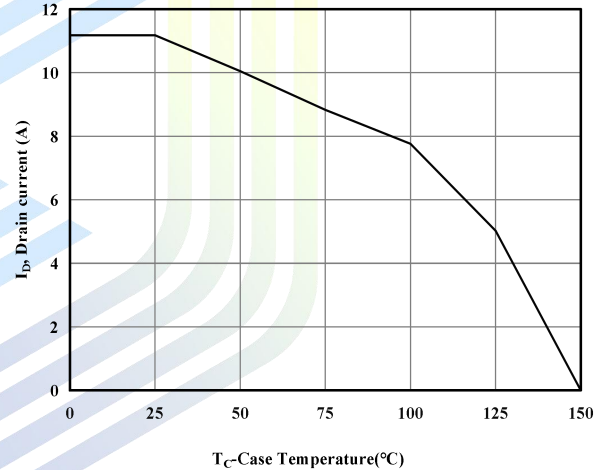
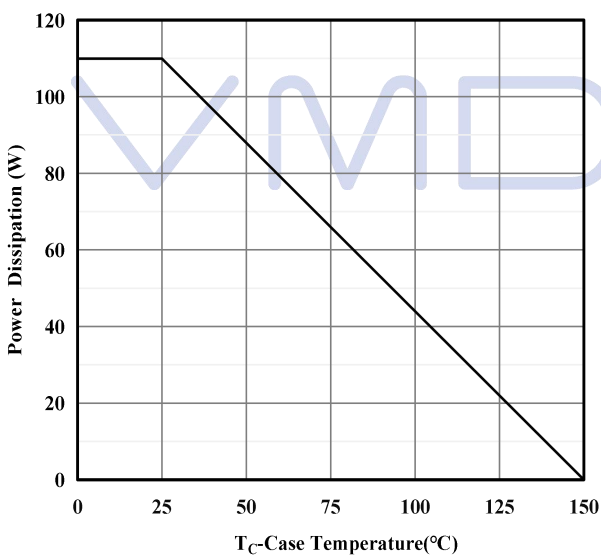
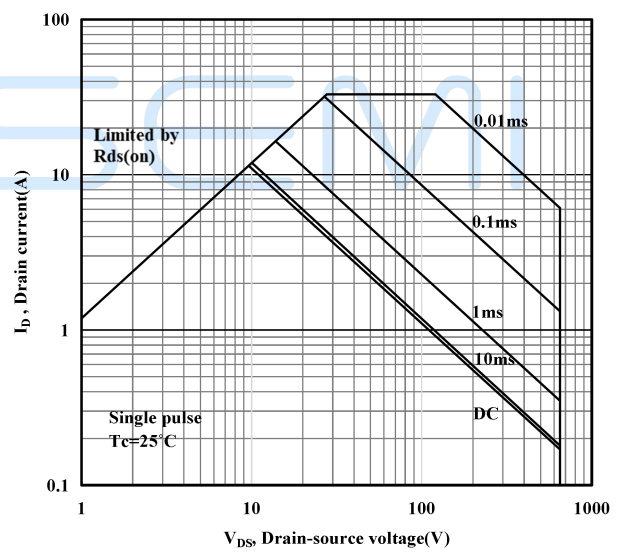
Note5: When mounted on 1 inch square copper board, $t\leq 10\text{sec}$. The value in any given application depends on the user's specific board design.

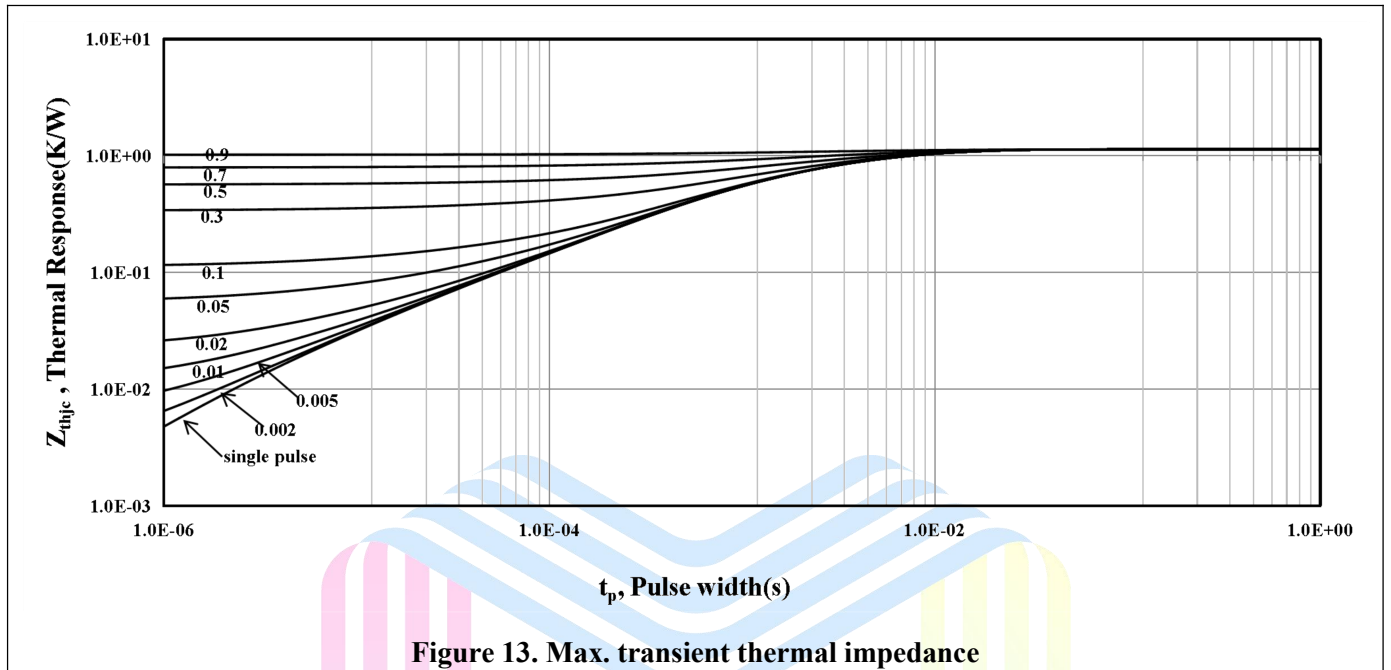
Electrical Characteristics($T_A=25\text{ }^\circ\text{C}$, unless otherwise specified)

| Parameter | Symbol | Test Conditions | Min | Typ | Max | Unit |
|--------------------------------------|---------------|---|-----|-------|------|-----------|
| Statistic Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $V_{GS}=0V, I_D=250\mu A$ | 650 | | | V |
| Drain-Source Leakage Current | I_{DSS} | $V_{DS}=650V, V_{GS}=0V$ | | | 1 | μA |
| Gate-Source Leakage Current | Forward | $I_{GSSF}, V_{GS}=30V, V_{DS}=0V$ | | | 100 | nA |
| | Reverse | $I_{GSSR}, V_{GS}=-30V, V_{DS}=0V$ | | | -100 | |
| Gate Threshold Voltage | $V_{GS(TH)}$ | $V_{DS}=V_{GS}, I_D=250\mu A$ | 2.9 | | 3.9 | V |
| Drain-Source On-State Resistance | $R_{DS(ON)}$ | $V_{GS}=10V, I_D=5.5A$ | | 350 | 380 | $m\Omega$ |
| Gate Resistance | R_G | $F=1MHz, \text{Open Drain}$ | | 5.1 | | Ω |
| Dynamic Characteristics | | | | | | |
| Input Capacitance | C_{iss} | $V_{DS}=50V, V_{GS}=0V, f=1MHz$ | | 752 | | pF |
| Output Capacitance | C_{oss} | | | 75 | | pF |
| Reverse Transfer Capacitance | C_{rss} | | | 1.9 | | pF |
| Turn-on Delay Time | $t_{d(on)}$ | $V_{DS}=400V, I_D=6A, R_G=2\Omega, V_{GS}=10V$ | | 10.9 | | ns |
| Rise Time | t_r | | | 5.6 | | |
| Turn-off Delay Time | $t_{d(off)}$ | | | 32.6 | | |
| Fall Time | t_f | | | 7.1 | | |
| Gate Charge Characteristics | | | | | | |
| Gate to Source Charge | Q_{gs} | $V_{DS}=400V, I_D=6A, V_{GS}=0 \text{ to } 10V$ | | 4.95 | | nC |
| Gate to Drain Charge | Q_{gd} | | | 6.20 | | |
| Gate Charge Total | Q_g | | | 17.52 | | |
| Gate Plateau Voltage | $V_{plateau}$ | | | 5.72 | | V |
| Reverse Diode Characteristics | | | | | | |
| Drain-Source Diode Forward Voltage | V_{SD} | $V_{GS}=0V, I_S=1A$ | | 0.74 | | V |
| Reverse Recovery Time | t_{rr} | $V_R=400V, I_S=6A, di/dt=100A/\mu s$ | | 200.5 | | ns |
| Reverse Recovery Charge | Q_{rr} | | | 1.86 | | μC |
| Peak Reverse Recovery Current | I_{rrm} | | | 16.3 | | A |

Electrical Characteristics Diagrams



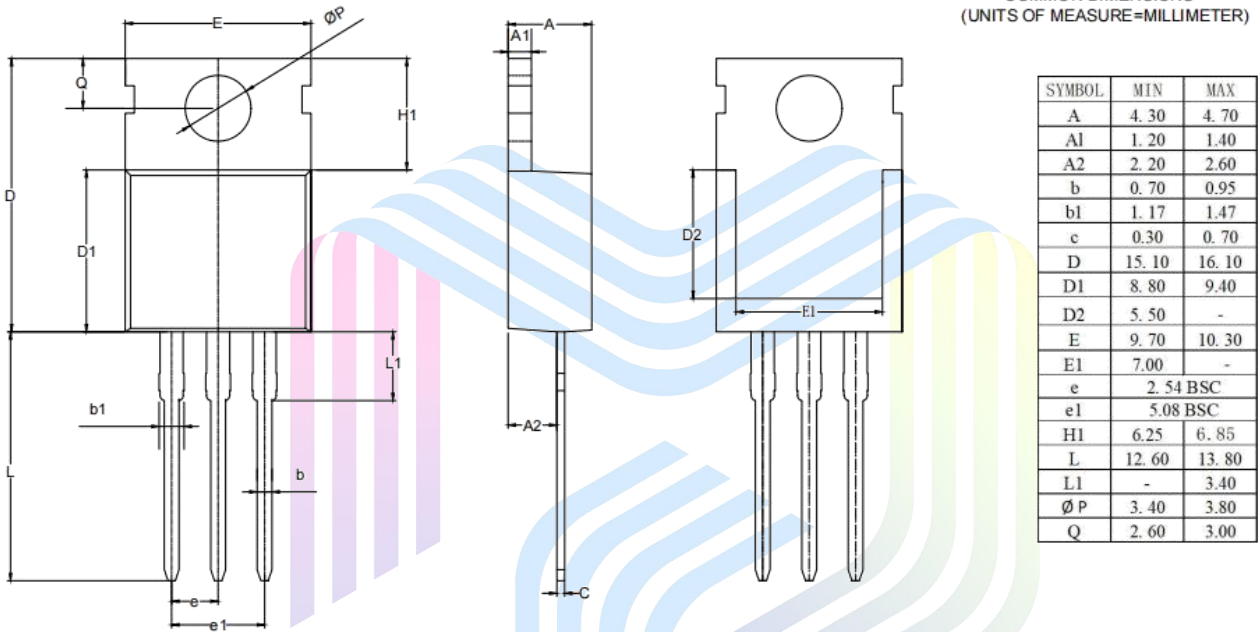

Figure 7. Threshold voltage

Figure 8. Forward characteristic of body diode

Figure 9. Drain-source on-state resistance

Figure 10. Drain current Derating

Figure 11. Power Dissipation

Figure 12. Safe operation area $T_c = 25^\circ\text{C}$




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Mechanical Dimensions

TO-220 Package Information



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