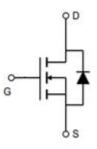


General Description

V1-207A2 N-Channel MOSFET is based on unique device design to achieve low RDS(ON), low gate charge, fast switching and excellent avalanche characteristics. This product is designed to minimize the die size in many handheld and mobile applications.

Symbol

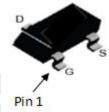


Symbol of V1-207A2

Features

- Low RDS(ON) & FOM
- $\blacksquare R_{DS(ON) max} = 23m\Omega@V_{GS} = 4.5V$
- Extremely low switching loss
- Excellent stability and uniformity
- RoHS and Halogen-Free Compliant

Package Type



Package Type of V1-207A2

Application

- Charging Circuit
- Battery Applications
- Synchronous Rectification
- High Frequency Switching

Ordering Information

Product Name	Package	Marking		
V1-207A2	SOT23-3	V1-207A2		

V1-207A2

Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{ m DSS}$	20	V
Gate-Source Voltage	$V_{ m GSS}$	±8	V
Continuous Drain Current Note 1, T _C =25°C	I_D	6	A
Pulsed Drain Current Note 2, T _C =25°C	I_{DM}	24	A
Max Power Dissipation, T _C =25°C	P_{D}	1.25	W
Avalanche Current, Single Pulse Note 5	I _{AS}	9.8	A
Avalanche Energy, Single Pulse Note3	E _{AS}	14.4	mJ
Continuous Diode Forward Current, T _C =25°C	I_{S}	1.7	A
Operation and storage temperature	T_{J} , T_{STG}	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$		-		°C/W
Thermal Resistance, Junction-to-Ambient Note4	$R_{ heta JA}$		100		C/W

Notes:

Note1: Calculated continuous current based on maximum allowable junction temperature.

Note2: Pulse width limited by safe operating area.

Note3: $V_{DS}=15V$, $V_{GS}=4.5V$, L=0.3mH, $Rg=25\Omega$, starting $T_{J}=25$ °C.

Note4: When mounted on 1 inch square copper board, t≤10sec. The value in any given application depends on the user's specific board design.



V1-207A2

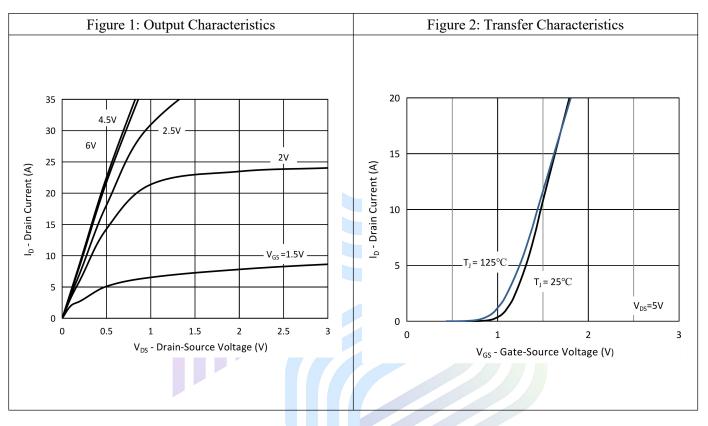
Electrical Characteristics (T_J= 25 °C, unless otherwise specified)

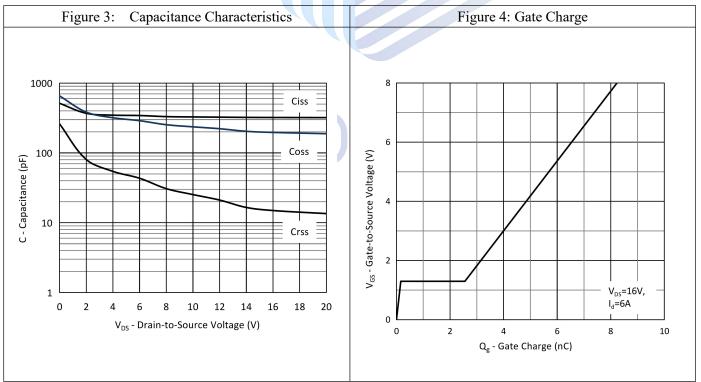
Parameter		Symbol	Test Conditions	Min	Тур	Max	Unit	
Statistic Characteristics				•				
Drain-Source Breakdown Voltage		$\mathrm{BV}_{\mathrm{DSS}}$	V _{GS} =0V, I _D =250uA 2				V	
Zero Gate Voltage Drain Curren	t	I_{DSS}	V _{DS} =20V, V _{GS} =0V			1	uA	
	Forward	I _{GSSF}	$V_{GS}=8V, V_{DS}=0V$			100	A	
Gate-Body Leakage Current	Reverse	I_{GSSR}	V_{GS} = -8V, V_{DS} =0V			-100	nA	
Gate Threshold Voltage		$V_{\text{GS(TH)}}$	$V_{DS}=V_{GS}, I_{D}=250uA$ 0.5		0.64	1.0	V	
Static Drain-Source On-Resistar			V _{GS} =2.5V, I _D =5A		21.7	30	30 23 mΩ	
Static Drain-Source On-Resistar	ice	$R_{\mathrm{DS(ON)}}$	V _{GS} =4.5V, I _D =6A		18.8	23		
Gate Resistance		R_G	F=1MHz, Open Drain		1.4		Ω	
Dynamic Characteristics								
Input Capacitance		Ciss	V -10V V -0V		327.7		pF	
Output Capacitance		C_{oss}	V_{DS} =10V, V_{GS} =0V, f =1MHz		236.4		pF	
Reverse Transfer Capacitance		C_{rss}	1-1МП2		25.3		pF	
Turn-on Delay Time		$t_{d(on)}$			3.1		ns	
Rise Time		$t_{\rm r}$	$V_{DS}=10V, I_{D}=6A,$		27.8			
Turn-off Delay Time		$t_{d(off)}$	$R_{G}=6.0\Omega, V_{GS}=4.5V$		23			
Fall Time		$t_{ m f}$			24.3			
Gate Charge Characteristics								
Gate to Source Charge		Q_{gs}			0.16			
Gate to Drain Charge		Q_{gd}	$V_{DS}=16V, I_{D}=6A,$		2.4		nC	
Gate Charge Total		Q_{g}	V _{GS} =4.5V		5.3			
Gate Plateau Voltage		$V_{Plateau}$			1.3		V	
Reverse Diode Characteristics								
Drain-Source Diode Forward Voltage		$ m V_{SD}$	$V_{GS}=0V, I_{SD}=1A$		0.78	1	V	
Reverse Recovery Time		t_{rr}	$V_R=10V, I_F=1A,$		19.5		ns	
Reverse Recovery Charge		Qrr	$dI_F/dt=100A/us$		9.7		пC	
Peak Reverse Recovery Current		I _{rrm}	uif/ut-100A/us		1		A	



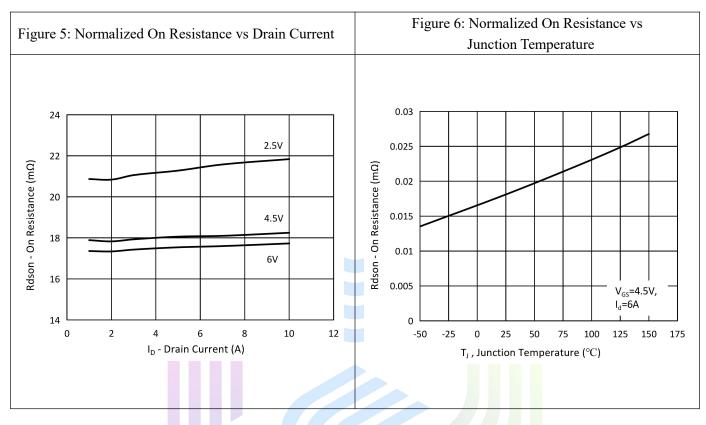
Typical Performance Characteristics

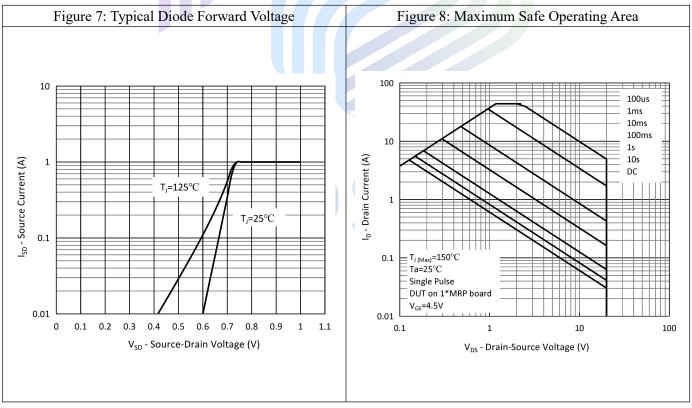
 $T_A = 25$ °C (unless otherwise stated)



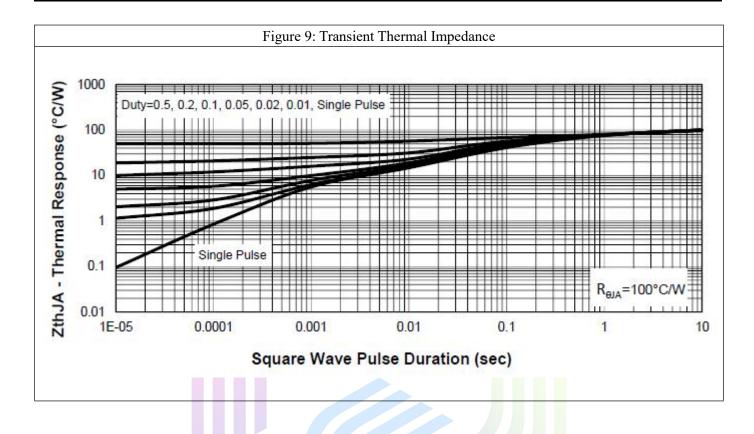






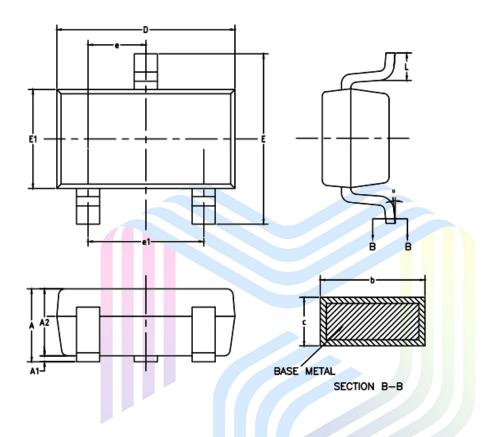








Mechanical Dimensions (SOT23-3 Unit:mm)



SYMBOL	MILLIN	METER	
OTWIDGE	MIN	MAX	
А	0.9	1.45	
A1	0	0.15	
A2	0.9	1.3	
b	0.28	0.5	
С	0.1	0.23	
D	2.82	3.05	
Е	2.6	3.0	
E1	1.5	1.75	
е	0.95	BSC	
e1	1.8	2	
L	0.3	0.6	
θ	0°	8°	



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