



VMDSEMI

VSTF065R700NB

Datasheet

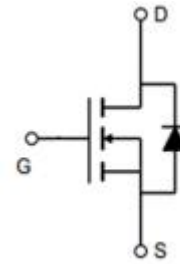


VMDSEMI

General Description

| | | |
|---------------|--------------------|-------|
| $V_{(BR)DSS}$ | $R_{DS(ON)_{max}}$ | I_D |
| 650V | 70mΩ@10V | 54A |

Symbol



Symbol of VSTF065R700NB

Features

- Extremely low switching loss
- Excellent stability and uniformity
- RoHS and Halogen-Free Compliant
- Ultra-fast and robust body diode

Application

- PC power
- LED lighting
- Telecom power
- Server power
- Solar/UPS

Package Type



TO-247

Package Type of VSTF065R700NB

Ordering Information

| Product Name | Package | Marking |
|---------------|---------|---------------|
| VSTF065R700NB | TO-247 | VSTF065R700NB |

Absolute Maximum Ratings($T_J=25\text{ }^\circ\text{C}$, unless otherwise specified)

| Parameter | Symbol | Rating | Unit |
|--|----------------|------------|------------------|
| Drain-Source Voltage | V_{DS} | 650 | V |
| Gate-Source Voltage | V_{GS} | ± 30 | V |
| Continuous Drain Current ^{Note 1} | I_D | 54 | A |
| Pulsed Drain Current ^{Note 2} | $I_{D, pulse}$ | 162 | A |
| Continuous Diode Forward Current ^{Note 1} | I_S | 54 | A |
| Diode Pulsed Current ^{Note 2} | $I_{S, pulse}$ | 162 | A |
| Max Power Dissipation ^{Note 3} | P_D | 500 | W |
| Avalanche Current, Single Pulse ^{Note 4} | I_{AS} | 19.4 | A |
| Avalanche Energy, Single Pulse ^{Note 4} | E_{AS} | 1882 | mJ |
| MOSFET dv/dt ruggedness, $V_{DS}=0\sim 480\text{V}$ | dv/dt | 50 | V/ns |
| Reverse diode dv/dt, $V_{DS}=0\sim 480\text{V}$, $I_{SD}\leq I_D$ | dv/dt | 15 | V/ns |
| Operation and storage temperature | T_J, T_{STG} | -55 to 150 | $^\circ\text{C}$ |

Thermal Resistance

| Parameter | Symbol | Min | Typ | Max | Unit |
|---|-----------------|-----|------|-----|--------------------|
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | - | 0.25 | - | $^\circ\text{C/W}$ |
| Thermal Resistance, Junction-to-Ambient ^{Note 5} | $R_{\theta JA}$ | - | 62.5 | - | |

Notes:

Note1: Calculated continuous current based on maximum allowable junction temperature.

Note2: Pulse width limited by safe operating area.

Note3: Based on max. junction temperature, using junction-case thermal resistance.

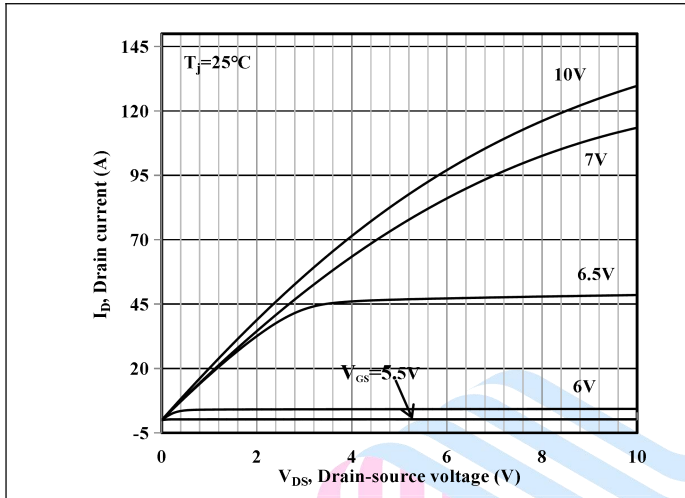
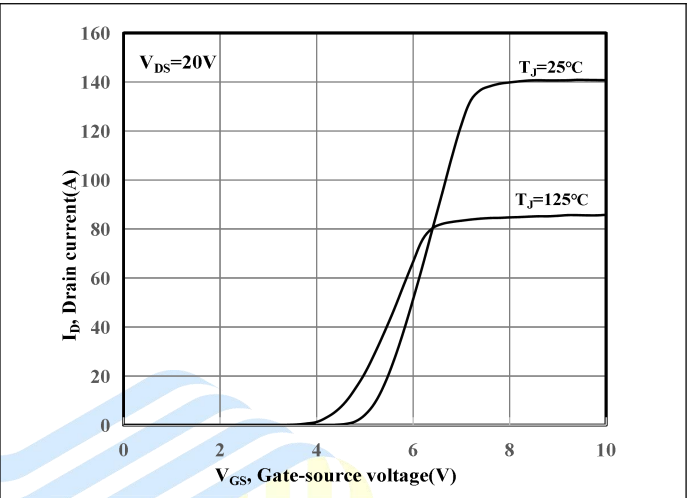
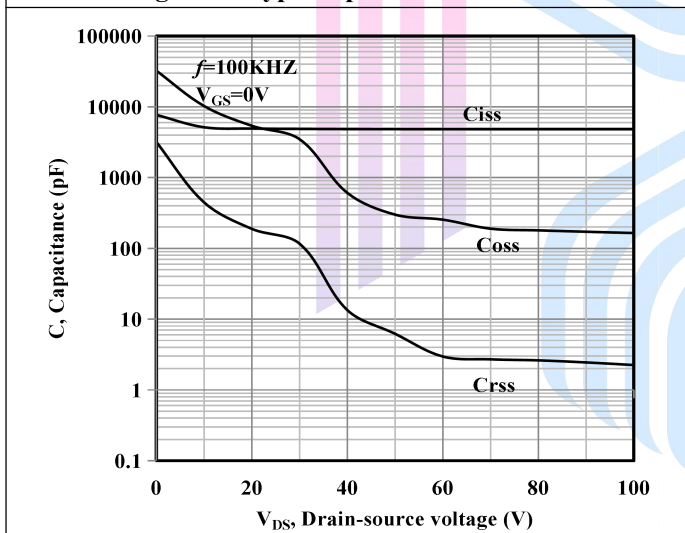
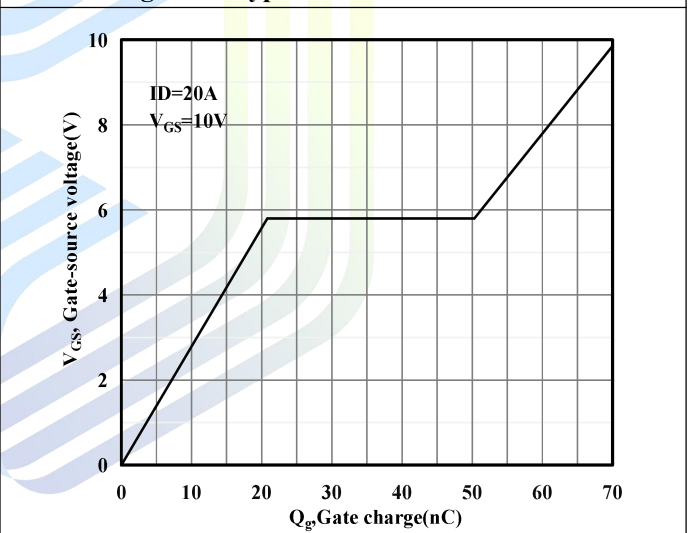
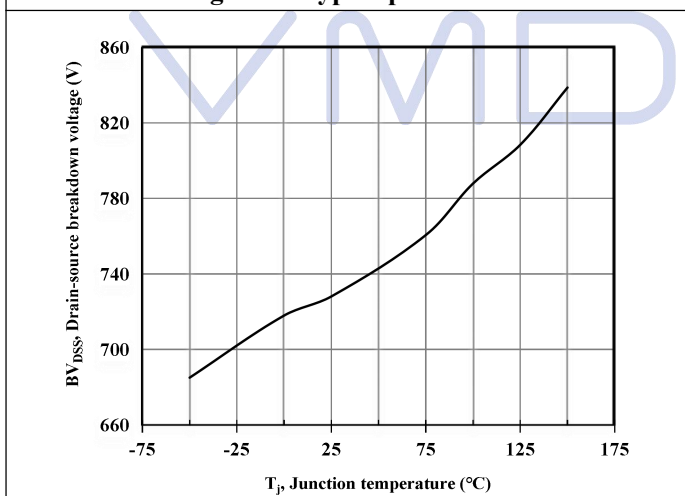
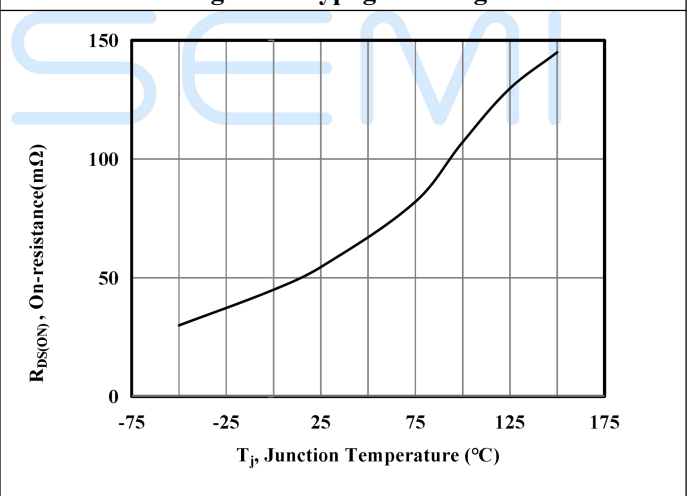
Note4: $V_{DD}=50\text{V}$, $V_{GS}=10\text{V}$, $L=10\text{mH}$, $R_G=25\Omega$, starting $T_A=25\text{ }^\circ\text{C}$.

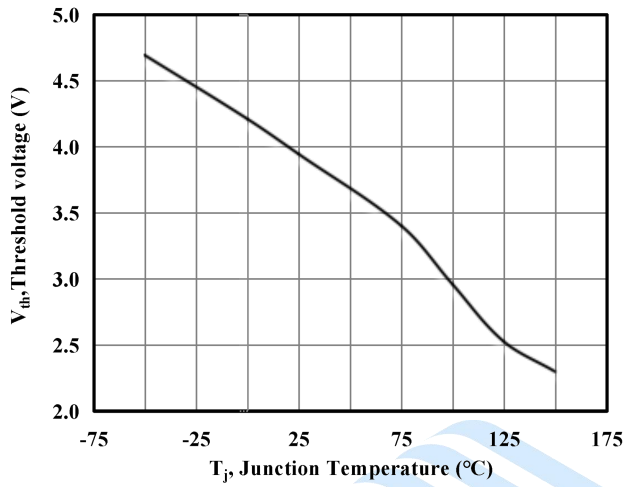
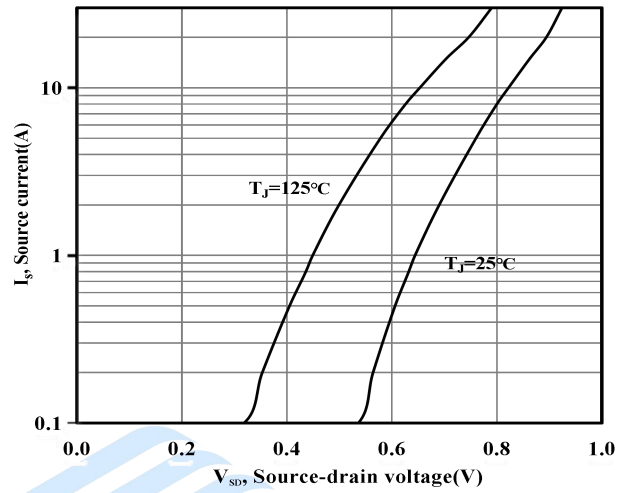
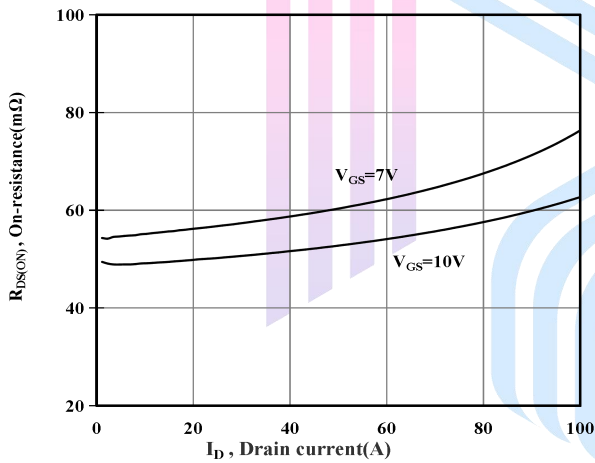
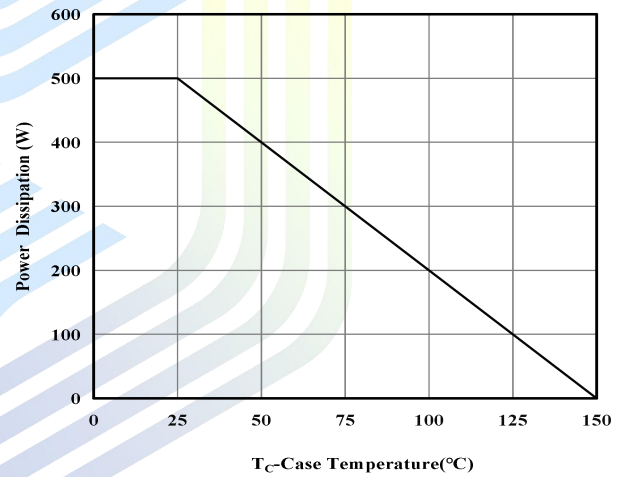
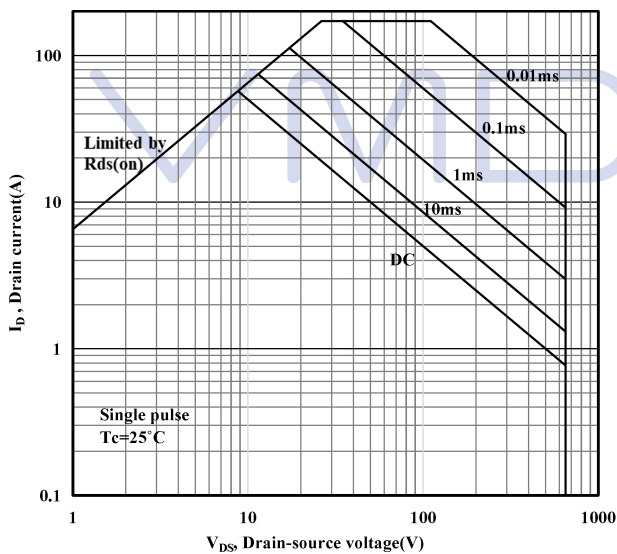
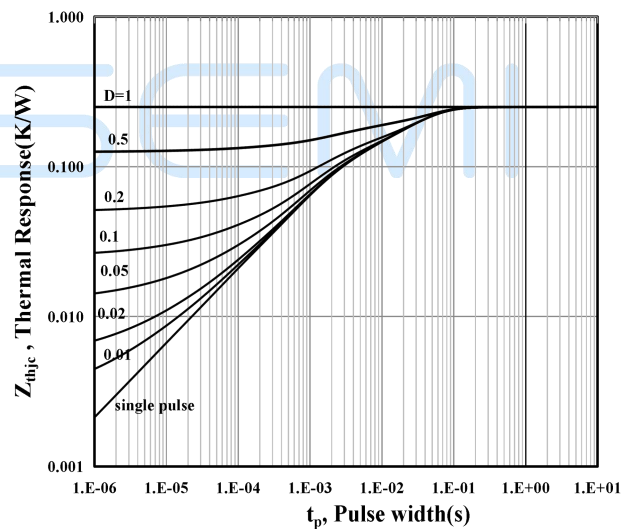
Note5: When mounted on 1 inch square copper board, $t\leq 10\text{sec}$. The value in any given application depends on the user's specific board design.

Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise specified)

| Parameter | Symbol | Test Conditions | Min | Typ | Max | Unit |
|--------------------------------------|---------------|--|-----|-------|------|-----------|
| Statistic Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $V_{GS}=0V, I_D=250\mu A$ | 650 | - | - | V |
| Drain-Source Leakage Current | I_{DSS} | $V_{DS}=650V, V_{GS}=0V$ | - | - | 10 | μA |
| Gate-Source Leakage Current | Forward | $I_{GSSF}, V_{GS}=30V, V_{DS}=0V$ | - | - | 100 | nA |
| | Reverse | $I_{GSSR}, V_{GS}=-30V, V_{DS}=0V$ | - | - | -100 | |
| Gate Threshold Voltage | $V_{GS(TH)}$ | $V_{DS}=V_{GS}, I_D=250\mu A$ | 3.0 | 4.0 | 5.0 | V |
| Drain-Source On-State Resistance | $R_{DS(ON)}$ | $V_{GS}=10V, I_D=27A$ | - | 53 | 70 | $m\Omega$ |
| Gate Resistance | R_G | $F=1MHz, \text{Open Drain}$ | - | 4.3 | - | Ω |
| Dynamic Characteristics | | | | | | |
| Input Capacitance | C_{iss} | $V_{DS}=50V$ | - | 4840 | - | pF |
| Output Capacitance | C_{oss} | $V_{GS}=0V$ | - | 300 | - | pF |
| Reverse Transfer Capacitance | C_{rss} | $f=100kHz$ | - | 6.22 | - | pF |
| Turn-on Delay Time | $t_{d(on)}$ | $V_{DS}=400V$ | - | 94 | - | ns |
| Rise Time | t_r | $I_D=30A$ | - | 29.4 | - | |
| Turn-off Delay Time | $t_{d(off)}$ | $R_G=25\Omega$ | - | 265.8 | - | |
| Fall Time | t_f | $V_{GS}=10V$ | - | 14.7 | - | |
| Gate Charge Characteristics | | | | | | |
| Gate to Source Charge | Q_{gs} | $V_{DS}=400V$ $I_D=30A$ $V_{GS}=0 \text{ to } 10V$ | - | 25.7 | - | nC |
| Gate to Drain Charge | Q_{gd} | | - | 39.1 | - | |
| Gate Charge Total | Q_g | | - | 88.8 | - | |
| Gate Plateau Voltage | $V_{plateau}$ | | - | 5.7 | - | V |
| Reverse Diode Characteristics | | | | | | |
| Drain-Source Diode Forward Voltage | V_{SD} | $V_{GS}=0V, I_S=1A$ | - | 0.64 | 1.4 | V |
| Reverse Recovery Time | t_{rr} | $V_R=400V$ | - | 155 | - | ns |
| Reverse Recovery Charge | Q_{rr} | $I_S=30A$ | - | 1081 | - | nC |
| Peak Reverse Recovery Current | I_{rrm} | $di/dt=100A/\mu s$ | - | 12.78 | - | A |

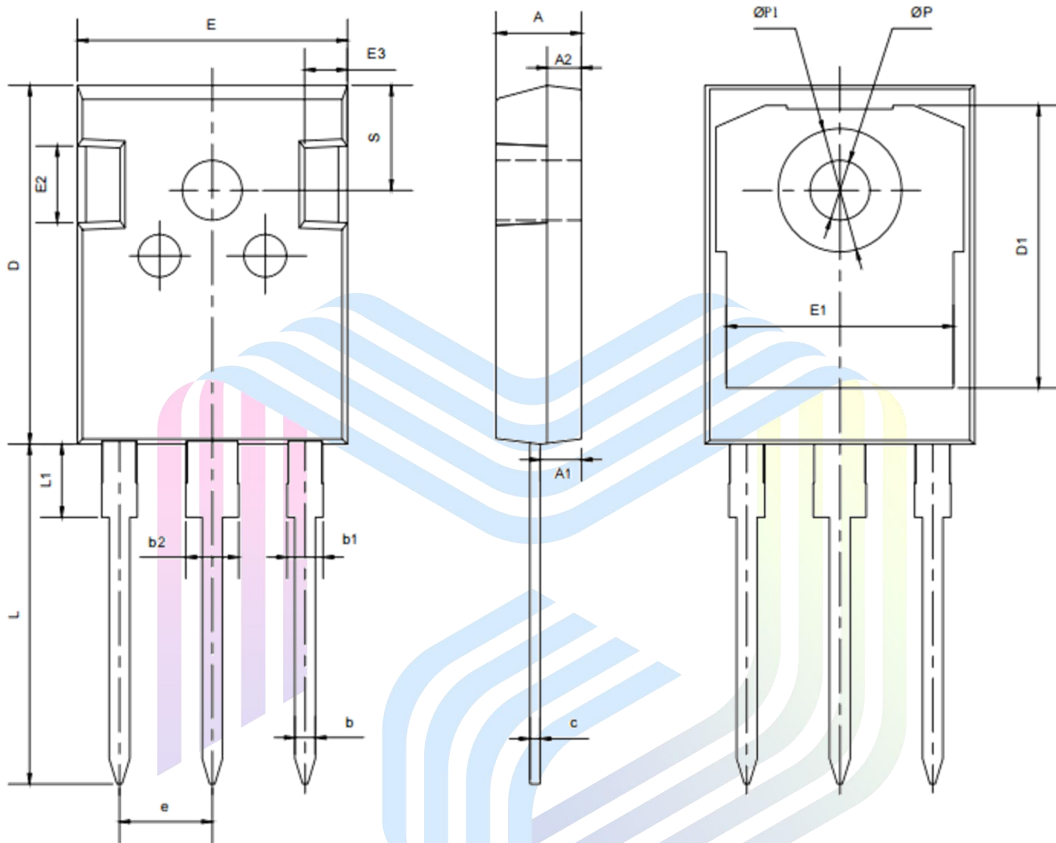
Electrical Characteristics Diagrams


Figure 1. Typ. output characteristics

Figure 2. Typ. transfer characteristics

Figure 3. Typ. capacitances

Figure 4. Typ. gate charge

Figure 5. Drain-source breakdown voltage

Figure 6. Drain-source on-state resistance


Figure 7. Threshold voltage

Figure 8. Forward characteristic of body diode

Figure 9. Drain-source on-state resistance

Figure 10. Power dissipation

Figure 11. Safe operation area $T_c=25^\circ\text{C}$

Figure 12. Max. transient thermal impedance

Mechanical Dimensions

TO-247 Package Information



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

| SYMBOL | MIN | MAX |
|--------|---------|-------|
| A | 4.80 | 5.20 |
| A1 | 2.21 | 2.61 |
| A2 | 1.85 | 2.15 |
| b | 1.11 | 1.36 |
| b1 | 1.91 | 2.21 |
| b2 | 2.91 | 3.21 |
| c | 0.51 | 0.75 |
| D | 20.70 | 21.30 |
| D1 | 16.25 | 16.85 |
| E | 15.50 | 16.10 |
| E1 | 13.00 | 13.60 |
| E2 | 4.80 | 5.60 |
| E3 | 2.10 | 2.70 |
| e | 5.44BSC | |
| L | 19.62 | 20.22 |
| L1 | - | 4.30 |
| φP | 3.40 | 3.80 |
| φP1 | - | 7.30 |
| S | 6.15BSC | |

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