

VSTF065R900NB

Datasheet



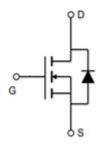


VSTF065R900NB

General Description

| V _{(BR)DSS} | R _{DS(ON)_max} | I_D | | |
|----------------------|-------------------------|-------|--|--|
| 650V | 90mΩ@10V | 50A | | |

Symbol



 $Symbol\ of\ VSTF065R900NB$

Features

- Extremely low switching loss
- Excellent stability and uniformity
- RoHS and Halogen-Free Compliant
- Ultra-fast and robust body diode

Application

- PC power
- LED lighting
- Telecom power
- Server power
- Solar/UPS

Package Type



TO-247

Package Type of VSTF065R900NB

Ordering Information

| Product Name | Package | Marking | | |
|---------------------|---------|---------------|--|--|
| VSTF065R900NB | TO-247 | VSTF065R900NB | | |



VSTF065R900NB

Absolute Maximum Ratings(T_J= 25 °C, unless otherwise specified)

| Parameter | Symbol | Rating | Unit | |
|---|-------------------|----------------------------------|------------|------|
| Drain-Source Voltage | | V_{DS} | 650 | V |
| Gate-Source Voltage | | V_{GS} | ±30 | V |
| Continuous Drain Current Note 1 | $T_C=25^{\circ}C$ | I_D | 50 | A |
| Pulsed Drain Current Note 2 | $T_C=25$ °C | I _{D, pulse} | 150 | A |
| Continuous Diode Forward Current Note 1 | $T_C=25^{\circ}C$ | I_S | 50 | A |
| Diode Pulsed Current Note 2 | $T_C=25$ °C | I _{S, pulse} | 150 | A |
| Max Power Dissipation Note 3 | $T_C=25^{\circ}C$ | P_{D} | 568 | W |
| Avalanche Current, Single Pulse Note 4 | | I _{AS} | 13.9 | A |
| Avalanche Energy, Single Pulse Note4 | | Eas | 1932 | mJ |
| MOSFET dv/dt ruggedness, V _{DS} =0~480V | | dv/dt | 50 | V/ns |
| Reverse diode dv/dt, V _{DS} =0~480V, I _{SD} <= I _D | | dv/dt | 15 | V/ns |
| Operation and storage temperature | | T _J ,T _{STG} | -55 to 150 | °C |

Thermal Resistance

| Parameter | Symbol | Min | Тур | Max | Unit |
|---|----------------|-----|------|-----|------|
| Thermal Resistance, Junction-to-Case | $R_{	heta JC}$ | - | 0.22 | - | °C/W |
| Thermal Resistance, Junction-to-Ambient Note5 | $R_{	heta JA}$ | - | 62.5 | - | C/W |

Notes:

Note1: Calculated continuous current based on maximum allowable junction temperature.

Note2: Pulse width limited by safe operating area.

Note3: Based on max. junction temperature, using junction-case thermal resistance.

Note4: V_{DD} =50V, V_{GS} =10V, L=20mH, R_G =25 Ω , starting T_A =25 °C.

Note5: When mounted on 1 inch square copper board, t≤10sec. The value in any given application depends on the user's specific board design.



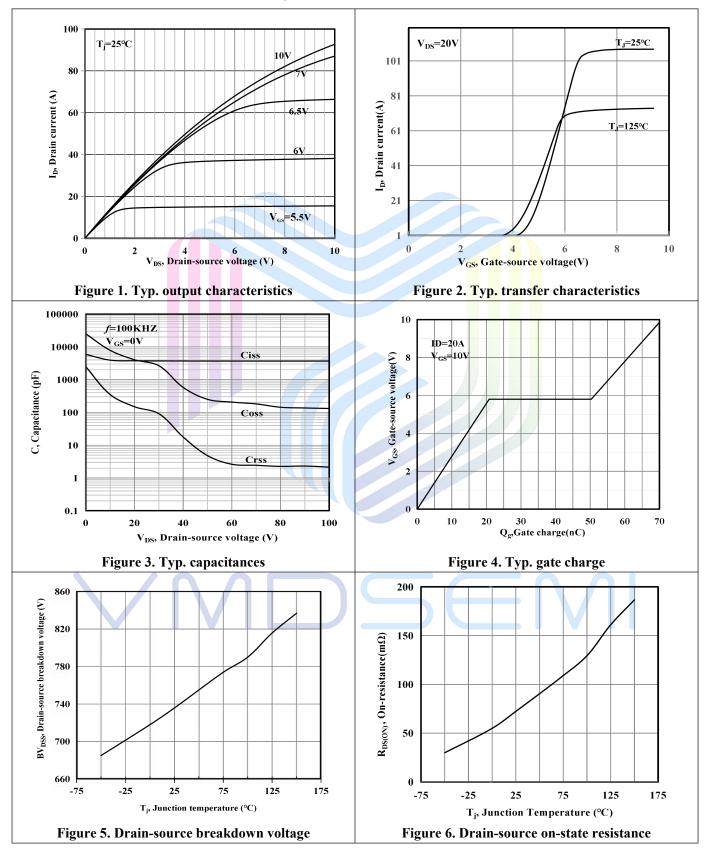
VSTF065R900NB

Electrical Characteristics (T_J= 25 °C, unless otherwise specified)

| Parameter | | Symbol | Test Conditions | Min | Тур | Max | Unit | |
|------------------------------------|------------------------------|--|---|------|-------|---------|-------------|--|
| Statistic Characteristics | | | | | | | | |
| Drain-Source Breakdown Voltag | $\mathrm{BV}_{\mathrm{DSS}}$ | V _{GS} =0V, I _D =250uA | 650 | - | - | V | | |
| Drain-Source Leakage Current | | I_{DSS} | V_{DS} =650V, V_{GS} =0V | - | - | 10 | uA | |
| Cata Saymaa Laakaaa Cymmant | Forward | I_{GSSF} | $V_{GS}=30V, V_{DS}=0V$ | - | - | 100 | A | |
| Gate-Source Leakage Current | Reverse | I_{GSSR} | V_{GS} =-30V, V_{DS} =0V | - | - | -100 nA | | |
| Gate Threshold Voltage | | $V_{GS(TH)}$ | $V_{DS}=V_{GS}$, $I_{D}=250uA$ | 3 | 4.0 | 5 | V | |
| Drain-Source On-State Resistance | ce | R _{DS(ON)} | V _{GS} =10V, I _D =23.5A | - | 72 | 90 | mΩ | |
| Gate Resistance | | R_G | F=1MHz, Open Drain | - | 4.2 | - | Ω | |
| Dynamic Characteristics | | | | | | | | |
| Input Capacitance | | C _{iss} | V _{DS} =50V | | 3700 | - | pF | |
| Output Capacitance | | Coss | V _{GS} =0V | - | 251 | - | pF | |
| Reverse Transfer Capacitance | | C _{rss} | f=100kHz | - | 4.8 | - | pF | |
| Turn-on Delay Time | urn-on Delay Time | | | - | 23.78 | - | | |
| Rise Time | | $t_{\rm r}$ | I _D =20A | - | 11.69 | - | 12 G | |
| Turn-off Delay Time | n-off Delay Time | | $R_G=2\Omega$ | - | 60.87 | - | ns | |
| Fall Time | | t_{f} | V _{GS} =10V | - | 6.29 | - | | |
| Gate Charge Characteristics | | | | | | | | |
| Gate to Source Charge | o Source Charge | | V -400V | 7- | 20.8 | - | nC | |
| Gate to Drain Charge | Gate to Drain Charge | | V_{DS} =400V I_{D} =20A | -/ | 29.5 | _ | | |
| Gate Charge Total | | Q_{g} | $V_{GS}=0$ to $10V$ | - | 70.7 | - | | |
| Gate Plateau Voltage | $V_{Plateau}$ | V GS-0 to 10 V | - | 5.8 | - | V | | |
| Reverse Diode Characteristics | | | | | | | | |
| Drain-Source Diode Forward Voltage | | $ m V_{SD}$ | $V_{GS}=0V, I_{S}=1A$ | - | 0.66 | 1.4 | V | |
| Reverse Recovery Time | t _{rr} | V _R =400V | - | 137 | - | ns | | |
| Reverse Recovery Charge | Qrr | $I_{\rm S}=20A$ | - | 928 | - | пC | | |
| Peak Reverse Recovery Current | I _{rrm} | di/dt=100A/us | - | 12.6 | 7-1 | A | | |
| | | | 5 | | V | | | |



Electrical Characteristics Diagrams





VSTF065R900NB

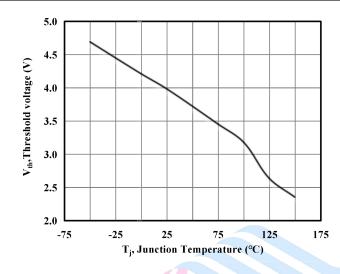


Figure 7. Threshold voltage

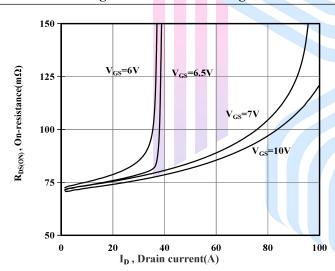


Figure 9. Drain-source on-state resistance

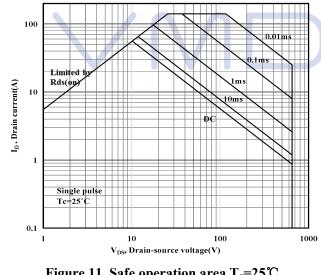


Figure 11. Safe operation area T_c=25℃

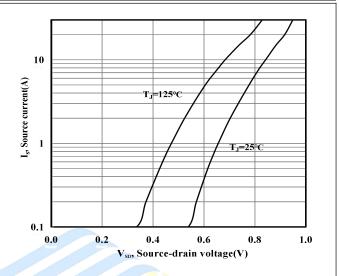


Figure 8. Forward characteristic of body diode

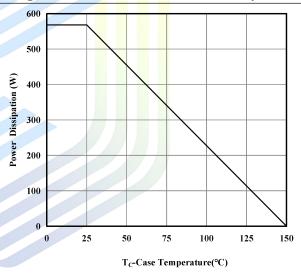


Figure 10. Power dissipation

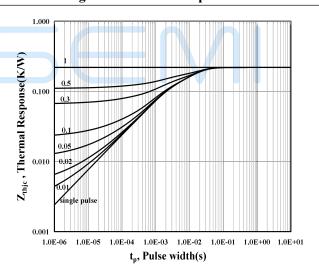
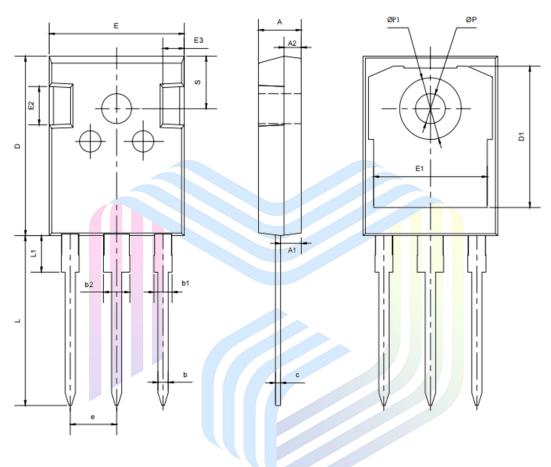


Figure 12. Max. transient thermal impedance



Mechanical Dimensions

TO-247 Package Information



COMMON DIMENSIONS (UNITS OF MEASURE=MILLIMETER)

| SYMBOL | MIN | MAX | | | |
|--------|-------|-------|--|--|--|
| Α | 4.80 | 5.20 | | | |
| A1 | 2.21 | 2.61 | | | |
| A2 | 1.85 | 2.15 | | | |
| b | 1.11 | 1.36 | | | |
| b1 | 1.91 | 2.21 | | | |
| b2 | 2.91 | 3.21 | | | |
| С | 0.51 | 0.75 | | | |
| D | 20.70 | 21.30 | | | |
| D1 | 16.25 | 16.85 | | | |
| E | 15.50 | 16.10 | | | |
| E1 | 13.00 | 13.60 | | | |
| E2 | 4.80 | 5.60 | | | |
| E3 | 2.10 | 2.70 | | | |
| е | 5.44 | BSC | | | |
| L | 19.62 | 20.22 | | | |
| L1 | - | 4.30 | | | |
| φΡ | 3.40 | 3.80 | | | |
| φP1 | - | 7.30 | | | |
| S | 6.15 | BSC | | | |

90mΩ, 650V, N-Channel Power MOSFET

VSTF065R900NB

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