



VMDSEMI

VSTL065R15BNA

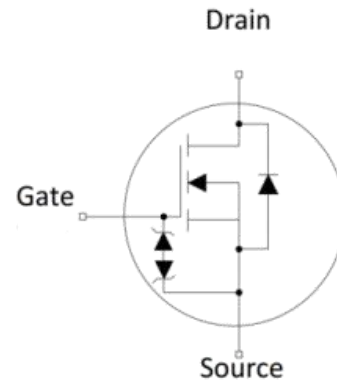
Datasheet



VMDSEMI

General Description
Symbol

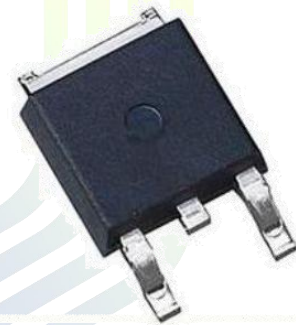
$V_{(BR)DSS}$	$R_{DS(ON)_{max}}$	I_D
650V	1500mΩ@10V	5A



Symbol of VSTL065R15BNA

Features

- Low RDS(on) & FOM
- Extremely low switching loss
- Excellent stability and uniformity
- Integrated ESD protection diode

Package Type

 TO-252
 Package Type of VSTL065R15BNA

Application

- PC power
- Telecom power
- Server power
- EV Charger
- Motor driver

Ordering Information

Product Name	Package	Marking
VSTL065R15BNA	TO-252	STL065R15BNA

Absolute Maximum Ratings ($T_J=25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	650	V
Gate-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current ^{Note 1}	I_D	5	A
Pulsed Drain Current ^{Note 2}	$I_{D, pulse}$	15	A
Continuous Diode Forward Current ^{Note 1}	I_S	5	A
Diode Pulsed Current ^{Note 2}	$I_{S, pulse}$	15	A
Max Power Dissipation ^{Note 3}	P_D	89	W
Avalanche Current, Single Pulse ^{Note 4}	I_{AS}	1.86	A
Avalanche Energy, Single Pulse ^{Note 4}	E_{AS}	104	mJ
Gate source ESD(HBM-C=100pF, R=1.5kΩ)	$V_{ESD(G-S)}$	Class 2	-
MOSFET dv/dt ruggedness, $V_{DS}=0\sim 480\text{V}$	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\sim 480\text{V}$, $I_{SD}\leq I_D$	dv/dt	15	V/ns
Operation and storage temperature	T_J, T_{STG}	-55 to 150	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	-	1.4	-	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient ^{Note 5}	$R_{\theta JA}$	-	62.5	-	

Notes:

Note1: Calculated continuous current based on maximum allowable junction temperature.

Note2: Pulse width limited by safe operating area.

Note3: Based on max. junction temperature, using junction-case thermal resistance.

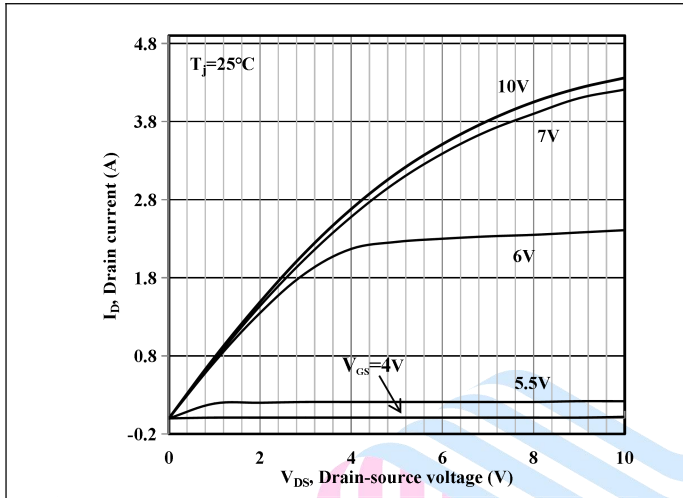
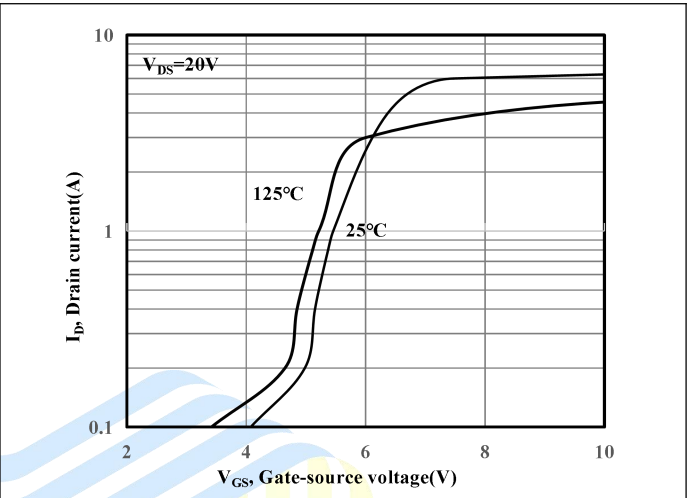
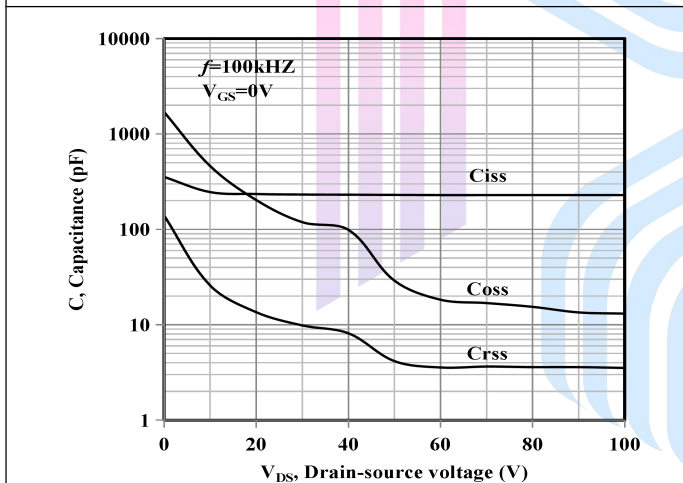
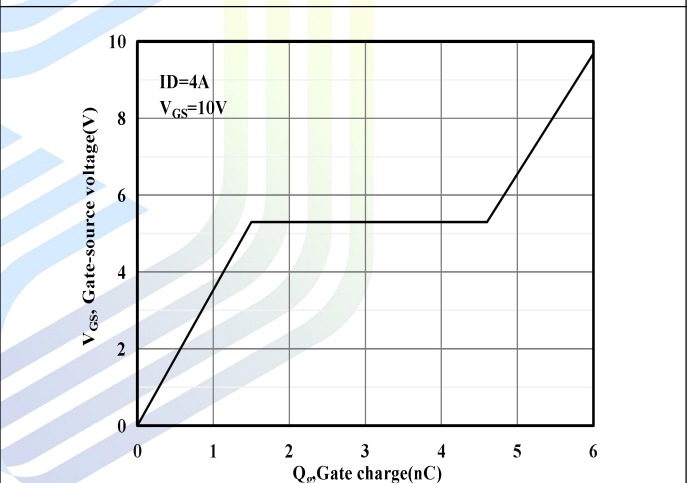
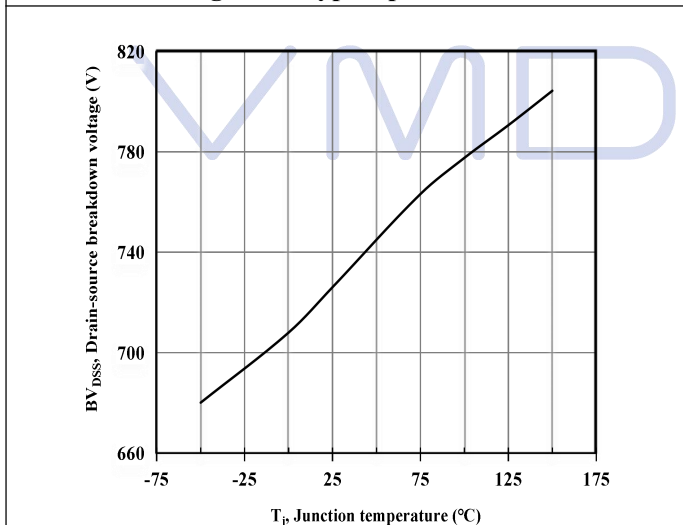
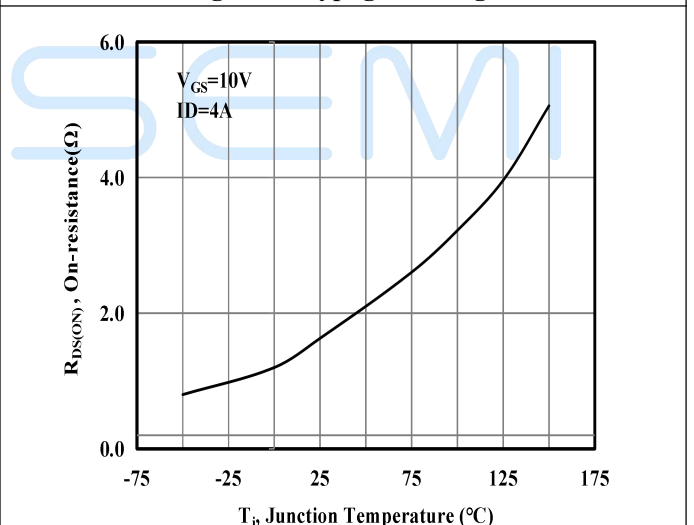
Note4: $V_{DD}=100\text{V}$, $V_{GS}=10\text{V}$, $L=60\text{mH}$, starting $T_A=25\text{ }^\circ\text{C}$.

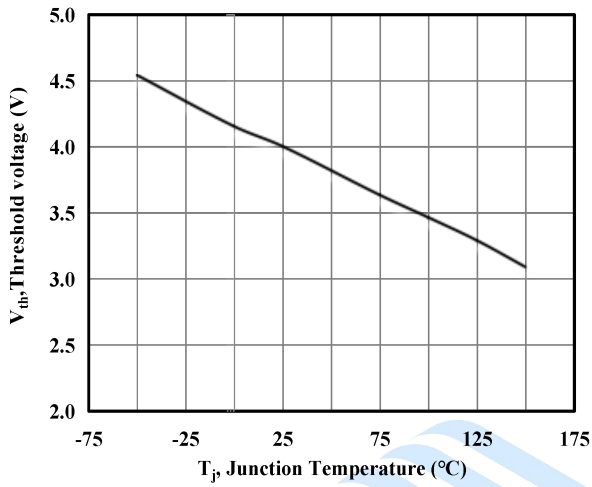
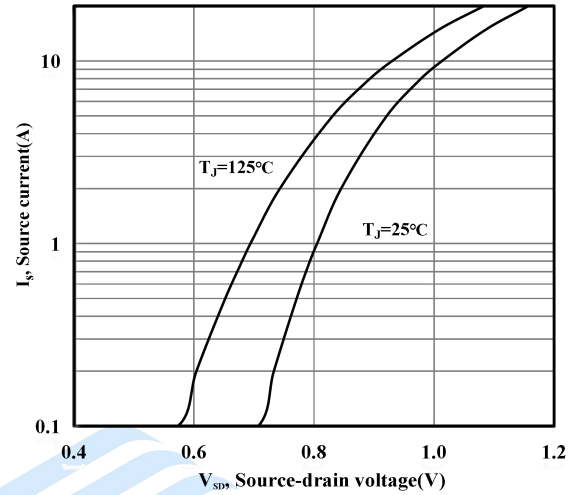
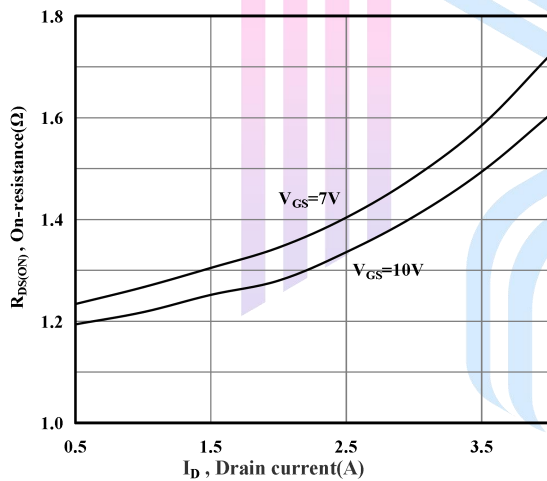
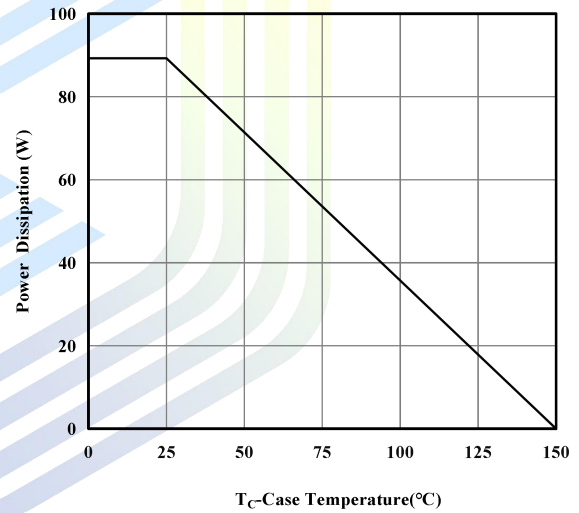
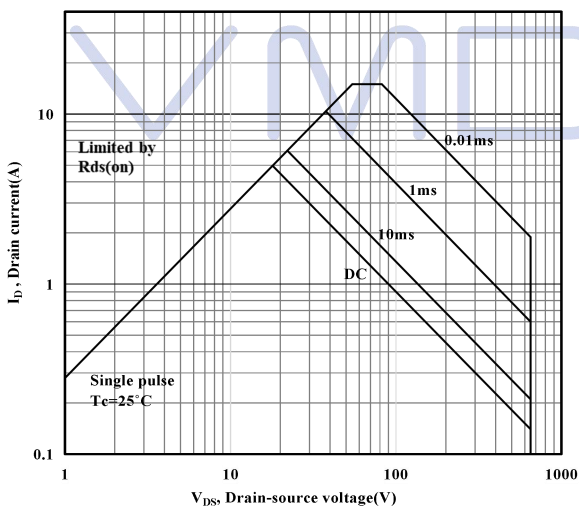
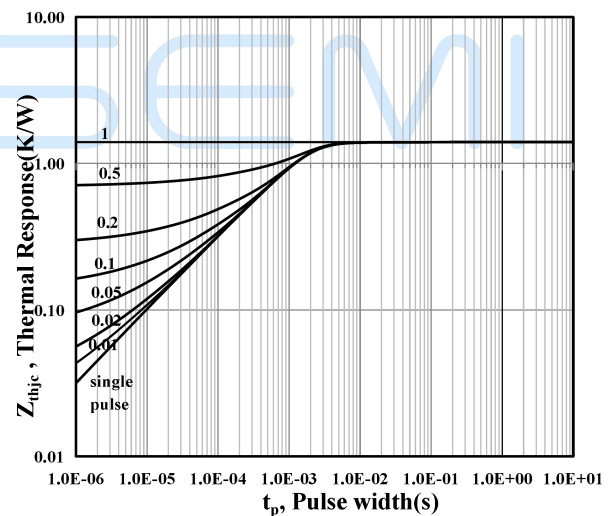
Note5: When mounted on 1 inch square copper board, $t\leq 10\text{sec}$. The value in any given application depends on the user's specific board design.

Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	650	-	-	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=650V, V_{GS}=0V$	-	-	1	μA
Gate-Source Leakage Current	Forward	$I_{GSSF}, V_{GS}=30V, V_{DS}=0V$	-	-	1	μA
	Reverse	$I_{GSSR}, V_{GS}=-30V, V_{DS}=0V$	-	-	-1	
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	3.0	4.0	5.0	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=2A$	-	1300	1500	$m\Omega$
Gate Resistance	R_G	$F=1MHz, \text{Open Drain}$	-	16.6	-	Ω
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS}=50V$	-	230	-	pF
Output Capacitance	C_{oss}	$V_{GS}=0V$	-	29	-	pF
Reverse Transfer Capacitance	C_{rss}	$f=100KHz$	-	4.16	-	pF
Turn-on Delay Time	$t_{d(on)}$	$V_{DS}=380V$	-	8.36	-	ns
Rise Time	t_r	$I_D=4A$	-	22.3	-	
Turn-off Delay Time	$t_{d(off)}$	$R_G=25\Omega$	-	18.7	-	
Fall Time	t_f	$V_{GS}=10V$	-	17.6	-	
Gate Charge Characteristics						
Gate to Source Charge	Q_{gs}	$V_{DS}=400V$ $I_D=4A$ $V_{GS}=0 \text{ to } 10V$	-	1.5	-	nC
Gate to Drain Charge	Q_{gd}		-	3.1	-	
Gate Charge Total	Q_g		-	6.1	-	
Gate Plateau Voltage	$V_{plateau}$		-	5.3	-	V
Reverse Diode Characteristics						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=1A$	-	0.8	1.3	V
Reverse Recovery Time	t_{rr}	$V_R=400V$	-	102	-	ns
Reverse Recovery Charge	Q_{rr}	$I_S=4A$	-	357	-	nC
Peak Reverse Recovery Current	I_{rrm}	$di/dt=100A/\mu s$	-	5.7	-	A

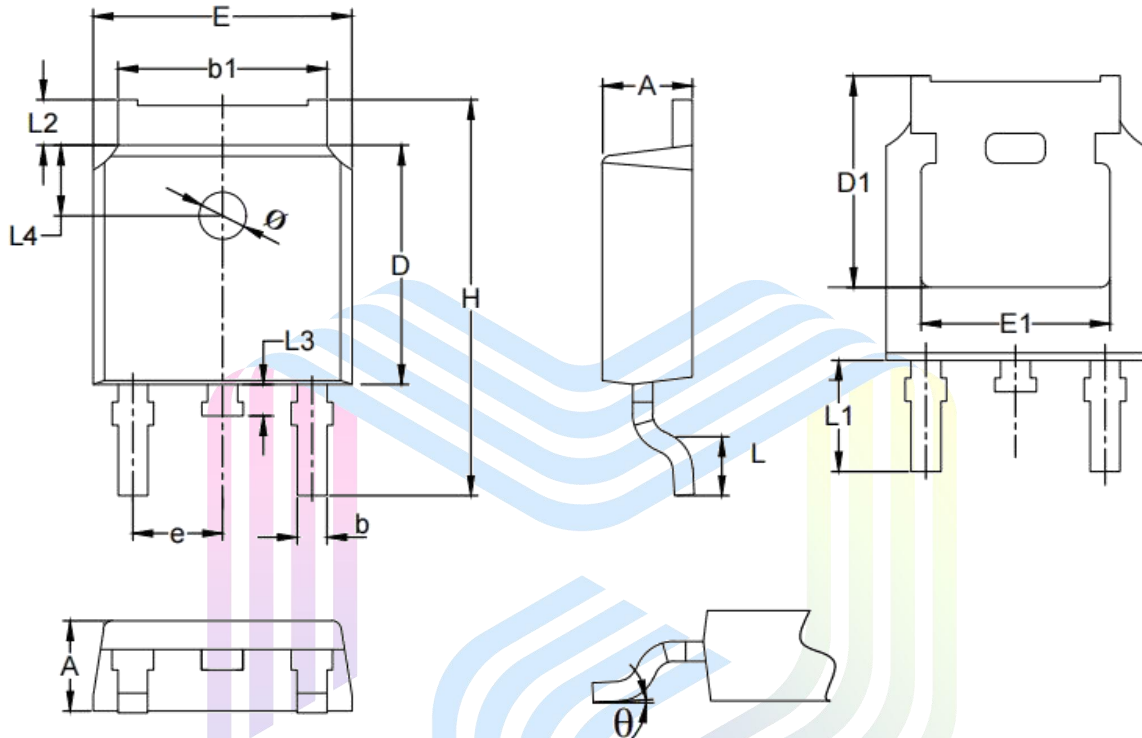
Electrical Characteristics Diagrams


Figure 1. Typ. output characteristics

Figure 2. Typ. transfer characteristics

Figure 3. Typ. capacitances

Figure 4. Typ. gate charge

Figure 5. Drain-source breakdown voltage

Figure 6. Drain-source on-state resistance


Figure 7. Threshold voltage

Figure 8. Forward characteristic of body diode

Figure 9. Drain-source on-state resistance

Figure 10. Power dissipation

Figure 11. Safe operation area $T_c=25^\circ\text{C}$

Figure 12. Max. transient thermal impedance

Mechanical Dimensions

TO-252 Package Information



COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

Symbol	MIN	MAX
A	2.2	2.4
A1	0	0.127
A2	-	-
b	0.66	0.9
b1	5.1	5.5
c	0.43	0.61
D	5.95	6.22
D1	5.3REF	
E	6.4	6.75
E1	4.8REF	
e	2.286BSC	
H	9.4	10.5
L	1.38	2
L1	2.9REF	
L2	0.88	1.28
L3	0.5	1
L4	1.8REF	
θ	0°	8°

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