# WinhiSemi

# WLPA2P5R310PA



## 31mΩ, -25V, P-Channel Power MOSFET

## WLPA2P5R310PA

## **General Description**

WLPA2P5R310PA MOSFET is based on VMD Semiconductor's unique device design to achieve low  $R_{DS(ON)}$ , low gate charge, fast switching and excellent avalanche characteristics. The low  $V_{th}$  series is specially optimized for synchronous rectification systems with low driving voltage.

## **Symbol**

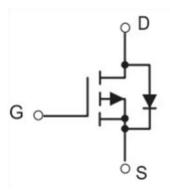


Figure 1 Symbol of WLPA2P5R310PA

## **Features**

- $\blacksquare R_{DS(ON) TYP} = 23.9 \text{m}\Omega @V_{GS} = -4.5 \text{V}$
- Extremely low switching loss
- Stable performance
- Fast switching and soft recovery

# **Application**

- Load Switch
- DC-DC converter
- Switched mode power supply
- Switching voltage regulator

## Package Type

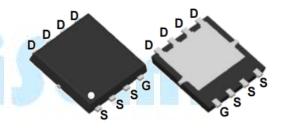


Figure 2 Package Type of WLPA2P5R310PA

# **Ordering Information**

| Product Name  | Package        |  |
|---------------|----------------|--|
| WLPA2P5R310PA | PDFN3.3*3.3-8L |  |

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# **Absolute Maximum Ratings**

| Parameter   | Symbol               | Rating     | Unit |
|---|----------------------|------------|------|
| Drain-Source Voltage  | $V_{ m DSS}$         | -25        | V    |
| Gate-Source Voltage <sup>Note 1</sup>                                   | $V_{ m GSS}$         | ±8         | V    |
| Continuous Drain Current <sup>Note 2</sup> T <sub>C</sub> =25°C         | $I_D$                | -22        | A    |
| Pulsed Drain Current <sup>Note 3</sup> , T <sub>C</sub> =25°C           | $I_{DM}$             | -66        | A    |
| Max Power Dissipation <sup>Note 4</sup> T <sub>C</sub> =25°C            | $P_{\mathrm{D}}$     | 19         | W    |
| Avalanche Current, Single Pulse   | I <sub>AS</sub>      | -50        | A    |
| Avalanche Energy, Single Pulse Note 5                                   | E <sub>AS</sub>      | 124        | mJ   |
| Continuous Diode Forward Current <sup>Note 2</sup> T <sub>C</sub> =25°C | $I_{S}$              | -22        | A    |
| Diode Pulse Current <sup>Note 3</sup> T <sub>C</sub> =25°C              | I <sub>S.PULSE</sub> | -66        | A    |
| Operation and storage temperature                                       | $T_{J}$ , $T_{STG}$  | -55 to 150 | °C   |

## **Thermal Resistance**

| Parameter                               | Symbol         | Min | Тур | Max | Unit   |
|---|----------------|-----|-----|-----|--------|
| Thermal Resistance, Junction-to-Case    | $R_{	heta JC}$ |     | 6.5 |     | °C/W   |
| Thermal Resistance, Junction-to-Ambient | $R_{	heta JA}$ |     | 60  |     | - C/ W |

## Notes:

- 1) It is recommended that the value be less than 8V in practice.
- 2) Calculated continuous current based on maximum allowable junction temperature.
- 3) Repetitive rating; pulse width limited by max.junction temperature.
- 4) P<sub>D</sub> is based on max.junction temperature, using junction-case thermal resistance.
- 5)  $V_{DS}$ =-24V, $V_{GS}$ =-4.5 V, L=0.1 mH,starting  $T_J$ =25 °C.

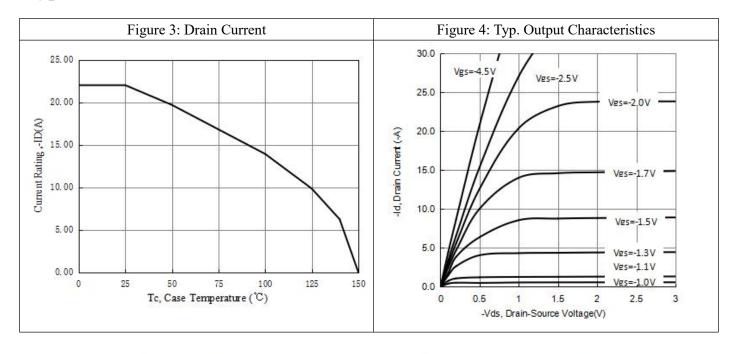
# $31m\Omega$ , -25V, P-Channel Power MOSFET

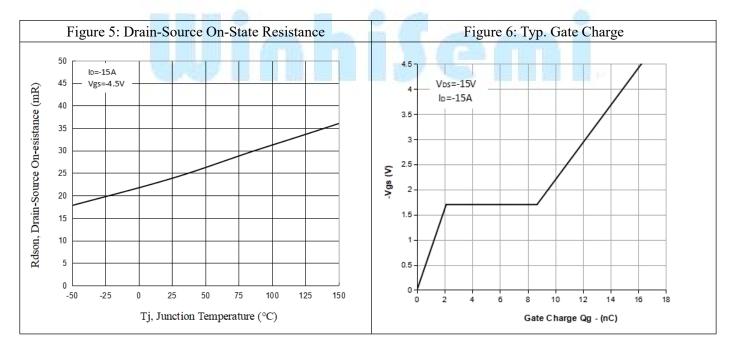
## WLPA2P5R310PA

# **Electrical Characteristics** (T<sub>J</sub>= 25 °C, unless otherwise specified)

| Parameter                          | Symbol                    | Test Conditions                             | Min  | Тур  | Max  | Unit |
|------------------------------------|---------------------------|---|------|------|------|------|
| Statistic Characteristics          | Statistic Characteristics |   |      |      |      |      |
| Drain-Source Breakdown Voltage     | BV <sub>DSS</sub>         | V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA | -25  |      |      | V    |
| Zero Gate Voltage Drain Current    | $I_{DSS}$                 | $V_{DS}$ =-20V, $V_{GS}$ =0V                |      |      | -1   | uA   |
| Gate-Body Leakage Current          | I <sub>GSS</sub>          | $V_{GS}=\pm 8V, V_{DS}=0V$                  |      |      | ±100 | nA   |
| Gate Threshold Voltage             | V <sub>GS(TH)</sub>       | $V_{DS}=V_{GS}$ , $I_D=-250uA$              | -0.4 |      | -1.0 | V    |
| Static Drain-Source On-Resistance  | D                         | $V_{GS}$ =-4.5V, $I_{D}$ =-15A              |      | 23.9 | 31   | mΩ   |
| Static Diani-Source On-Resistance  | R <sub>DS(ON)</sub>       | $V_{GS}$ =-6V, $I_{D}$ =-15A                |      | 21.6 | 26.8 | mΩ   |
| Gate Resistance                    | $R_G$                     | f=1MHz, Open Drain                          |      | 0.2  |      | Ω    |
| Dynamic Characteristics            |                           |   |      |      |      |      |
| Input Capacitance                  | C <sub>ISS</sub>          | $V_{DS}$ =-10V                              |      | 937  |      | pF   |
| Output Capacitance                 | Coss                      | $V_{GS}=0V$                                 |      | 418  |      | pF   |
| Reverse Transfer Capacitance       | $C_{RSS}$                 | f=1MHz                                      |      | 275  |      | pF   |
| Turn-on Delay Time                 | t <sub>d(on)</sub>        | V <sub>DS</sub> =-15V                       |      | 5.4  |      |      |
| Rise Time                          | $t_{\rm r}$               | $I_D=-15A$                                  |      | 27.6 |      |      |
| Turn-off Delay Time                | $t_{ m d(off)}$           | $R_G=4.7\Omega$                             |      | 23.9 |      | ns   |
| Fall Time                          | $t_{\mathrm{f}}$          | $V_{GS}$ =-4.5 $V$                          |      | 32.8 |      |      |
| Gate Charge Characteristics        |                           |   | _    |      |      |      |
| Gate to Source Charge              | $Q_{gs}$                  | V <sub>DS</sub> =-15V                       |      | 2.1  |      |      |
| Gate to Drain Charge               | $Q_{\mathrm{gd}}$         | $I_D=-15A$                                  |      | 6.6  |      | пC   |
| Gate Charge Total                  | Qg                        | $V_{GS}$ =-4.5V                             |      | 16.2 |      |      |
| Gate Plateau Voltage               | V <sub>Plateau</sub>      |   |      | -1.7 |      | V    |
| Reverse Diode Characteristics      |                           |   |      |      |      |      |
| Drain-Source Diode Forward Voltage | $V_{\mathrm{SD}}$         | $V_{GS}=0V, I_{SD}=-15A$                    |      | -1.0 |      | V    |
| Reverse Recovery Time              | t <sub>rr</sub>           | $V_R=15V$                                   |      | 22.7 |      | ns   |
| Reverse Recovery Charge            | Qrr                       | $I_{F}=1A$                                  |      | 14   |      | nC   |
| Peak Reverse Recovery Current      | $I_{rrm}$                 | $dI_F/dt=100A/us$                           |      | 1.2  |      | A    |

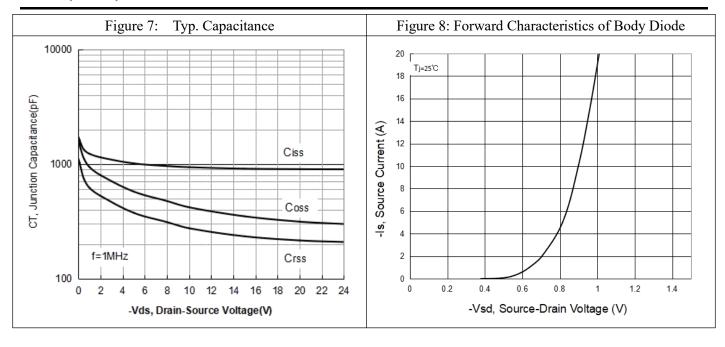
# **Typical Performance Characteristics**

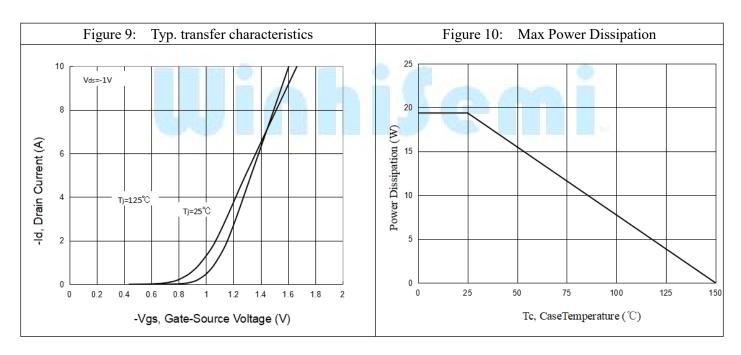


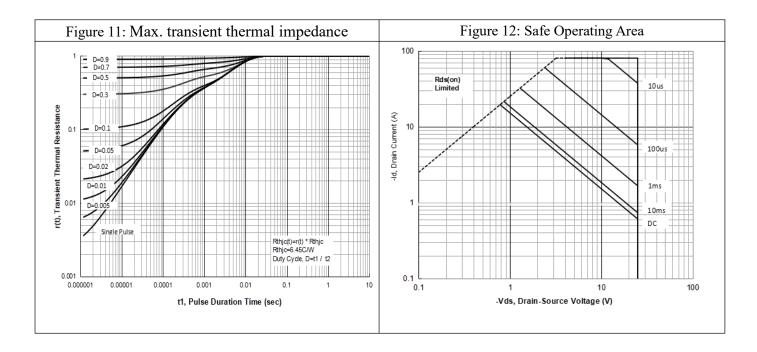


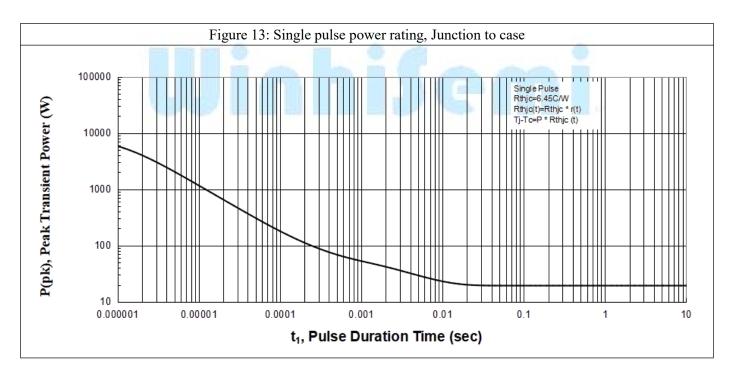
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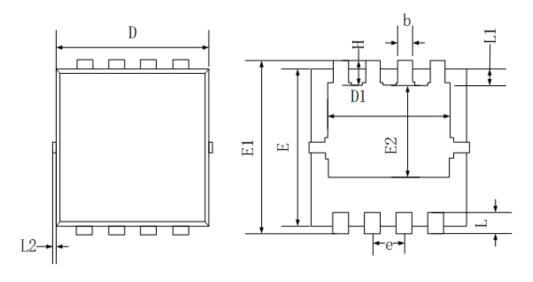


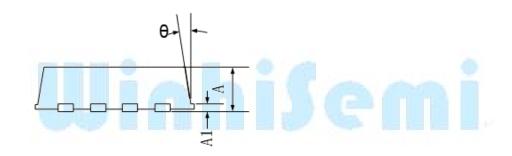






# **Mechanical Dimensions (PDFN3.3\*3.3 Unit: mm)**





| Cymbol | Dimensi | ons(mm) |  |  |
|--------|---------|---------|--|--|
| Symbol | Min.    | Max.    |  |  |
| Α      | 0.70    | 0.90    |  |  |
| A1     | 0.10    | 0.25    |  |  |
| D      | 2.90    | 3.25    |  |  |
| D1     | 2.25    | 2.69    |  |  |
| Е      | 2.90    | 3.20    |  |  |
| E1     | 3.00    | 3.60    |  |  |
| E2     | 1.35    | 2.20    |  |  |
| b      | 0.20    | 0.40    |  |  |
| е      | 0.65BSC |         |  |  |
| L      | 0.30    | 0.50    |  |  |
| L1     | 0.13BSC |         |  |  |
| L2     | 0.00    | 0.20    |  |  |
| Н      | 0.15    | 0.65    |  |  |
| θ      | 0°      | 14°     |  |  |

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