WinhiSemi

VTGA060N02TA

Datasheet



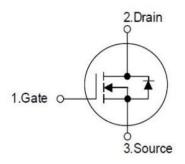
14m Ω , 18V, N-Channel Power MOSFET

VTGA060N02TA

General Description

VTGA060N02TA N-Channel MOSFET is based on unique device design to achieve low RDS_(ON), low gate charge, fast switching and excellent avalanche characteristics.

Symbol



Symbol of VTGA060N02TA

Features

- Low RDS(ON) & FOM
- $\blacksquare R_{DS(ON) max} = 14m\Omega @V_{GS} = 4.5V$
- Extremely low switching loss
- Fast switching and soft recovery

Package Type



Application

- Charging Circuit
- Battery Applications
- Synchronous Rectification
- High Frequency Switching

Package Type of VTGA060N02TA

Ordering Information

Product Name	Package	Marking
VTGA060N02TA	DFN3*3	60N02

14mΩ, 18V, N-Channel Power MOSFET

VTGA060N02TA

Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	18	V
Gate-Source Voltage	V _{GS}	±8	V
Continuous Drain Current ^{Note 1} , T _C =25°C	I_D	22	A
Pulsed Drain Current ^{Note 2}	I_{DM}	66	A
Max Power Dissipation Note 3, T _C =25°C	P _D	19.4	W
Avalanche Current, Single Pulse Note 5	I _{AS}	28.49	A
Avalanche Energy, Single Pulse Note 5	Eas	121.7	mJ
Operation Junction temperature	T _J	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	Min	Тур	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$		6.45		°C/W
Thermal Resistance, Junction-to-Ambient ^{Note4}	$R_{ heta JA}$		62		C/W

Notes:

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_D is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with Ta=25 °C.
- 5) V_{DS} =15V, V_{GS} =4.5V, L=0.3mH, Rg=25Ω, starting T_{J} =25 °C.

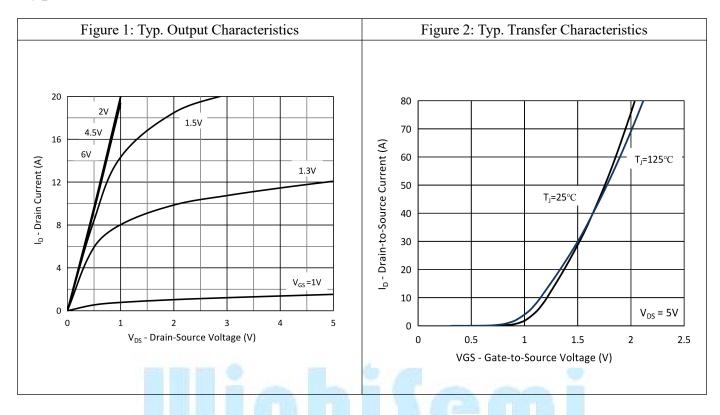
$14m\Omega$, 18V, N-Channel Power MOSFET

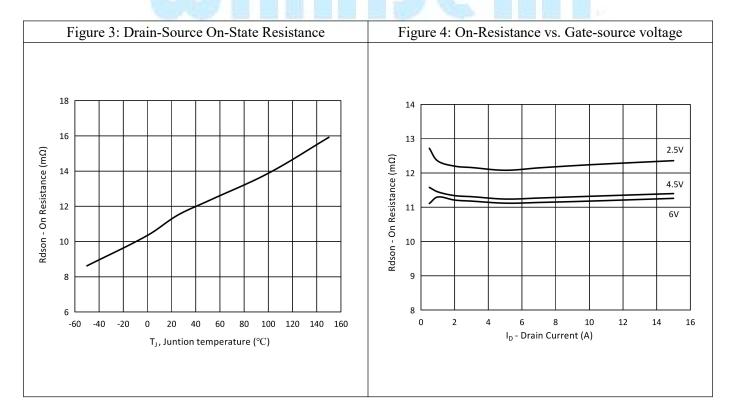
VTGA060N02TA

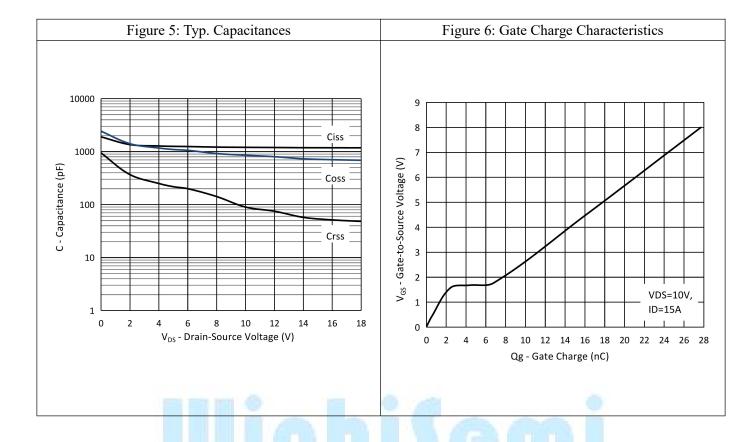
Electrical Characteristics (T_J= 25 °C, unless otherwise specified)

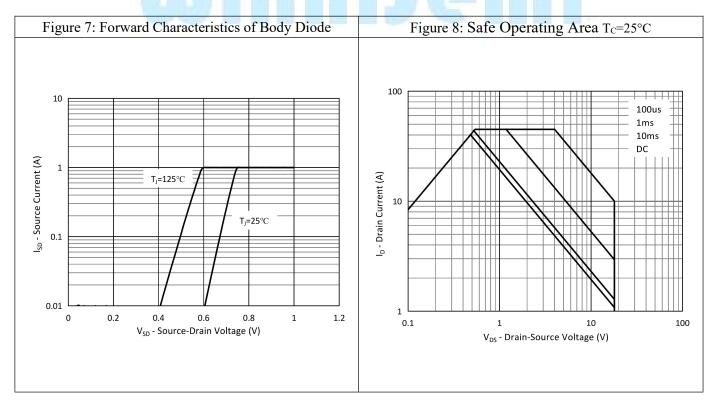
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Statistic Characteristics							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	18			V	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=18V, V_{GS}=0V$			1	uA	
Gate-Body Leakage Current	I _{GSS}	$V_{GS}=\pm 8V, V_{DS}=0V$			±100	nA	
Gate Threshold Voltage	V _{GS(TH)}	$V_{DS}=V_{GS}$, $I_D=250uA$	0.35	0.55	0.85	V	
Static Drain-Source On-Resistance	D	V_{GS} =4.5V, I_{D} =5A		10.8	14	$m\Omega$	
Static Diani-Source On-Resistance	R _{DS(ON)}	V_{GS} =4.5V, I_{D} =15A		11.4	14	$m\Omega$	
Gate Resistance	R_G	f=1MHz, open drain		0.79		Ω	
Dynamic Characteristics							
Input Capacitance	Ciss	$V_{GS}=0V$		1207		pF	
Output Capacitance	Coss	$V_{DS}=10V$		855.5		pF	
Reverse Transfer Capacitance	C_{rss}	f=1MHz		90		pF	
Turn-on Delay Time	t _{d(on)}	$V_{DS}=15V$		10			
Rise Time	t _r	V_{GS} =4.5 V		3.2			
Turn-off Delay Time	$t_{d(off)}$	$I_D=6A$		35.8		ns	
Fall Time	t_{f}	$R_G=3\Omega$		11			
Switching Characteristics							
Total Gate Charge (@VGS=8V)	$Q_{\rm g}$	V _{GS} =0 to 8V		27.7			
Total Gate Charge (@VGS=4.5V)	Qg	$V_{GS} = 0.00 \text{ eV}$ $V_{DS} = 10 \text{ V}$		16.1		пC	
Gate to Source Charge	Q_{gs}	$I_{D}=15A$	in in the	2.44		IIC .	
Gate to Drain Charge	Q_{gd}	1D-13A		4.35			
Reverse Diode Characteristics							
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=12A$		0.82	1.2	V	
Reverse Recovery Time	t _{rr}	V _{DS} =10V		34.02		ns	
Reverse Recovery Charge	Qrr	$I_F=12A$		16.26		пC	
Peak Reverse Recovery Current	I _{rrm}	di/dt=100A/us		0.7		A	

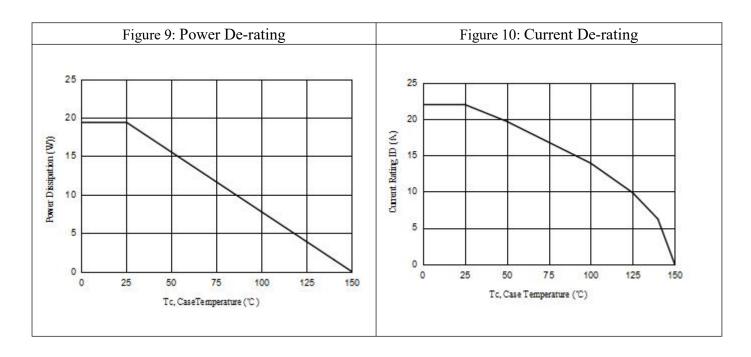
Typical Performance Characteristics

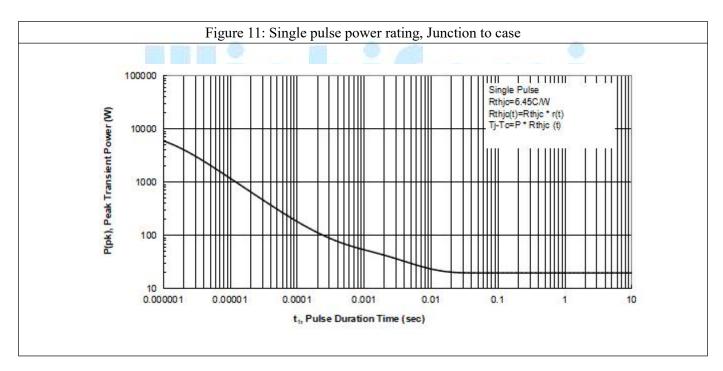


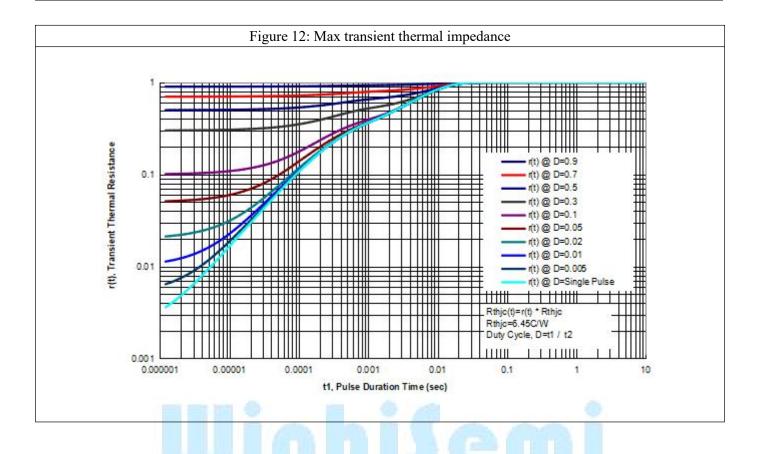




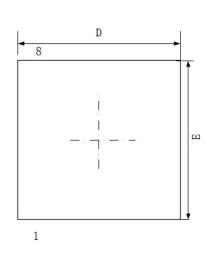


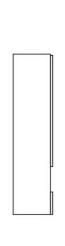


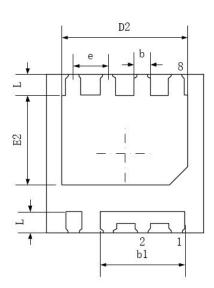


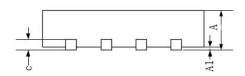


Mechanical Dimensions (DFN3*3 Unit:mm)









SYMBOL	MILLMETER			
STIVIDOL	MIN	NOM	MAX	
А	0.70	0.75	0.80	
A1	0.00	0.02	0.05	
b	0.25	0.30	0.35	
b1	1.55	1.60	1.65	
С	0.19	0.20	0.21	
D	2.90	3.00	3.10	
D2	2.30	2.40	2.50	
Е	2.90	3.00	3.10	
E2	1.60	1.70	1.80	
е	0.65BSC			
L	0.35	0.40	0.45	

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