

VSTD065R38ANA

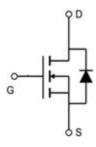
Datasheet



General Description

V _{(BR)DSS}	R _{DS(ON)_max}	I_D
650V	380mΩ@10V	11A

Symbol



Symbol of VSTD065R38ANA

Features

- Extremely low switching loss
- Excellent stability and uniformity
- RoHS and Halogen-Free Compliant

Application

- PC power
- LED lighting
- Telecom power
- Server power
- Solar/UPS

Package Type



Package Type of VSTD065R38ANA

Ordering Information

Product Name	Package	Marking		
VSTD065R38ANA	TO-220F	STD065R38ANA		



VSTD065R38ANA

Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	650	V
Gate-Source Voltage	V_{GS}	±30	V
Continuous Drain Current Note 1, T _C =25°C	I_D	11	A
Pulsed Drain Current Note 2, T _C =25°C	I _{D, pulse}	33	A
Continuous Diode Forward Current Note 1, T _C =25°C	I_S	11	A
Diode Pulsed Current Note 2, T _C =25°C	I _{S, pulse}	33	A
Max Power Dissipation Note 3, T _C =25°C	P_{D}	79	W
Avalanche Current, Single Pulse Note 4	I _{AS}	3.4	A
Avalanche Energy, Single Pulse Note4	Eas	453.4	mJ
MOSFET dv/dt ruggedness, V _{DS} =0~480V	dv/dt	50	V/ns
Reverse diode dv/dt, $V_{DS}=0\sim480V$, $I_{SD}<=I_{D}$	dv/dt	15	V/ns
Operation and storage temperature	T _J ,T _{STG}	-55 to 150	°C

Thermal Resistance

Parameter		Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	7	$R_{ heta JC}$		1.59		°C/W
Thermal Resistance, Junction-to-Ambient Note5		$R_{ heta JA}$		62		C/W

Notes:

Note1: Calculated continuous current based on maximum allowable junction temperature.

Note2: Pulse width limited by safe operating area.

Note3: Based on max. junction temperature, using junction-case thermal resistance.

Note4: $V_{DD}=100V$, $V_{GS}=10V$, L=80mH, starting $T_A=25$ °C.

Note5: When mounted on 1 inch square copper board, t≤10sec. The value in any given application depends on the user's specific board design.



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Electrical Characteristics(T_A= 25 °C, unless otherwise specified)

Parameter		Symbol	Test Conditions	Min	Тур	Max	Unit	
Statistic Characteristics								
Drain-Source Breakdown Voltage		BV_{DSS}	V _{GS} =0V, I _D =250uA	650			V	
Drain-Source Leakage Current		I_{DSS}	V _{DS} =650V, V _{GS} =0V			1	uA	
Forward Forward		I_{GSSF}	V_{GS} =30V, V_{DS} =0V	=30V, V _{DS} =0V		100		
Gate-Source Leakage Current	Reverse	I_{GSSR}	V_{GS} =-30V, V_{DS} =0V			-100	nA	
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250uA$	2.9		3.9	V	
Drain-Source On-State Resistance	ce	R _{DS(ON)}	$V_{GS}=10V, I_{D}=5.5A$		360	380	$m\Omega$	
Gate Resistance		R_G	F=1MHz, Open Drain		5.14		Ω	
Dynamic Characteristics								
Input Capacitance		C_{iss}	V _{DS} =50V		746.5		pF	
Output Capacitance		Coss	V _{GS} =0V		62.2		pF	
Reverse Transfer Capacitance		C_{rss}	f=1MHz		2.2		pF	
Turn-on Delay Time		t _{d(on)}	V_{DS} =400V		10.7			
Rise Time		t_r	I _D =6A		6.6		ns	
Turn-off Delay Time		$t_{ m d(off)}$	$R_G=2\Omega$		29.5		113	
Fall Time		t_{f}	V _{GS} =10V		6.1			
Gate Charge Characteristics								
Gate to Source Charge		Q_{gs}	V _{DS} =400V		4.4			
Gate to Drain Charge		Q_{gd}	$I_D=6A$		5.3		nC	
Gate Charge Total		Q_{g}	$V_{GS}=0$ to $10V$		15.6			
Gate Plateau Voltage		V _{Plateau}	VGS=0 to 10 V		5.44		V	
Reverse Diode Characteristics								
Drain-Source Diode Forward Voltage		V_{SD}	$V_{GS}=0V, I_S=1A$		0.73		V	
Reverse Recovery Time		t_{rr}	$V_R=400V$		294.5		ns	
Reverse Recovery Charge		Qrr	$I_S=6A$		2.6		uС	
Peak Reverse Recovery Current		I _{rrm}	di/dt=100A/us		17.5		A	



Electrical Characteristics Diagrams

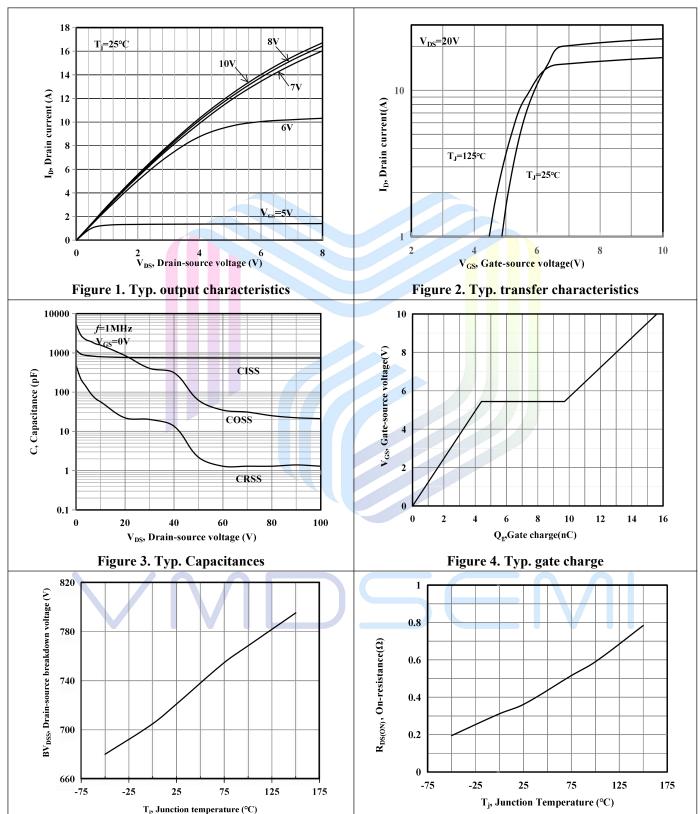


Figure 5. Drain-source breakdown voltage

Figure 6. Drain-source on-state resistance



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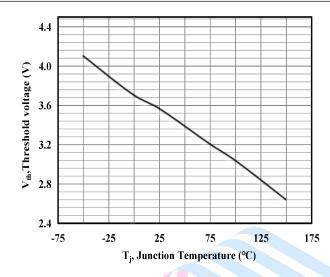


Figure 7. Threshold voltage

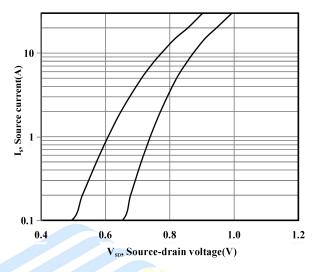


Figure 8. Forward characteristic of body diode

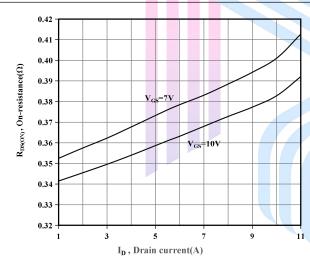


Figure 9. Drain-source on-state resistance

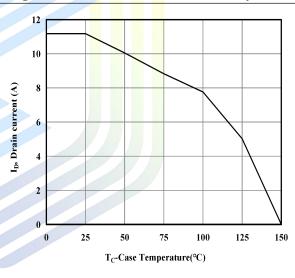
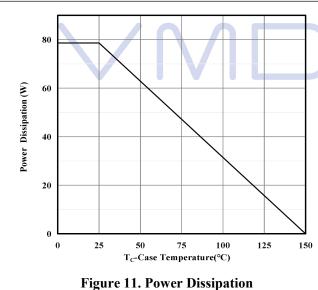
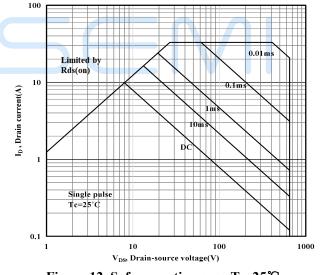


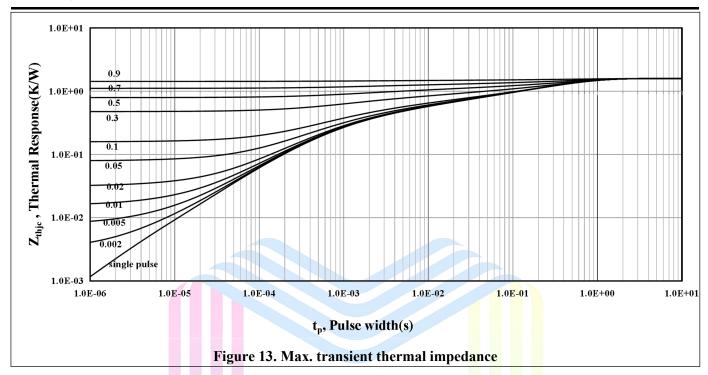
Figure 10. Drain current Derating







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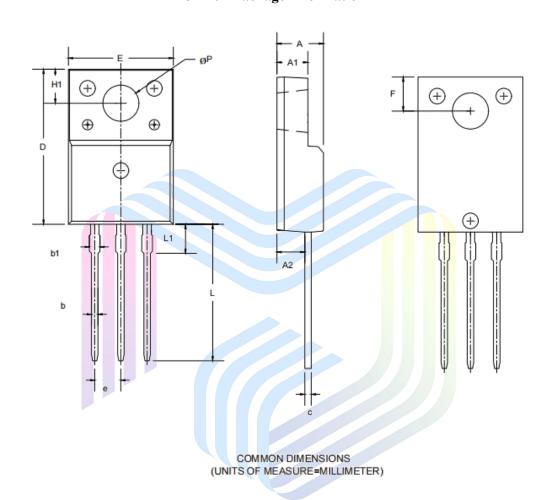






Mechanical Dimensions

TO-220F Package Information





SYMBOL	MIN	MAX			
A	4. 50	4.90			
A1	2, 30	2.80			
A2	2.50	2.90			
b	0.70	0.95			
b1	1.08	1.55			
С	0.40	0.70			
D	15.00	16. 17			
Е	9.50	10. 50			
e	2. 54BSC				
F	2.80	3.65			
H1	6. 7REF				
L	12.50	13. 50			
L1	2.90	3.90			
ΦР	2.90	3.40			

380mΩ, 650V, N-Channel Power MOSFET

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