WinhiSemi

VUGA053N25TA



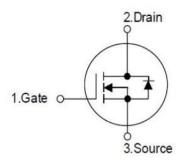
$10m\Omega$, 20V, N-Channel Power MOSFET

VUGA053N25TA

General Description

VUGA053N25TA N-Channel MOSFET is based on unique device design to achieve low RDS_(ON), low gate charge, fast switching and excellent avalanche characteristics.

Symbol



Symbol of VUGA053N25TA

Features

- Low RDS(ON) & FOM
- $\blacksquare R_{DS(ON) max} = 10 m\Omega @V_{GS} = 4.5V$
- Extremely low switching loss
- Fast switching and soft recovery

Package Type



Application

- Charging Circuit
- Battery Applications
- Synchronous Rectification
- High Frequency Switching

Package Type of VUGA053N25TA

Ordering Information

Product Name	Package	Marking
VUGA053N25TA	PDFN3.3*3.3	53N25

10mΩ, 20V, N-Channel Power MOSFET

VUGA053N25TA

Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	±8	V
Continuous Drain Current ^{Note 1} , T _C =25°C	I_D	22	A
Pulsed Drain Current ^{Note 2}	I_{DM}	66	A
Max Power Dissipation Note 3, T _C =25°C	P_{D}	19.4	W
Avalanche Current, Single Pulse Note 5	I _{AS}	27.23	A
Avalanche Energy, Single Pulse Note 5	Eas	111.22	mJ
Operation Junction temperature	T_{J}	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	Min	Тур	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$		6.45		°C/W
Thermal Resistance, Junction-to-Ambient ^{Note4}	$R_{ heta JA}$		62		°C/W

Notes:

- 1) Calculated continuous current based on maximum allowable junction temperature.
- 2) Repetitive rating; pulse width limited by max. junction temperature.
- 3) P_D is based on max. junction temperature, using junction-case thermal resistance.
- 4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with Ta=25 °C.
- 5) V_{DS} =15V, V_{GS} =4.5V, L=0.3mH, Rg=25 Ω , starting T_{J} =25 °C.

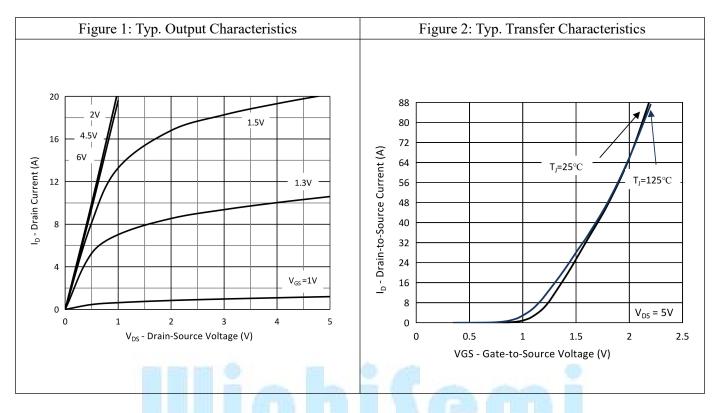
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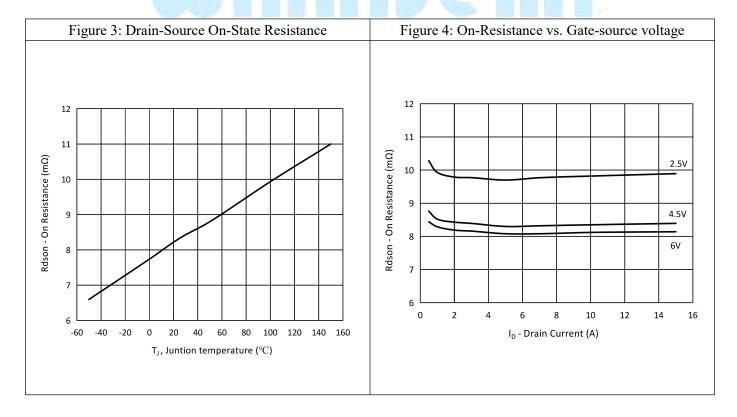
VUGA053N25TA

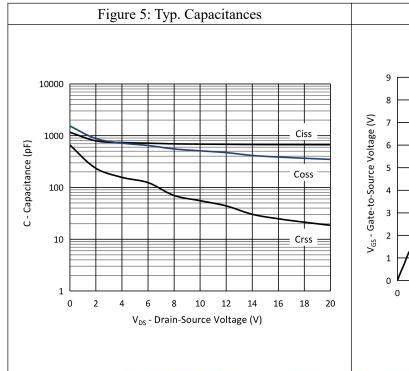
Electrical Characteristics (T_J= 25 °C, unless otherwise specified)

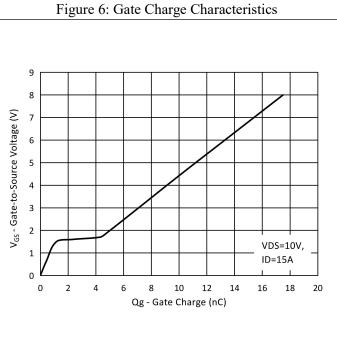
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V, I _D =250uA	20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=20V, V_{GS}=0V$			1	uA
Gate-Body Leakage Current	I _{GSS}	$V_{GS}=\pm 8V, V_{DS}=0V$			±100	nA
Gate Threshold Voltage	V _{GS(TH)}	$V_{DS}=V_{GS}$, $I_D=250uA$	0.4	0.6	0.9	V
G. C. D. C. O. D. C.	D	V_{GS} =4.5V, I_{D} =5A		8.2	10	mΩ
Static Drain-Source On-Resistance	R _{DS(ON)}	V_{GS} =4.5V, I_{D} =15A		8.2	10	mΩ
Gate Resistance	R_G	f=1MHz, open drain		0.41		Ω
Dynamic Characteristics						
Input Capacitance	Ciss	$V_{GS}=0V$		687.7		pF
Output Capacitance	Coss	$V_{DS}=10V$		510.4		pF
Reverse Transfer Capacitance	C_{rss}	f=1MHz		55.5		pF
Turn-on Delay Time	t _{d(on)}	$V_{DS}=15V$		7.3		
Rise Time	t_r	V_{GS} =4.5V		2.9		
Turn-off Delay Time	$t_{ m d(off)}$	$I_D=6A$		25.1		ns
Fall Time	t_{f}	$R_G=3\Omega$		8.4		
Switching Characteristics						
Total Gate Charge (@VGS=8V)	$Q_{\rm g}$	V _{GS} =0 to 8V		17.49		
Total Gate Charge (@VGS=4.5V)	Qg	$V_{GS} = 0.00 \text{ eV}$ $V_{DS} = 10 \text{ V}$		10.15		пC
Gate to Source Charge	Q_{gs}	$I_{D}=15A$	in in the	1.15		IIC
Gate to Drain Charge	Q_{gd}	1D-13A		3.13		
Reverse Diode Characteristics						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=12A$		0.82	1.2	V
Reverse Recovery Time	t _{rr}	V _{DS} =10V		28.74		ns
Reverse Recovery Charge	Qrr	$I_F=12A$		17.03		nC
Peak Reverse Recovery Current	I _{rrm}	di/dt=100A/us		1.05		A

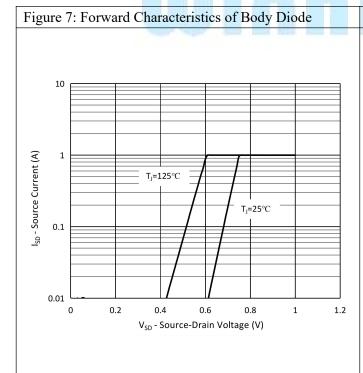
Typical Performance Characteristics











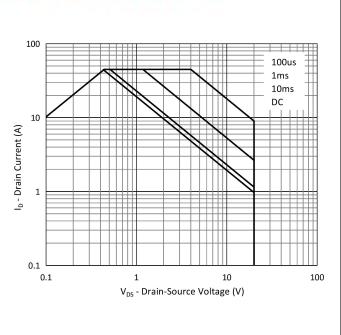
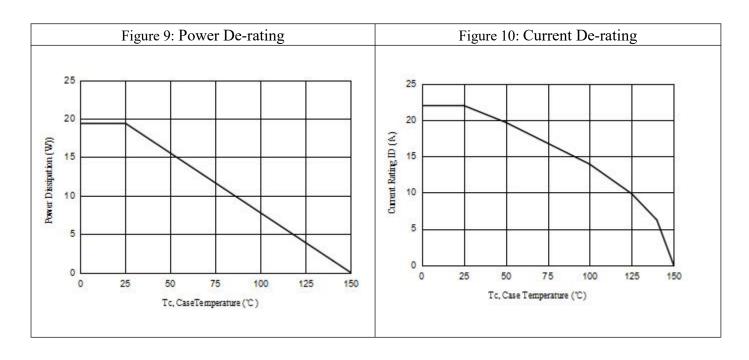
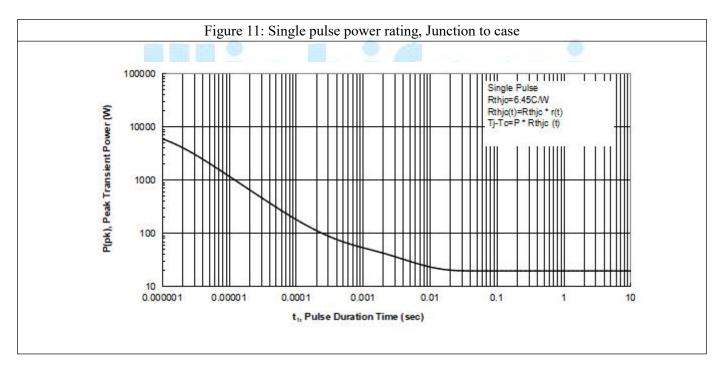
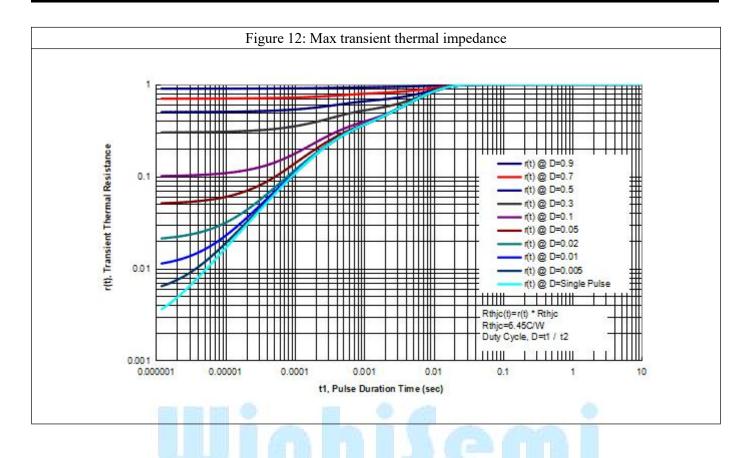


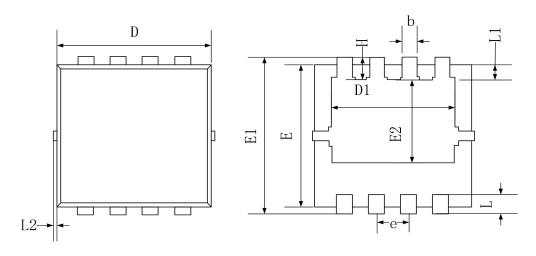
Figure 8: Safe Operating Area

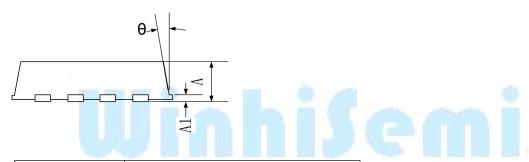






Mechanical Dimensions (PDFN3.3*3.3 Unit:mm)





SYMBOL	MILLIMETERS		
STWIBOL	MIN	MAX	
Α	0.70	0.90	
A1	0.10	0.25	
D	2.90	3.25	
D1	2.25	2.69	
E	2.90	3.20	
E1	3.00	3.60	
E2	1.35	2.20	
b	0.20	0.40	
е	0.65BSC		
L	0.30	0.50	
L1	0.13BSC		
L2	0.00	0.20	
Н	0.15	0.65	
θ	0° 14°		

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