

# VTTD065R17BNA

**Datasheet** 





### VTTD065R17BNA

### **General Description**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)_max</sub>	$I_D$
650V	$1.7\Omega@10V$	7A

# **Symbol**

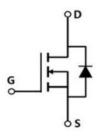


Figure 1 Symbol of VTTD065R17BNA

### **Features**

- Trench Technology Power MOSFET
- Low R<sub>DS(ON)</sub>
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested
- 100% ∆V<sub>DS</sub> Tested

# Package Type



# **Application**

■ Power Switching Application

TO-220-3L-F

Figure 2 Package Type of VTTD065R17BNA

# **Ordering Information**

Product Name	Package
VTTD065R17BNA	TO-220-3L-F



### VTTD065R17BNA

# Absolute Maximum Ratings (T<sub>A</sub>= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DSS</sub>	650	V
Gate-Source Voltage	V <sub>GSS</sub>	±30	V
Continuous Drain Current <sup>Note1</sup> $T_C=25$ °C	$I_D$	7	
Pulsed Drain Current Note2	$I_{DM}$	28	A
Avalanche Current <sup>Note3</sup>	I <sub>AS</sub>	17.5	
Single Pulsed Avalanche Energy <sup>Note3</sup>	Eas	76.5	mJ
Total Power Dissipation <sup>Note5</sup> $T_C=25$ °C	P <sub>D</sub>	50	W
Junction Temperature	TJ	150	°C
Storage Temperature	Tstg	-55 to 150	°C

### **Thermal Resistance**

Parameter	Symbol	Min	T <mark>y</mark> p	Max	Unit
Thermal Resistance, Junction-to-Ambient <sup>Note6</sup>	$R_{ heta JA}$		65		°C/W
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$		2.5		°C/W





#### VTTD065R17BNA

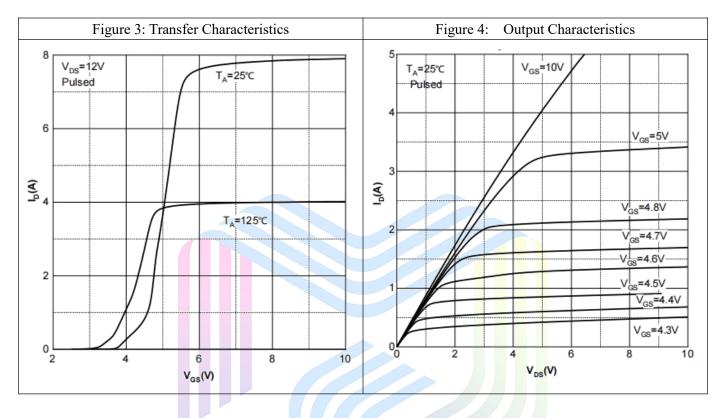
### **Electrical Characteristics** (T<sub>J</sub>= 25 °C, unless otherwise specified)

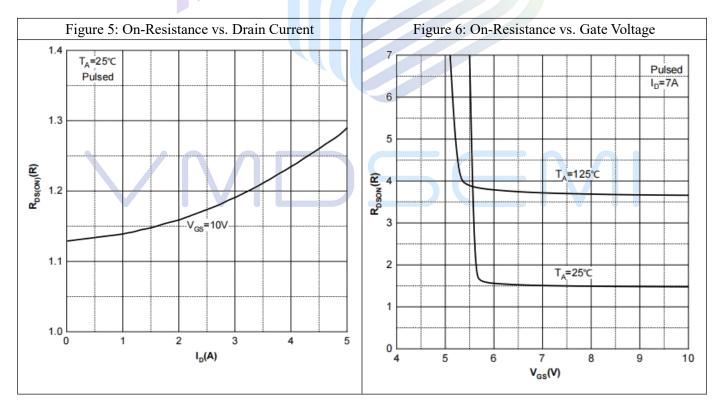
Parameter	Symbol	<b>Test Conditions</b>	Min	Тур	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_{D}=250uA$	650			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 650V, V_{GS} = 0V$			1	uA
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS} = \pm 30V, V_{DS} = 0V$			±100	nA
Gate Threshold Voltage <sup>Note4</sup>	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_D=250uA$	2	3.2	4	V
Static Drain-Source On-Resistance <sup>Note4</sup>	R <sub>DS(ON)</sub>	$V_{GS}=10V, I_{D}=1A$		1.2	1.7	Ω
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =45V		1170		pF
Output Capacitance	Coss	V <sub>GS</sub> =0V		63.9		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>	f=1MHz		2.6		pF
Total Gate Charge	Qg	V <sub>DS</sub> =335V		17.1		
Gate-Source Charge	$Q_{\mathrm{gs}}$	V <sub>GS</sub> =10V		0.6		nC
Gate-Drain Charge	Qgd	$I_D=1A$		3.6		
Gate Resistance	Rg	f = 1MHz, Open drain		2.1		Ω
Switching Parameters						
Turn-on Delay Time	t <sub>d(on)</sub>			33.6		
Turn-on Rise Time	$t_{\rm r}$	$V_{DD}=300V$		7.2		42.5
Turn-off Delay Time	$t_{\rm d(off)}$	$I_D=2A$		64		ns
Turn-off Fall Time	$t_{\mathrm{f}}$	$R_G=25\Omega$		31.2		
Diode Characteristics						
Diode Forward Voltage Note4	$V_{\mathrm{SD}}$	$V_{GS}=0V$ , $I_S=2A$			1.2	V

#### Notes:

- 1. The maximum current rating is limited by package. And device mounted on a large heatsink.
- 2. Pulse Test : Pulse Width  $\leq 10\mu s$ , duty cycle  $\leq 1\%$ .
- $3.E_{AS}$  condition:  $V_{DD} = 100V$ ,  $V_{GS} = 10V$ , L = 0.5mH,  $R_G = 25\Omega$  Starting  $T_J = 25$ °C.
- 4. Pulse Test : Pulse Width  $\leq 300 \mu s$ , duty cycle  $\leq 2\%$ .
- 5. The power dissipation  $P_D$  is limited by  $T_{J(MAX)} = 150$ °C. And device mounted on a large heatsink
- 6.Device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub> =25°C.

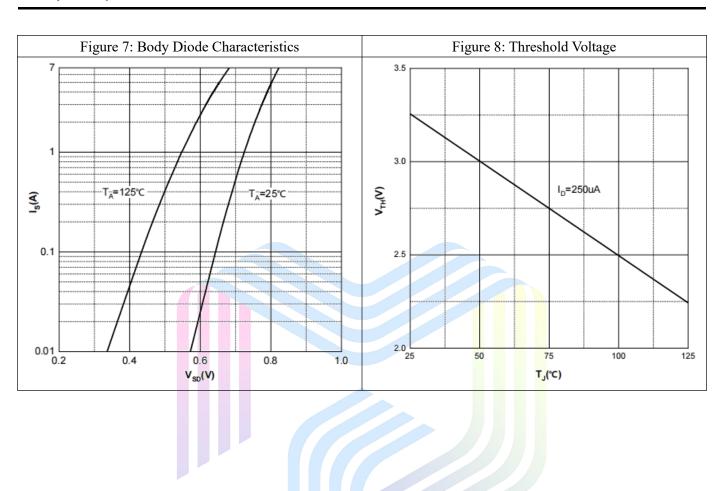
# **Typical Performance Characteristics**







### VTTD065R17BNA

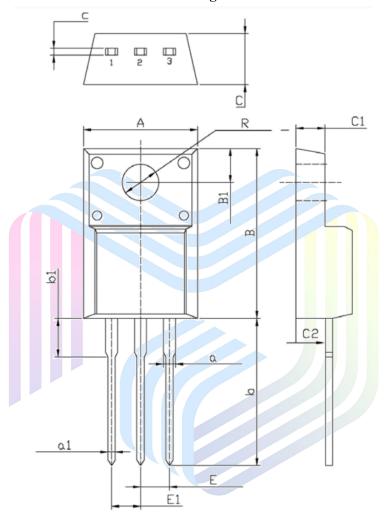






# **Mechanical Dimensions:**

**TO-220-3L-F Package Information** 



Cumbal	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
C	4.500	4.900	0.177	0.193	
С	0.400	0.600	0.016	0.024	
Α	9.960	10.360	0.392	0.408	
В	15.670	16.070	0.617	0.633	
B1	3.300	3.500	0.130	0.138	
R	3.080	3.280	0.121	0.129	
b	12.480	13.480	0.491	0.531	
b1	2.900	3.900	0.114	0.154	
а	1.080	1.480	0.043	0.058	
a1	0.700	0.900	0.028	0.035	
E	2.340	2.740	0.092	0.108	
E1	2.340	2.740	0.092	0.108	
C1	2.340	2.740	0.092	0.108	
C2	2.560	2.960	0.101	0.117	



#### VTTD065R17BNA

#### **NOTICE**

Hangzhou VMD Semiconductor Co., Ltd (VMD) reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to VMD's terms and conditions supplied at the time of order acknowledgement.

VMD, its affiliates, agents, and employees, and all persons acting on its or their behalf, disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

VMD disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify VMD's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

VMD warrants performance of its hardware products to the specifications at the time of sale, testing, reliability and quality control are used to the extent VMD deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

VMD does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using VMD's components. To minimize risk, customers must provide adequate design and operating safeguards.

VMD does not warrant or convey any license to any intellectual property rights either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in VMD's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice.

VMD is not responsible or liable for such altered documentation. Resale of VMD's products with statements different from or beyond the parameters stated by VMD for that product or service voids all express or implied warrantees for the associated VMD product or service and is an unfair and deceptive business practice.

All Rights Reserved.





# Via-Media Semiconductor Limited Company

### http://www.vmdsemi.com

#### **Main Sites:**

#### - Headquarters

Hangzhou Via-Media Semiconductor Co., LTD. 1305-1306, Building 71, No. 90, Wensan Road, Xihu District, Hangzhou, Zhejiang Province, P.R. China Tel: +86-0571-8515 0563

#### - Shanghai

Shanghai R&D Center. 1506~1508, Xinyin Building, 888 Yishan Road, Shanghai, P.R of China Tel: +86- 021-54201999

#### - Xi'an

Xi'an R&D Center 1703B, Building A, Greenland Center, Jinye Road, High-Tech Zone, Xi'an, Shaanxi, P.R of China

#### - Chengdu Office

Chengdu Winhi Semiconductor Co., LTD. Floor 15, Building 5, No. 171, Hele 2<sup>nd</sup> Street, Chengdu, Sichuan Province, P.R. China Tel: +86-028-8505 0771

#### Shenzhen

Shenzhen Sales Center.

17B, No.1 Phoenix Building, 2008 Shennan Road, Shenzhen, P.R of China
Tel: +86-0755- 82570682