VFPB010R048NA

Datasheet

General Description

$V_{(BR)DSS}$	$R_{DS(ON)\_max}$	I_D
100V	4.8mΩ@10V	125A

Symbol

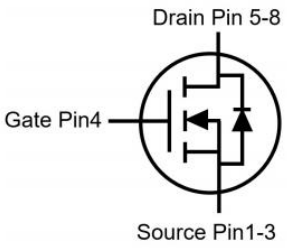
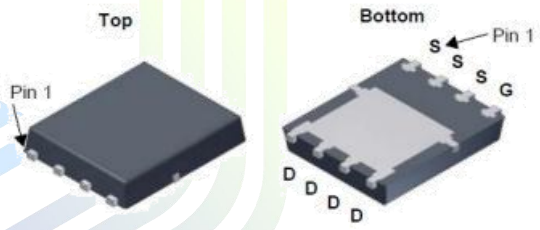


Figure 1 Symbol of VFPB010R048NA

Features

- Low $R_{DS(ON)}$
- Fast Switching and High efficiency
- 100% Avalanche Tested
- Pb-free lead plating;
- RoHS compliant

Package Type



PDFN5*6

Figure 2 Package Type of VFPB010R048NA

Application

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC converter
- Switched mode power supply

Ordering Information

Product Name	Package
VFPB010R048NA	PDFN5*6

Absolute Maximum Ratings (T_A= 25 ºC, unless otherwise specified)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current	T_C=25ºC	I_D	125	A
Continuous Drain Current	T_C=100ºC		79	A
Pulsed Drain Current[Note 2]	T_C=25ºC	$I_{D.pulse}$	500	A
Continuous Diode Forward Current	T_C=25ºC	I_S	125	A
Continuous Drain Current	T_A=25ºC	I_{DSM}	24	A
Continuous Drain Current	T_A=70ºC		19	A
Max Power Dissipation	T_C=25ºC	P_D	114	W
Max Power Dissipation[Note 3]	T_A=25ºC	P_{DSM}	4.2	W
Avalanche Energy, Single Pulse [Note 4]		E_{AS}	144	mJ
Operation and storage temperature		T_J,T_{STG}	-55 to 150	ºC

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{θJC}$		1.1	1.3	ºC/W
Thermal Resistance, Junction-to-Ambient	$R_{θJA}$		30	36	

Electrical Characteristics(T_J= 25 ºC, unless otherwise specified)

Parameter		Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics							
Drain-Source Breakdown Voltage		BV_{DSS}	V_{GS}=0V, I_D=250uA	100			V
Zero Gate Voltage Drain Current		I_{DSS}	V_{DS}=100V, V_{GS}=0V			1	uA
Zero Gate Voltage Drain Current T_J= 125 ºC			V_{DS}=100V, V_{GS}=0V			100	uA
Gate-Body Leakage Current	Forward	I_{GSSF}	V_{GS}=20V, V_{DS}=0V			100	nA
	Reverse	I_{GSSR}	V_{GS}=-20V, V_{DS}=0V			-100	
Gate Threshold Voltage		$V_{GS(TH)}$	V_{DS}=V_{GS}, I_D=250uA	2.3	3.3	3.9	V
Drain-Source On-Resistance[Note1]		$R_{DS(ON)}$	V_{GS}=10V, I_D=40A		3.8	4.8	mΩ
Drain-Source On-Resistance[Note1] T_J= 100 ºC					5.0		
Gate resistance		R_G	f=1 MHz, Open drain	0.5	1.1	1.7	Ω
Dynamic Characteristics							
Input Capacitance		C_{ISS}	V_{DS}=30V	3100	3645	4190	pF
Output Capacitance		C_{OSS}	V_{GS}=0V	1650	1940	2230	pF
Reverse Transfer Capacitance		C_{RSS}	f=1MHz	25	35	45	pF
Turn-on Delay Time		$t_{d(on)}$	V_{DS}=50V		15		ns
Rise Time		t_r	I_D=40A		39		
Turn-off Delay Time		$t_{d(off)}$	R_G=3Ω		27		
Fall Time		t_f	V_{GS}=10V		13		
Gate Charge Characteristics							
Gate to Source Charge		Q_{gs}	V_{GS}=10V		18	24	nC
Gate to Drain Charge		Q_{gd}	V_{DS}=50V		8.5	13	
Gate Charge Total@V_{GS}=10V		Q_g	I_D=40A		47	62	
Reverse Diode Characteristics							
Drain-Source Diode Forward Voltage		V_{SD}	V_{GS}=0V, I_{SD}=40A		0.8	1.2	V
Reverse Recovery Time		t_{rr}	I_{SD}=40A V_{GS}=0V		79	158	ns
Reverse Recovery Charge		Q_{rr}	di/dt=100A/us		118	236	nC

Notes:

1. Pulse width≤380μs; duty cycle≤ 2%.
2. Repetitive rating;pulse width limited by max junction temperature.
3. The power dissipation P_{DSM} is based on $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C.
4. Limited by T_{Jmax}, starting T_J = 25°C, L = 0.5mH, R_G = 25Ω, I_{AS} = 24A, V_{GS} =10V.

Typical Performance Characteristics

Figure 3: Typ. Output Characteristics

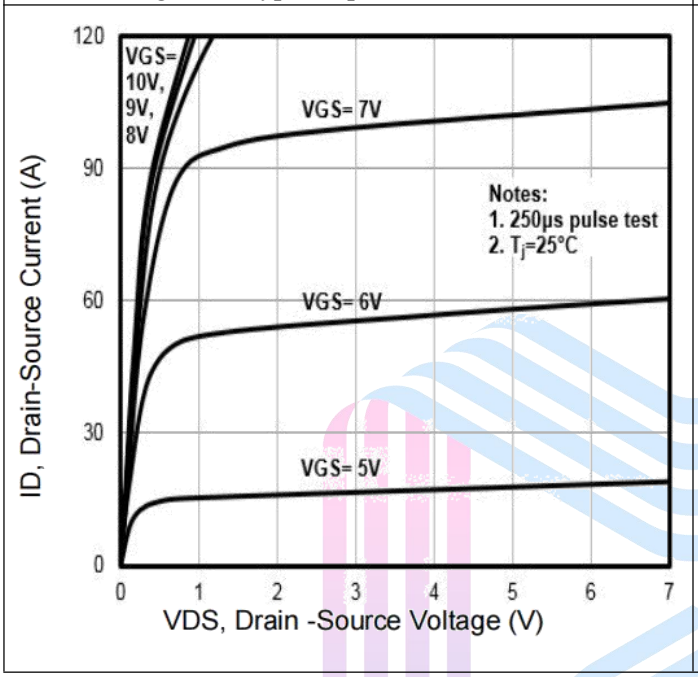


Figure 4: Typ. Gate Charge

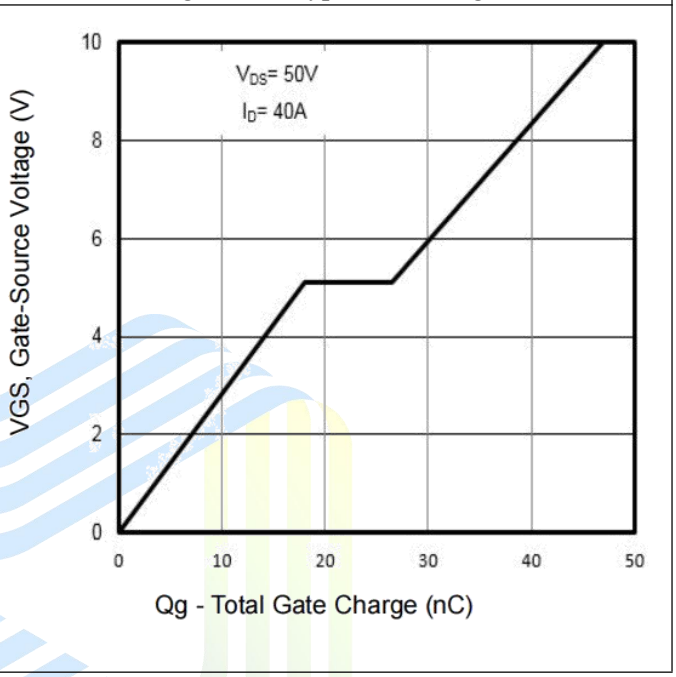


Figure 5: Normalized On-State Resistance

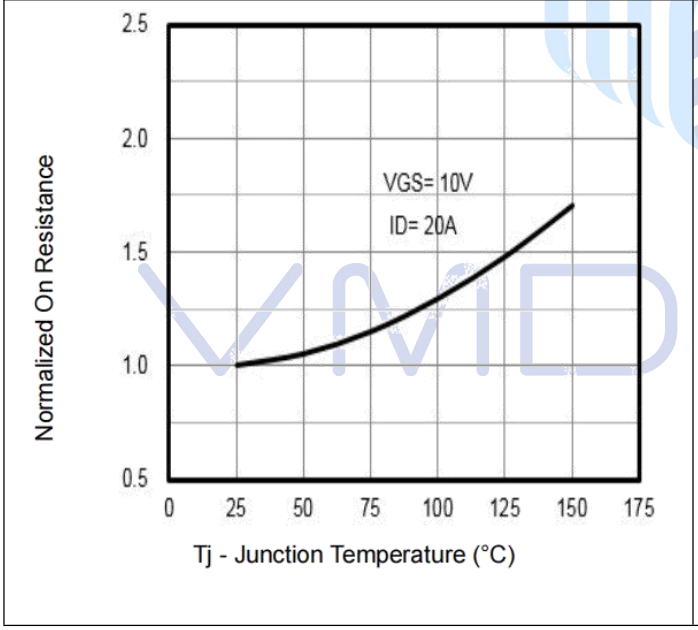


Figure 6: Typ. Transfer Characteristics

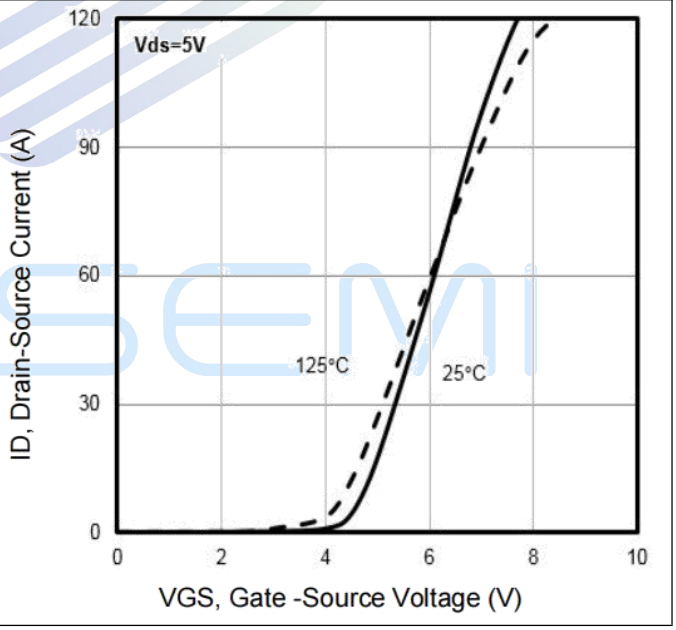


Figure 7: Typ. Capacitances

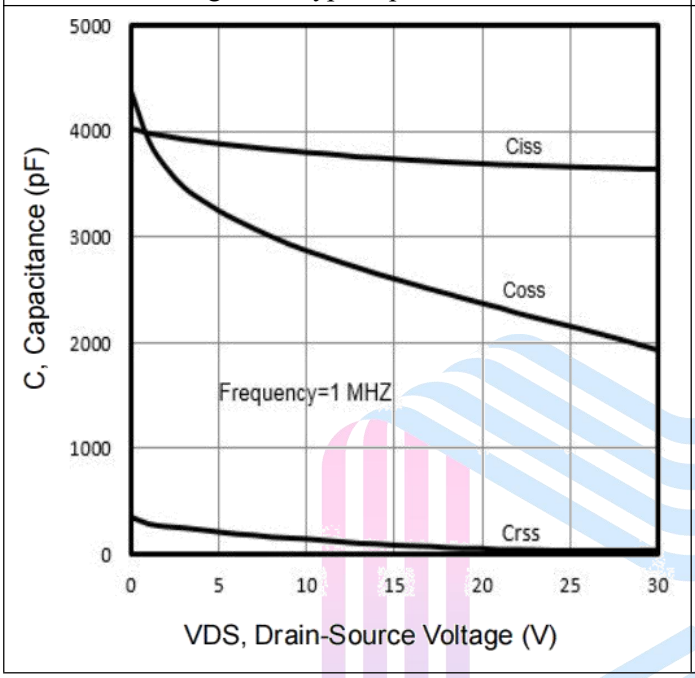


Figure 8: Forward Characteristics of Body Diode

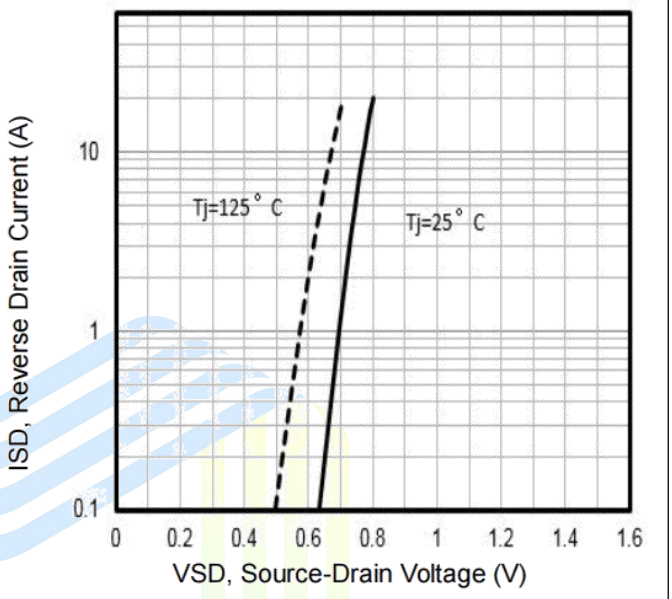


Figure 9: Gate-Source Threshold Voltage

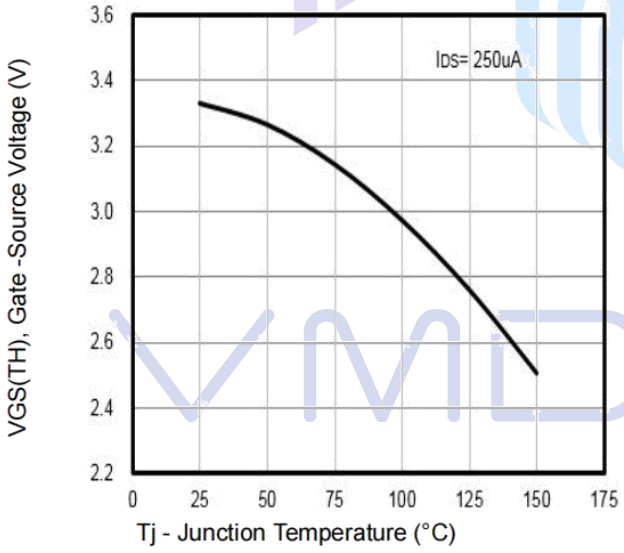


Figure 10: Safe Operating Area

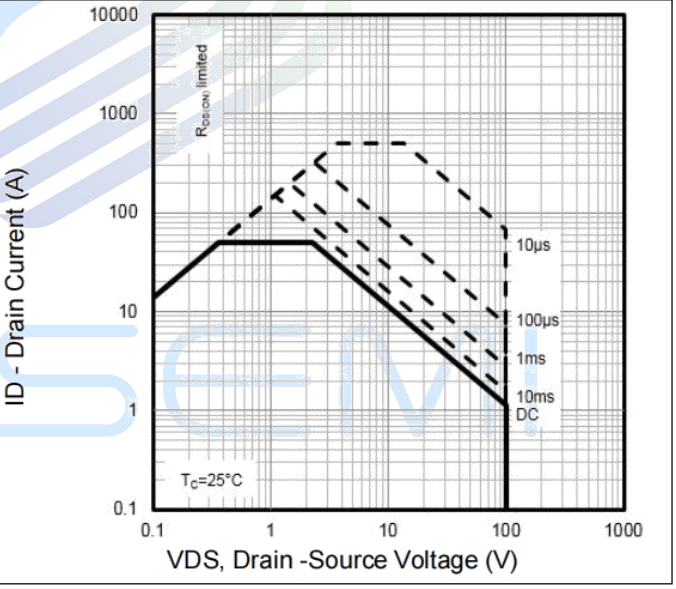
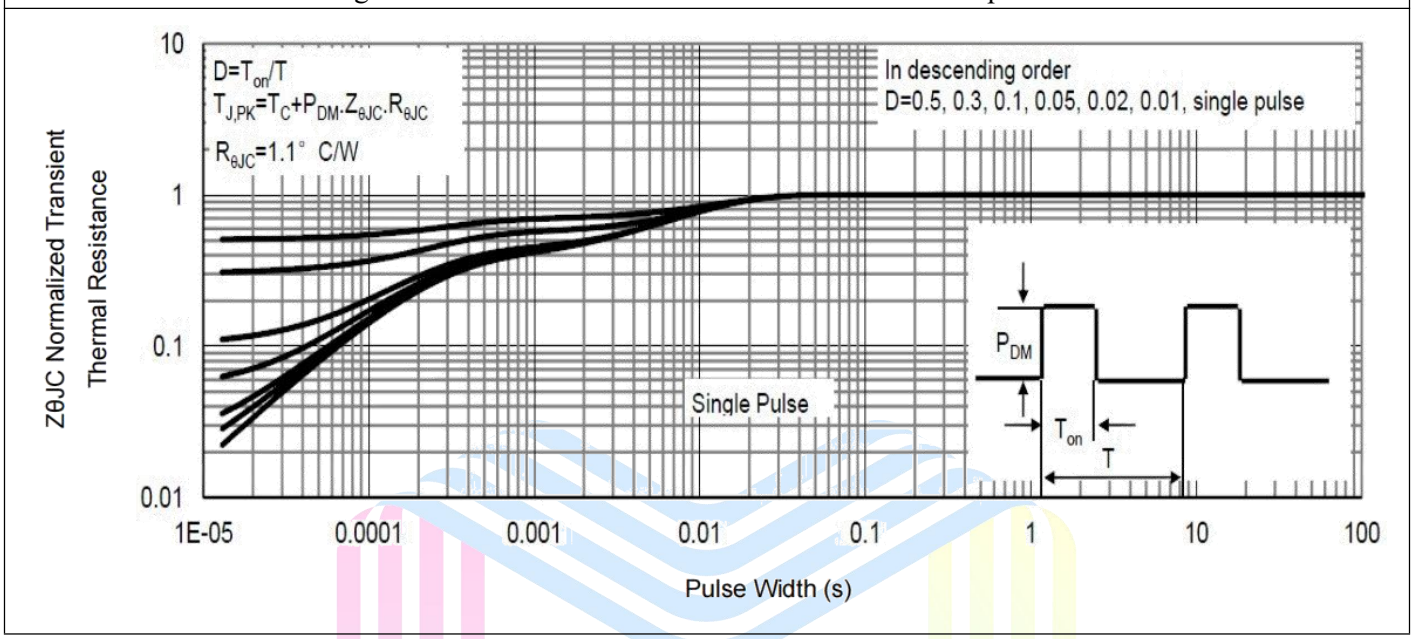
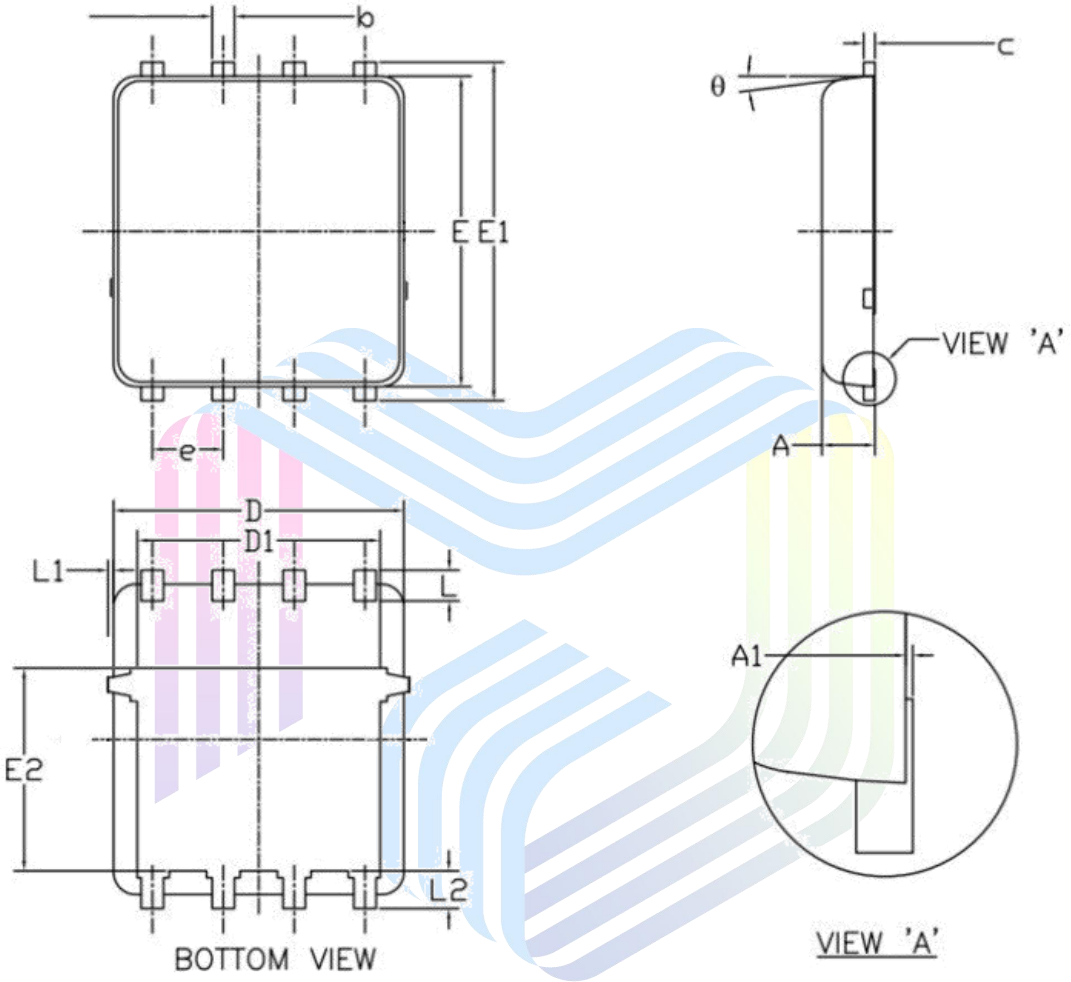


Figure 11: Normalized Maximum Transient Thermal Impedance



Mechanical Dimensions

Package Information PDFN5*6



Symbol	DIMENSIONS (unit : mm)		
	Min	Typ	Max
A	0.90	1.00	1.20
A1	0.00	--	0.05
b	0.30	0.40	0.51
c	0.20	0.25	0.33
D	4.80	4.90	5.40
D1	3.61	4.00	4.25
E	5.65	5.80	6.06
E1	5.90	6.10	6.35
E2	3.38	3.58	3.92
e	1.27 BSC		
L	0.51	0.61	0.71
L1	--	--	0.15
L2	0.41	0.51	0.61
θ	0°	--	12°

Notes:

1. Refer to JEDEC MO-240 variation AA.
2. Dimensions "D" and "E" do NOT include mold flash protrusions or gate burrs.
3. Dimensions "D" and "E" include interterminal flash or protrusion. Interterminal flash or protrusion shall not exceed 0.25mm per side.

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Via-Media Semiconductor Limited Company

http://www.vmdsemi.com

Main Sites:

- **Headquarters**

Hangzhou Via-Media Semiconductor Co., LTD.
1305-1306, Building 71, No. 90, Wensan Road, Xihu District, Hangzhou, Zhejiang Province, P.R. China
Tel: +86-0571-8515 0563

- **Chengdu Office**

Chengdu Winhi Semiconductor Co., LTD.
Floor 15, Building 5, No. 171, Hele 2nd Street, Chengdu, Sichuan Province, P.R. China
Tel: +86-028-8505 0771

- **Shanghai**

Shanghai R&D Center.
1506~1508, Xinyin Building, 888 Yishan Road, Shanghai, P.R of China
Tel: +86-021-54201999

- **Shenzhen**

Shenzhen Sales office
Room 4A15, Block AB, Tianxiang Building, Chegongmiao , Futian District, Shenzhen, P.R of China
Tel: +86-0755-82570682

- **Xi'an**

Xi'an R&D Center
1703B, Building A, Greenland Center, Jinye Road, High-Tech Zone, Xi'an, Shaanxi, P.R of China