



VMDSEMI

VFPB010R060NA

Datasheet



VMDSEMI

General Description

$V_{(BR)DSS}$	$R_{DS(ON)_{max}}$	I_D
100V	6.0mΩ@10V	130A

Symbol

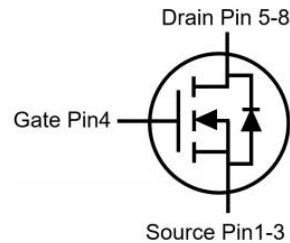
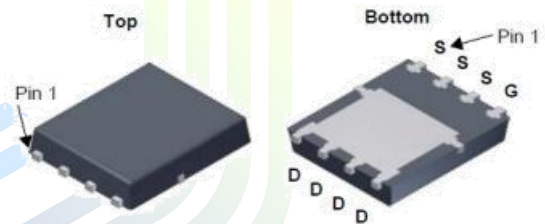


Figure 1 Symbol of VFPB010R060NA

Features

- Low $R_{DS(ON)}$
- Fast Switching and High efficiency
- 100% Avalanche Tested
- Pb-free lead plating;
- RoHS compliant

Package Type



PDFN5*6

Figure 2 Package Type of VFPB010R060NA

Application

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC converter
- Switched mode power supply

Ordering Information

Product Name	Package
VFPB010R060NA	PDFN5*6

Absolute Maximum Ratings ($T_A=25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DSS}	100	V
Gate-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current	I_D	$T_C=25^\circ\text{C}$ 130	A
Continuous Drain Current		$T_C=100^\circ\text{C}$ 82	A
Pulsed Drain Current ^{Note 2}	$I_{D,pulse}$	$T_C=25^\circ\text{C}$ 520	A
Continuous Diode Forward Current	I_S	$T_C=25^\circ\text{C}$ 130	A
Continuous Drain Current	I_{DSM}	$T_A=25^\circ\text{C}$ 24	A
Continuous Drain Current		$T_A=70^\circ\text{C}$ 19	A
Max Power Dissipation	P_D	$T_C=25^\circ\text{C}$ 125	W
Max Power Dissipation		$T_C=100^\circ\text{C}$ 50	
Max Power Dissipation ^{Note 3}	P_{DSM}	$T_A=25^\circ\text{C}$ 4.2	
Max Power Dissipation ^{Note 3}		$T_A=70^\circ\text{C}$ 2.7	
Avalanche Energy, Single Pulse ^{Note 4}	E_{AS}	281	mJ
Operation and storage temperature	T_I, T_{STG}	-55 to 150	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$		1.0	1.2	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$		30	36	

Electrical Characteristics($T_J = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	100			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$			1	μA
Zero Gate Voltage Drain Current $T_J=125\text{ }^\circ\text{C}$		$V_{DS}=100V, V_{GS}=0V$			100	μA
Gate-Body Leakage Current	Forward	$I_{GSSF}, V_{GS}=20V, V_{DS}=0V$			100	nA
	Reverse	$I_{GSSR}, V_{GS}=-20V, V_{DS}=0V$			-100	
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.4	2.8	3.9	V
Drain-Source On-Resistance ^{Note1}	$R_{DS(ON)}$	$V_{GS}=10V, I_D=40A$		4.3	6	mΩ
Drain-Source On-Resistance ^{Note1} $T_J=100\text{ }^\circ\text{C}$				5.5		
Gate resistance	R_G	$f=1\text{ MHz, Open drain}$	0.2	0.7	3	Ω
Dynamic Characteristics						
Input Capacitance	C_{ISS}	$V_{DS}=30V$	2670	3560	4735	pF
Output Capacitance	C_{OSS}	$V_{GS}=0V$	1280	1705	2270	
Reverse Transfer Capacitance	C_{RSS}	$f=1\text{ MHz}$	25	35	50	
Turn-on Delay Time	$t_{d(on)}$	$V_{DS}=50V$ $I_D=40A$ $R_G=3\Omega$ $V_{GS}=10V$		15		ns
Rise Time	t_r			31		
Turn-off Delay Time	$t_{d(off)}$			30		
Fall Time	t_f			14		
Gate Charge Characteristics						
Gate to Source Charge	Q_{gs}	$V_{GS}=10V$		17	23	nC
Gate to Drain Charge	Q_{gd}	$V_{DS}=50V$		10	15	
Gate Charge Total@ $V_{GS}=10V$	Q_g	$I_D=40A$		52	69	
Reverse Diode Characteristics						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=40A$		0.8	1.2	V
Reverse Recovery Time	t_{rr}	$I_{SD}=40A, V_{GS}=0V$ $di/dt=100A/\mu s$		55	110	ns
Reverse Recovery Charge	Q_{rr}				62	124

Notes:

1. Pulse width $\leq 380\mu s$; duty cycle $\leq 2\%$.
2. Repetitive rating; pulse width limited by max junction temperature.
3. The power dissipation P_{DSM} is based on $R_{\theta JA}$ and the maximum allowed junction temperature of $150\text{ }^\circ\text{C}$.
4. Limited by T_{Jmax} , starting $T_J = 25\text{ }^\circ\text{C}$, $L = 0.1\text{ mH}$, $R_G = 25\Omega$, $I_{AS} = 75A$, $V_{GS} = 10V$.

Typical Performance Characteristics

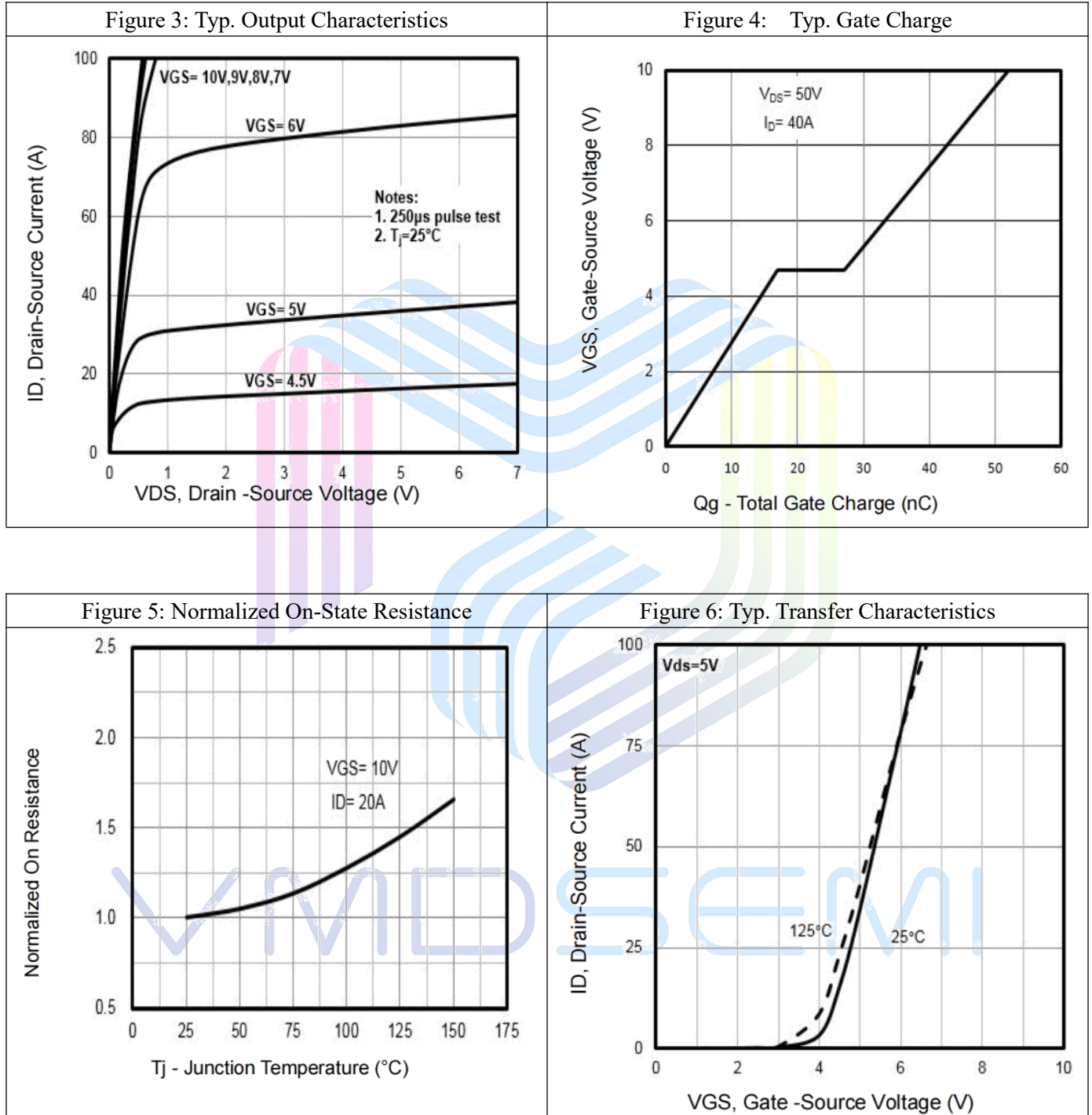


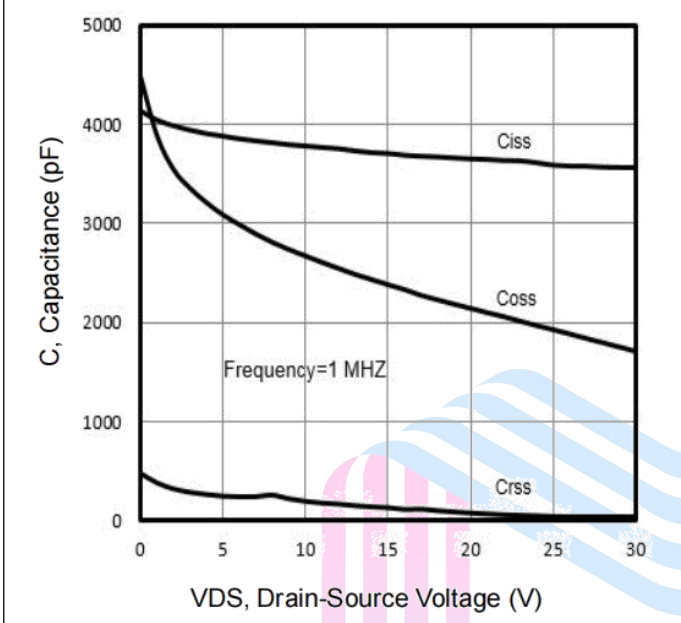
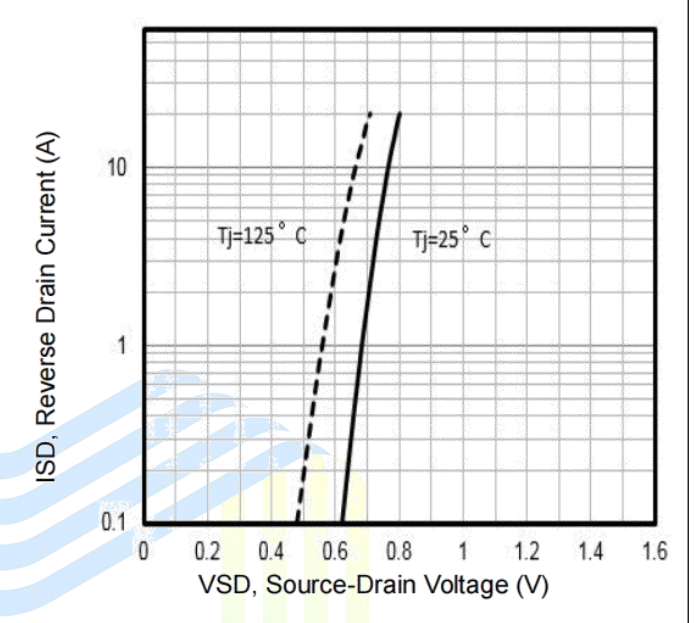
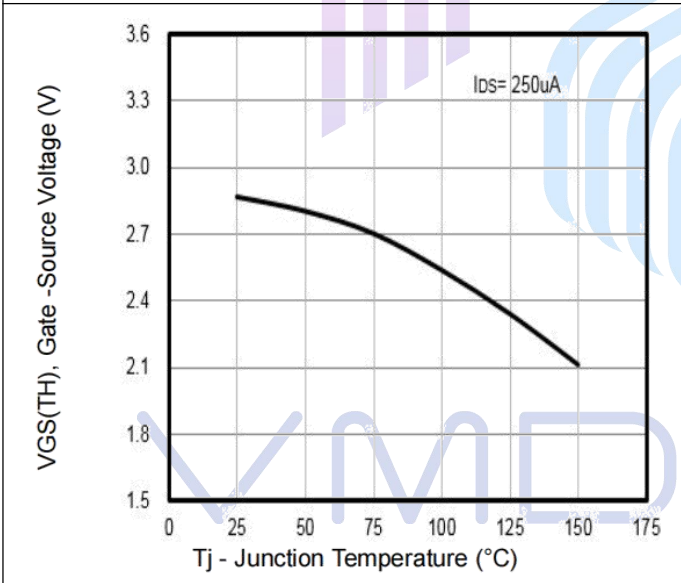
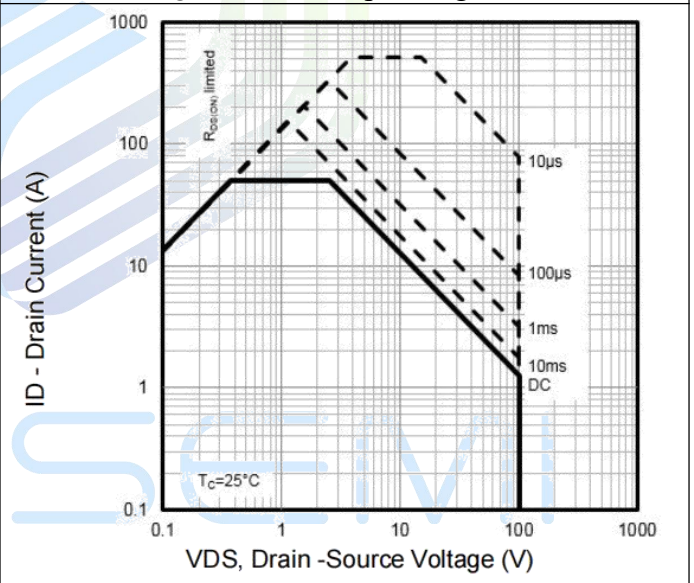
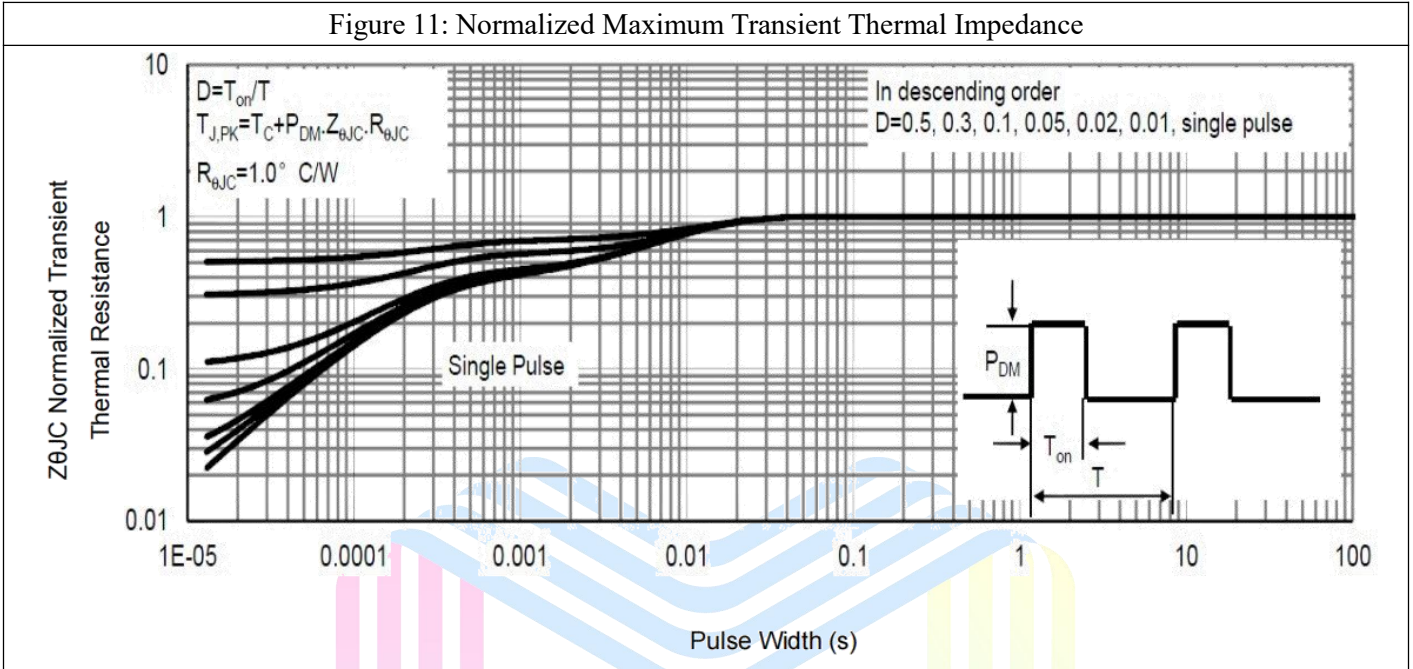
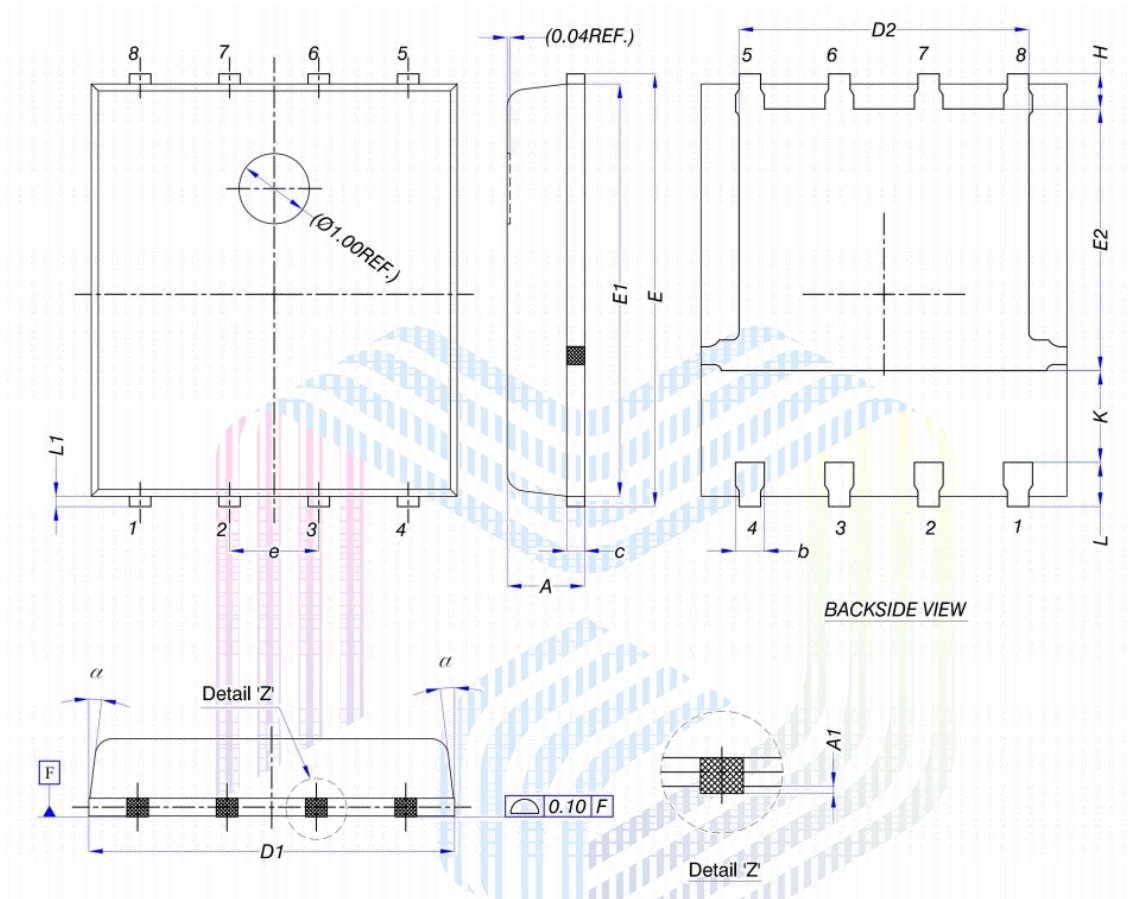
Figure 7: Typ. Capacitances

Figure 8: Forward Characteristics of Body Diode

Figure 9: Gate-Source Threshold Voltage

Figure 10: Safe Operating Area


Figure 11: Normalized Maximum Transient Thermal Impedance



Mechanical Dimensions

Package Information PDFN5*6



Symbol	DIMENSIONS (unit : mm)		
	Min	Typ	Max
A	1.00	1.10	1.20
A1	0.00	--	0.05
b	0.30	0.40	0.50
c	0.20	0.25	0.30
D1	5.00	5.20	5.40
D2	3.80	4.10	4.25
E	5.95	6.15	6.35
E1	5.66	5.86	6.06
E2	3.52	3.72	3.92
e	1.27 BSC		
H	0.40	0.50	0.60
K	1.10	--	--
L	0.50	0.60	0.70
L1	0.08	0.15	0.22
α	0°	--	12°

Notes:

1. Refer to JEDEC MO-240 variation AA.
2. Dimensions "D1" and "E1" do NOT include mold flash protrusions or gate burrs.
3. Dimensions "D1" and "E1" include interterminal flash or protrusion. Interterminal flash or protrusion shall not exceed 0.25mm per side.

NOTICE

Hangzhou VMD Semiconductor Co., Ltd (VMD) reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to VMD's terms and conditions supplied at the time of order acknowledgement.

VMD, its affiliates, agents, and employees, and all persons acting on its or their behalf, disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product. VMD disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify VMD's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

VMD warrants performance of its hardware products to the specifications at the time of sale, testing, reliability and quality control are used to the extent VMD deems necessary to support this warranty. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

VMD does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using VMD's components. To minimize risk, customers must provide adequate design and operating safeguards.

VMD does not warrant or convey any license to any intellectual property rights either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in VMD's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice.

VMD is not responsible or liable for such altered documentation. Resale of VMD's products with statements different from or beyond the parameters stated by VMD for that product or service voids all express or implied warranties for the associated VMD product or service and is an unfair and deceptive business practice.

All Rights Reserved.





Via-Media Semiconductor Limited Company

<http://www.vmdsemi.com>

Main Sites:

- Headquarters

Hangzhou Via-Media Semiconductor Co., LTD.
1305-1306, Building 71, No. 90, Wensan Road, Xihu
District, Hangzhou, Zhejiang Province, P.R. China
Tel: +86-0571-8515 0563

- Chengdu Office

Chengdu Winhi Semiconductor Co., LTD.
Floor 15, Building 5, No. 171, Hele 2nd Street,
Chengdu, Sichuan Province, P.R. China
Tel: +86-028-8505 0771

- Shanghai

Shanghai R&D Center.
1506~1508, Xinyin Building, 888 Yishan Road,
Shanghai, P.R of China
Tel: +86-021-54201999

- Shenzhen

Shenzhen Sales office
Room 4A15, Block AB, Tianxiang Building,
Chengongmiao, Futian District, Shenzhen, P.R of China
Tel: +86-0755-82570682

- Xi'an

Xi'an R&D Center
1703B, Building A, Greenland Center, Jinye Road,
High-Tech Zone, Xi'an, Shaanxi, P.R of China