

VFTL010R077NA

Datasheet





VFTL010R077NA

General Description

V _{(BR)DSS}	$R_{DS(ON)_max}$	I_D
100V	7.7mΩ@10V	80A

Symbol

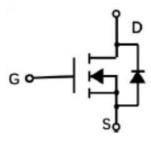
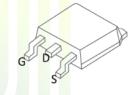


Figure 1 Symbol of VFTL010R077NA

Features

- Split Gate Trench Technology
- \blacksquare Low $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

Package Type



Application

- Industrial Power Supply
- Load Switch

TO-252-2L

Figure 2 Package Type of VFTL010R077NA

Ordering Information





VFTL010R077NA

Absolute Maximum Ratings (T_A= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DSS}	100	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current ^{Note1} $T_C=25$ °C	I_D	80	
Pulsed Drain Current Note2	I_{DM}	320	A
Avalanche Current ^{Note3}	I _{AS}	39	
Single Pulsed Avalanche Energy ^{Note3}	Eas	380	mJ
Total Power Dissipation ^{Note5} $T_C=25$ °C	P _D	89	W
Junction Temperature	TJ	150	°C
Storage Temperature	Tstg	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	M in	Typ	Max	Unit
Thermal Resistance, Junction-to-Ambient Note6	$R_{ heta JA}$		50		°C/W
Thermal Resistance, Junction-to-Case	$R_{ heta m JC}$		1.4		°C/W





VFTL010R077NA

Electrical Characteristics (T_J= 25 °C, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_{D}=250uA$	100			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 80V, V_{GS} = 0V$			1	uA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA
Gate Threshold Voltage ^{Note4}	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_D=250uA$	1	2	3	V
Static Drain-Source On-Resistance ^{Note4}	R _{DS(ON)}	$V_{GS}=10V, I_{D}=20A$		6.7	7.7	$m\Omega$
Forward Transconductance ^{Note4}	gfs	$V_{DS}=10V, I_{D}=10A$		29		S
Dynamic Characteristics						
Input Capacitance	C _{ISS}	V _{DS} =50V		2915		pF
Output Capacitance	Coss	$V_{GS}=0V$		370		pF
Reverse Transfer Capacitance	C _{RSS}	f=1MHz		16		pF
Total Gate Charge	Q_{g}	V _{DS} =50V		55		
Gate-Source Charge	Q_{gs}	V _{GS} =10V		18		nC
Gate-Drain Charge	Q_{gd}	$I_D=45A$		14		
Gate Resistance	Rg	f = 1MHz, Open drain		1.2		Ω
Switching Parameters						
Turn-on Delay Time	t _{d(on)}	$V_{DD} = 50V$		15		
Turn-on Rise Time	$t_{\rm r}$	$V_{GS}=10V$		10		ng
Turn-off Delay Time	$t_{d(off)}$	$R_L=1.2\Omega$		32		ns
Turn-off Fall Time	t_{f}	$R_G=3\Omega$		10		
Diode Characteristics						
Diode Forward Voltage Note4	V_{SD}	$V_{GS}=0V, I_{S}=10A$			1.2	V

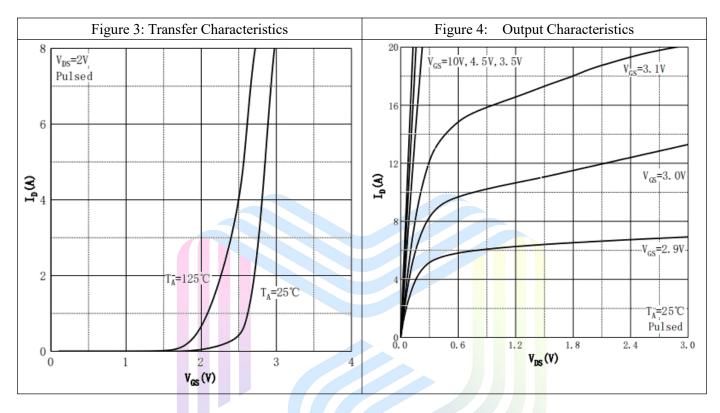
Notes:

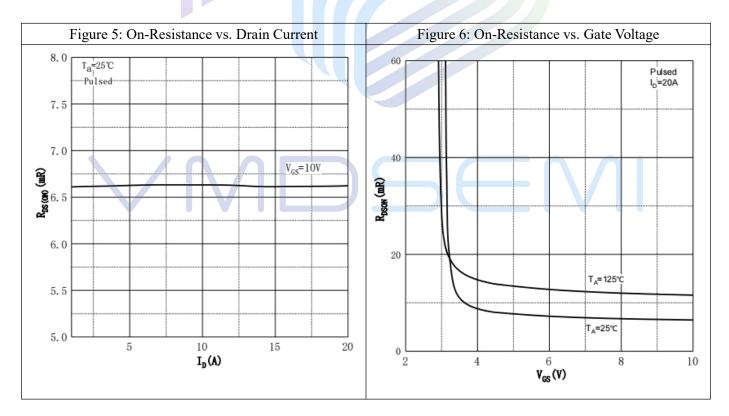
- 1. The maximum current rating is limited by package. And device mounted on a large heatsink.
- 2. Pulse Test : Pulse Width $\leq 10\mu s$, duty cycle $\leq 1\%$.
- 3.E_{AS} condition: $V_{DD} = 50V$, $V_{GS} = 10V$, L = 0.5mH, $R_G = 25\Omega$ Starting $T_J = 25$ °C.

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- 4. Pulse Test : Pulse Width $\leq 300 \mu s$, duty cycle $\leq 2\%$.
- 5. The power dissipation P_D is limited by $T_{J(MAX)} = 150$ °C. And device mounted on a large heatsink
- 6.Device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.

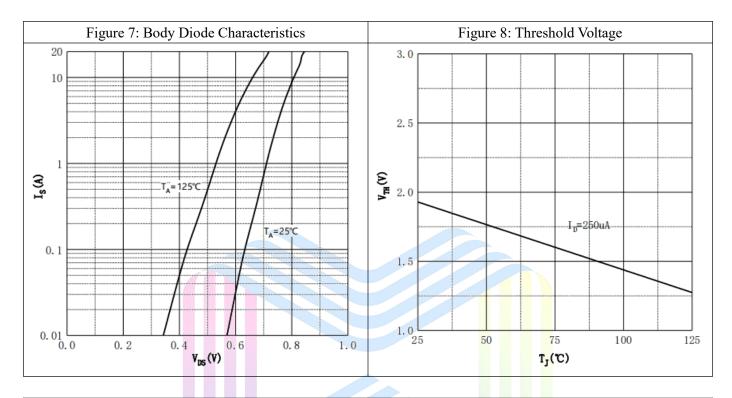
Typical Performance Characteristics

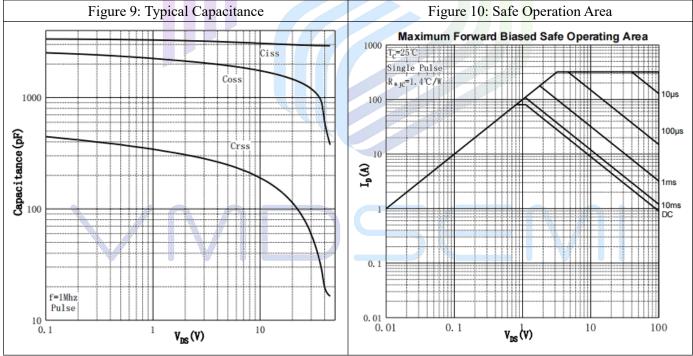






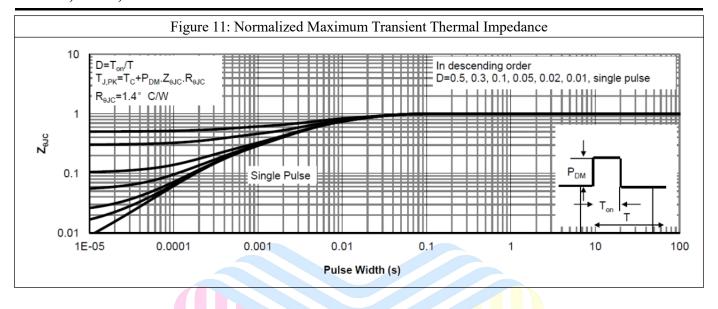
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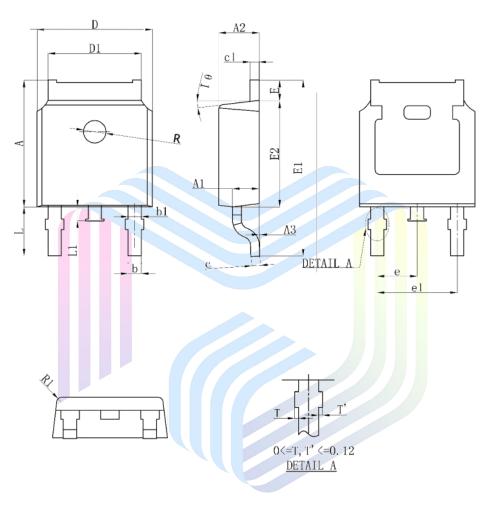






Mechanical Dimensions:

TO-252-2L Package Information



Cumbal	Dimensions In Millimeters		Dimensions In Inches			
Symbol	Min.	Max.	Min.	Max.		
Α	7.050	7.150	0.278	0.281		
A1	0.960	1.060	0.038	0.042		
A2	2.200	2.400	0.087	0.094		
A3	0.000	0.100	0.000	0.004		
b	0.760	REF	0.03	OREF		
b1	1.000	REF	0.039REF			
С	0.508	BREF	0.020REF			
c1	0.508	BREF	0.020REF			
D	6.550	6.650	0.258	0.262		
D1	5.100	5.460	0.201	0.215		
E	0.950	1.050	0.037	0.041		
E1	9.700	10.400	0.382	0.409		
E2	6.000	6.200	0.236	0.244		
е	2.286BSC		0.090BSC			
e1	4.572REF		0.180REF			
L	2.650	2.950	0.104	0.116		
L1	0.700	0.900	0.028	0.035		
θ1	7°REF		7°REF			
R	1.300REF		0.051REF			
R1	0.250	REF	0.010REF			

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