



VMDSEMI

VFTA015R074NA

Datasheet

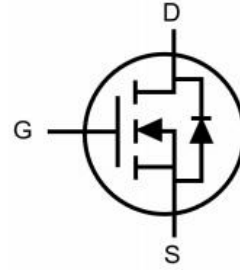


VMDSEMI

General Description

Symbol

$V_{(BR)DSS}$	$R_{DS(ON)_{max}}$	I_D
150V	7.4mΩ@10V	195A



Symbol of VFTA015R074NA

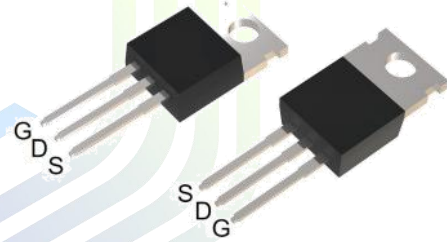
Features

- Low $R_{DS(ON)}$
- 100% Avalanche Tested
- Enhancement mode
- Low switching losses

Application

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC converter
- Switched mode power supply

Package Type



TO-220AB

Package Type of VFTA015R074NA

VMDSEMI

Ordering Information

Product Name	Package
VFTA015R074NA	TO-220AB

Absolute Maximum Ratings ($T_A=25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DSS}	150	V
Gate-Source Voltage	V_{GSS}	± 25	V
Continuous Drain Current (Wire bond limited) $T_C=25^\circ\text{C}$	I_D	195	A
Continuous Drain Current (Silicon limited) $T_C=100^\circ\text{C}$		138	
Continuous Drain Current (Silicon limited) $T_C=25^\circ\text{C}$		195	
Pulsed Drain Current ^{Note 1} $T_C=25^\circ\text{C}$	$I_{D,pulse}$	450	A
Diode Forward Current $T_C=25^\circ\text{C}$	I_S	195	A
Continuous Drain Current $T_A=25^\circ\text{C}$	I_{DSM}	12	A
Continuous Drain Current $T_A=70^\circ\text{C}$		10	A
Max Power Dissipation $T_C=25^\circ\text{C}$	P_D	577	W
Max Power Dissipation ^{Note 3} $T_A=25^\circ\text{C}$	P_{DSM}	2.2	W
Avalanche Energy, Single Pulse ^{Note 2}	E_{AS}	306	mJ
Operation and storage temperature	T_I, T_{STG}	-55 to 175	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	-	0.22	0.26	$^\circ\text{C/W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	-	48	58	



Electrical Characteristics($T_J = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	150	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=150V, V_{GS}=0V$	-	-	1	μA
Zero Gate Voltage Drain Current $T_J = 125\text{ }^\circ\text{C}$		$V_{DS}=150V, V_{GS}=0V$	-	-	100	μA
Gate-Body Leakage Current	Forward	$I_{GSSF}, V_{GS}=25V, V_{DS}=0V$	-	-	100	nA
	Reverse	$I_{GSSR}, V_{GS}=-25V, V_{DS}=0V$	-	-	-100	
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.6	3.1	3.6	V
Drain-Source On-Resistance ^{Note4}	$R_{DS(ON)}$	$V_{GS}=10V, I_D=80A$	-	5.7	7.4	m Ω
Drain-Source On-Resistance ^{Note4} $T_J = 100\text{ }^\circ\text{C}$			-	7.6	-	
Gate resistance	R_G	$f=1\text{ MHz, Open drain}$	0.2	1.9	5	Ω
Dynamic Characteristics						
Input Capacitance	C_{ISS}	$V_{DS}=75V$	4450	8895	15565	pF
Output Capacitance	C_{OSS}	$V_{GS}=0V$	310	625	1090	pF
Reverse Transfer Capacitance	C_{RSS}	$f=1\text{ MHz}$	5	15	30	pF
Turn-on Delay Time	$t_{d(on)}$	$V_{DS}=75V$	-	29	-	ns
Rise Time	t_r	$I_D=80A$	-	107	-	
Turn-off Delay Time	$t_{d(off)}$	$R_G=3.9\Omega$	-	66	-	
Fall Time	t_f	$V_{GS}=10V$	-	110	-	
Gate Charge Characteristics						
Gate to Source Charge	Q_{gs}	$V_{GS}=10V$	-	40	70	nC
Gate to Drain Charge	Q_{gd}	$V_{DS}=75V$	-	26	46	
Gate Charge Total	Q_g	$I_D=80A$	-	120	210	
Reverse Diode Characteristics						
Drain-Source Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_{SD}=80A$	-	0.9	1.2	V
Reverse Recovery Time	t_{rr}	$I_{SD}=80A, V_{GS}=0V$	-	127	254	ns
Reverse Recovery Charge	Q_{rr}	$di/dt=100A/\mu s$	-	470	940	nC

Notes:

- Single pulse; pulse width $\leq 100\mu s$.
- EAS is based on starting $T_J = 25\text{ }^\circ\text{C}$, $L = 0.5\text{ mH}$, $I_{AS}=35A$, $R_G = 25\Omega$, $V_{GS} = 10V$;
- The power dissipation P_{dsm} is based on $T_J = 150\text{ }^\circ\text{C}$, using junction-to-ambient thermal resistance $R_{\theta JA}$.
- Pulse width $\leq 380\mu s$; duty cycle $\leq 2\%$.

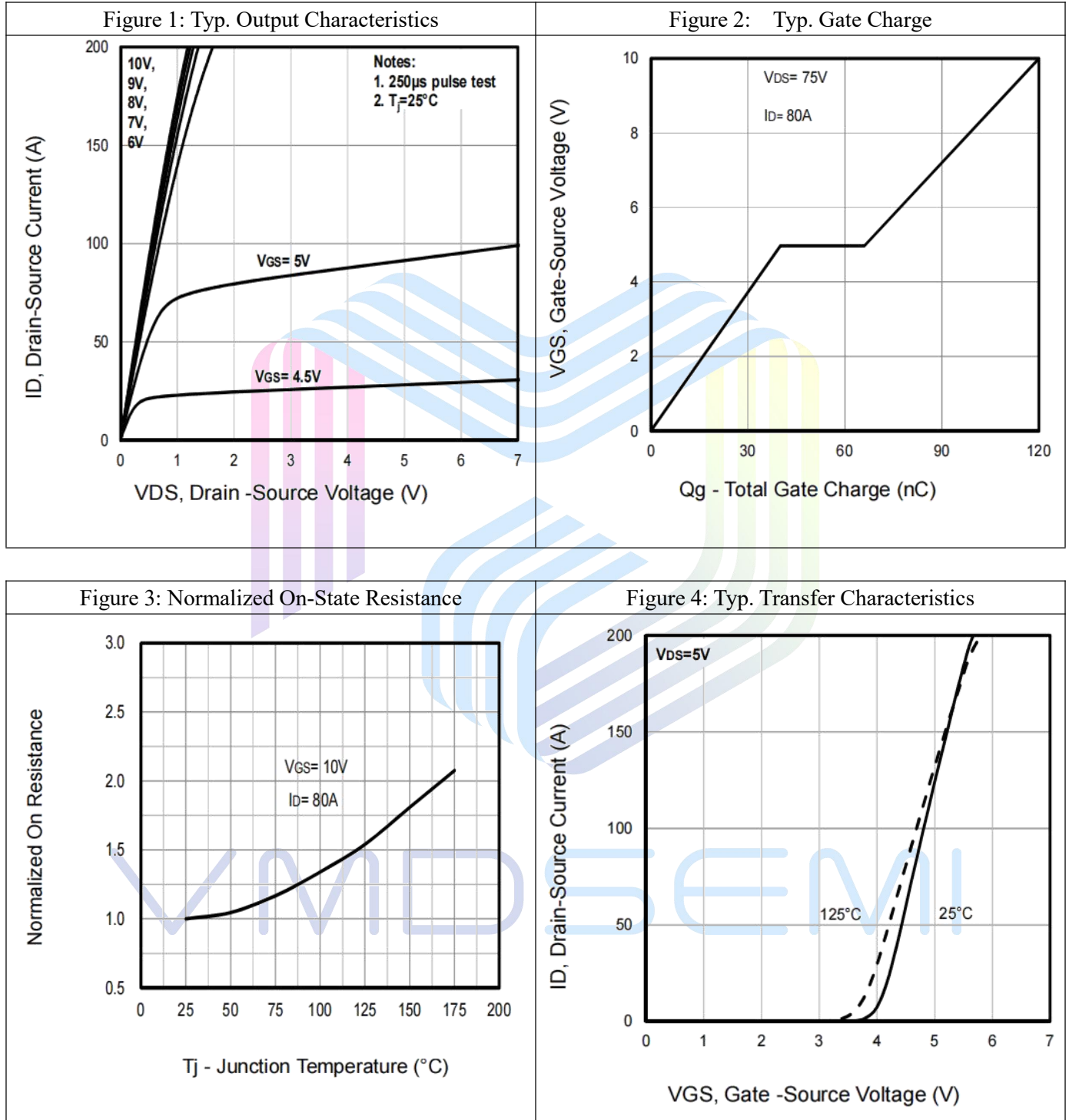
Typical Performance Characteristics


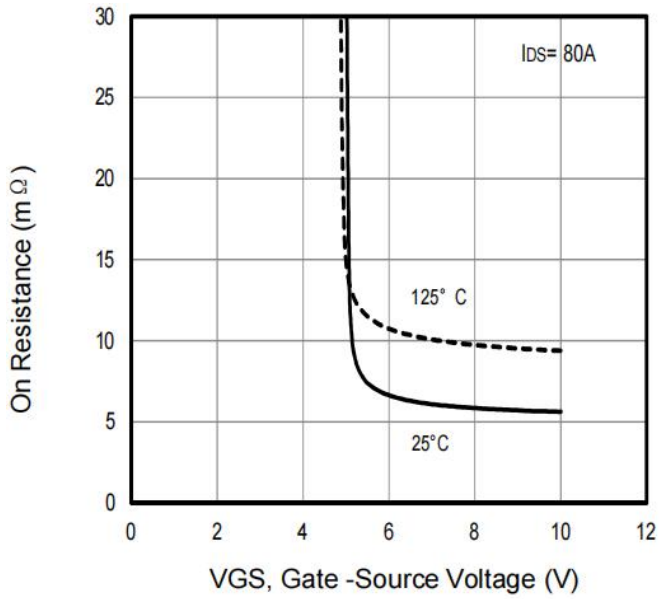
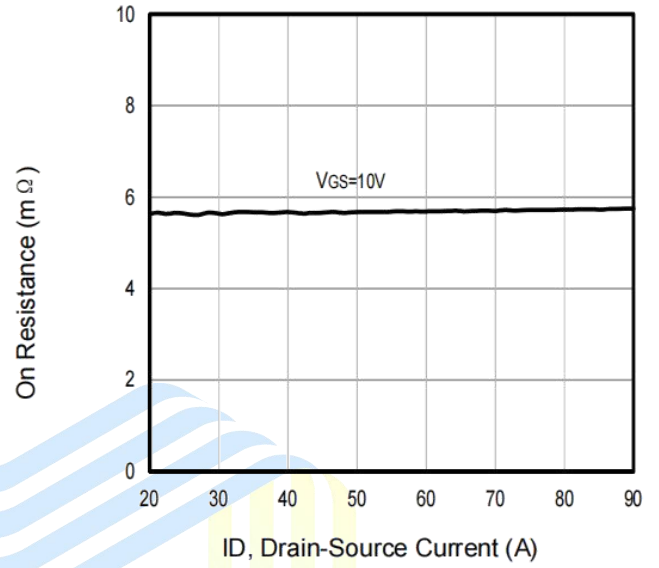
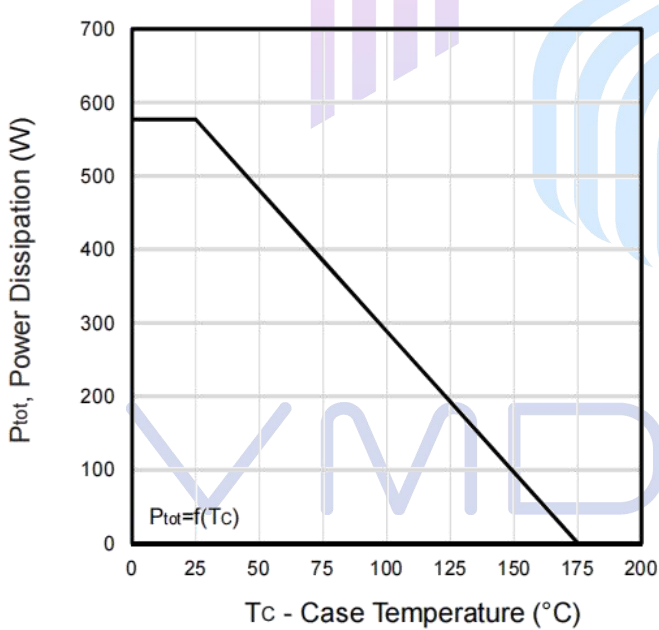
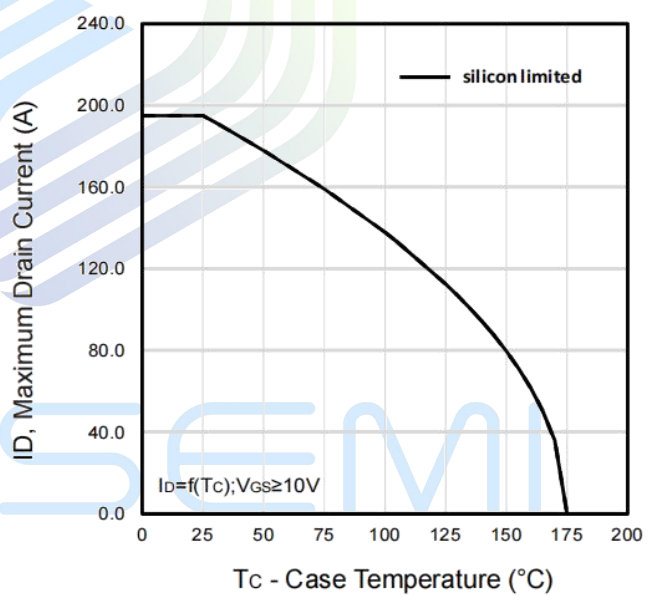
Figure 5: Typical On Resistance vs V_{GS}

Figure 6: Typical On Resistance vs I_D and V_{GS}

Figure 7: Power Dissipation Vs. Case Temperature

Figure 8: Drain Current Vs. Case Temperature


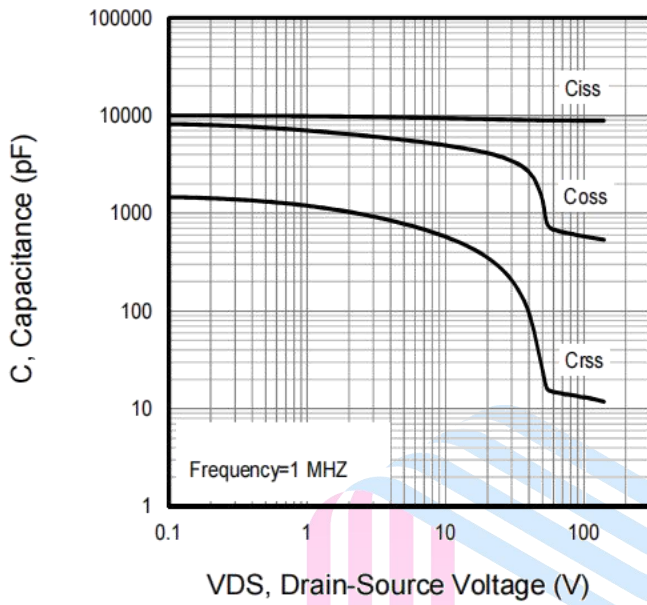
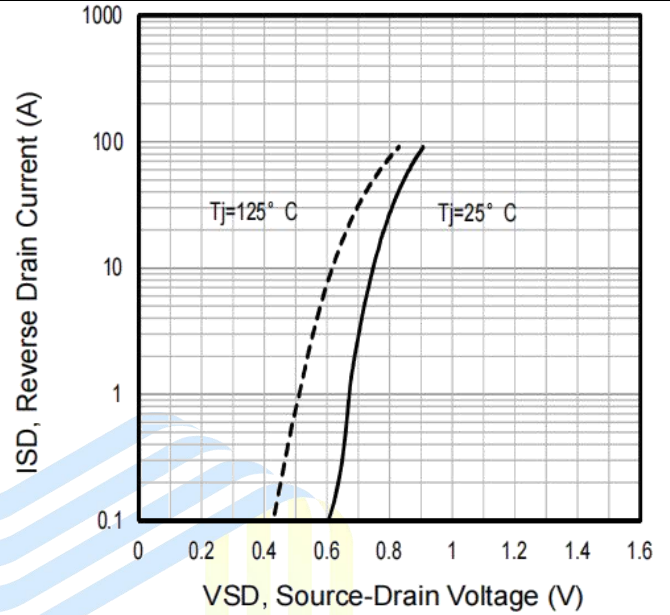
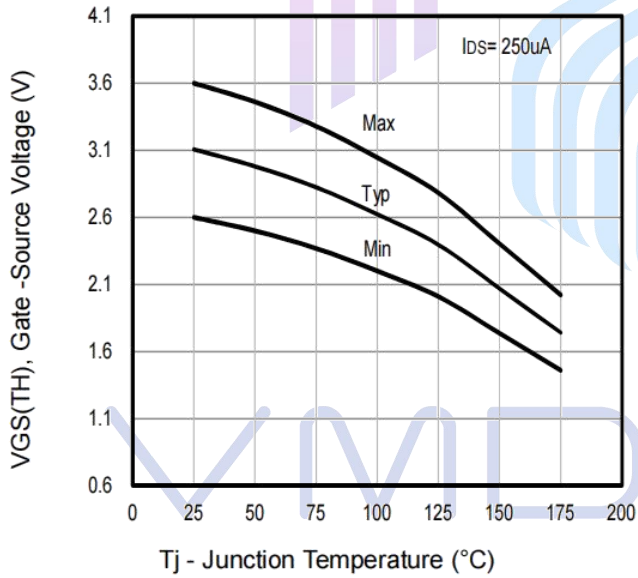
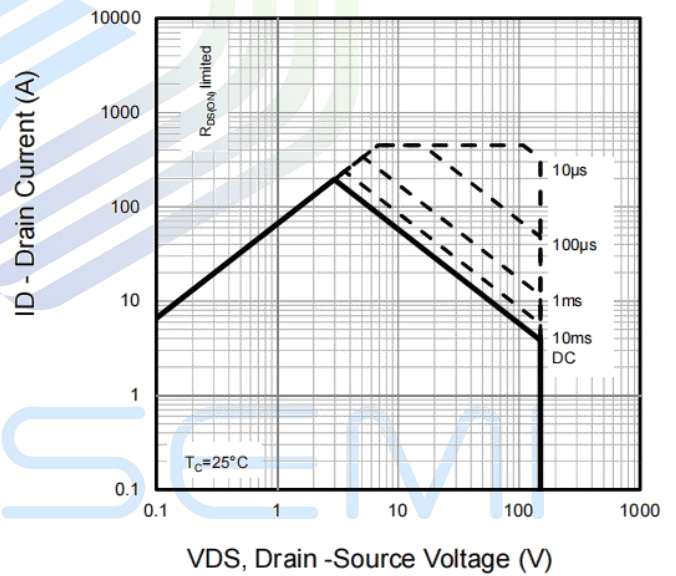
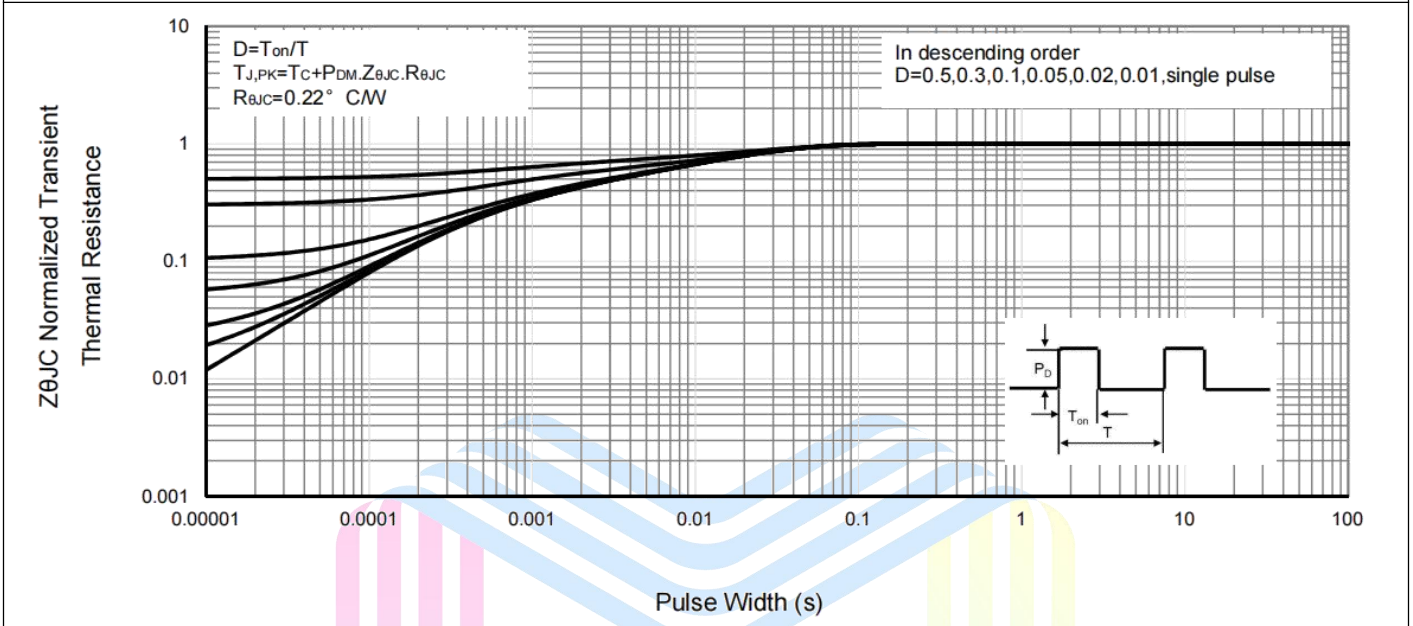
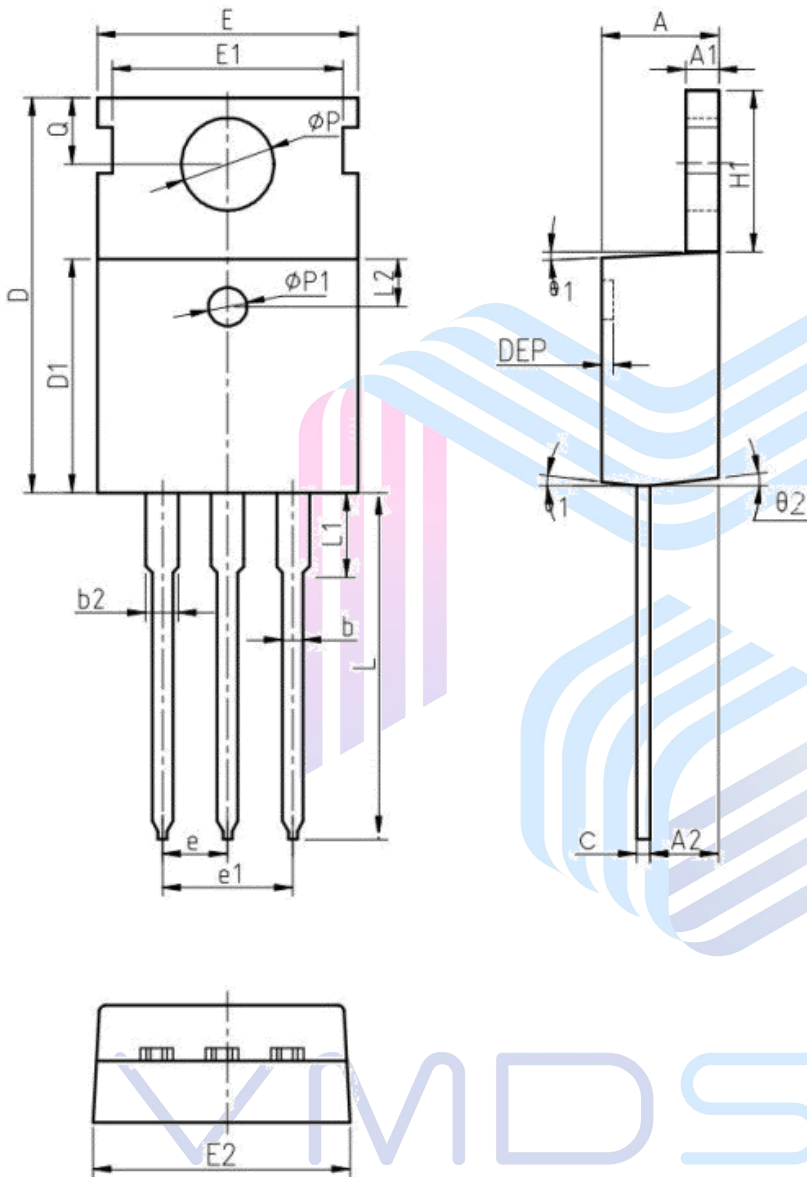
Figure 9: Typ. Capacitances

Figure 10: Forward Characteristics of Body Diode

Figure 11: Gate-Source Threshold Voltage

Figure 12: Safe Operating Area


Figure 13: Normalized Maximum Transient Thermal Impedance



Mechanical Dimensions

Package Information TO-220AB



Symbol	Dimensions (unit: mm)		
	Min	Typ	Max
A	4.30	4.52	4.70
A1	1.15	1.30	1.40
A2	2.20	2.40	2.60
b	0.70	0.80	1.00
b2	1.17	1.32	1.50
c	0.45	0.50	0.61
D	15.30	15.65	15.90
D1	9.00	9.20	9.40
DEP	0.05	0.10	0.25
E	9.66	9.90	10.28
E1	-	8.70	-
E2	9.80	10.00	10.20
φP1	1.40	1.50	1.60
e	2.54 BSC		
e1	5.08 BSC		
H1	6.40	6.50	6.80
L	12.70	-	14.27
L1	-	-	3.95
L2	2.40	2.50	2.60
φP	3.53	3.60	3.70
Q	2.70	2.80	2.90
θ1	5 °	7 °	9 °
θ2	1 °	3 °	5 °

Notes:

1. Refer to JEDEC TO-220 variation AB
2. Dimension "D" and "E" do NOT include mold flash. Mold flash shall not exceed 0.127mm per side.

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Via-Media Semiconductor Limited Company

<http://www.vmdsemi.com>

Main Sites:

- Headquarters

Hangzhou Via-Media Semiconductor Co., LTD.
1305-1306, Building 71, No. 90, Wensan Road, Xihu
District, Hangzhou, Zhejiang Province, P.R. China
Tel: +86-0571-8515 0563

- Chengdu Office

Chengdu Winhi Semiconductor Co., LTD.
Floor 15, Building 5, No. 171, Hele 2nd Street,
Chengdu, Sichuan Province, P.R. China
Tel: +86-028-8505 0771

- Shanghai

Shanghai R&D Center.
1506~1508, Xinyin Building, 888 Yishan Road,
Shanghai, P.R of China
Tel: +86-021-54201999

- Shenzhen

Shenzhen Sales office
Room 4A15, Block AB, Tianxiang Building,
Chengongmiao, Futian District, Shenzhen, P.R of China
Tel: +86-0755-82570682

- Xi'an

Xi'an R&D Center
1703B, Building A, Greenland Center, Jinye Road,
High-Tech Zone, Xi'an, Shaanxi, P.R of China