VFPA010R180NA

Datasheet

General Description

$V_{(BR)DSS}$	$R_{DS(ON)\_max}$	I_D
100V	18mΩ@10V	43A
	25mΩ@4.5V	

Symbol

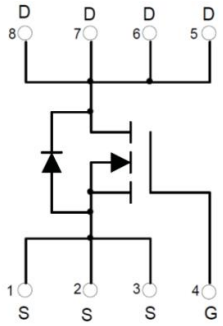
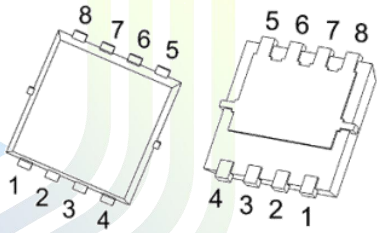


Figure 1 Symbol of VFPA010R180NA

Features

- Split Gate Trench Technology
- Low $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

Package Type



PDFN3.3X3.3-8L

Figure 2 Package Type of VFPA010R180NA

Application

- Power Switch Application

Ordering Information

Product Name	Package
VFPA010R180NA	PDFN3.3X3.3 -8L

Absolute Maximum Ratings (T_A= 25 ºC, unless otherwise specified)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	100	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current[Note1]	T_C= 25 ºC	I_D	43	A
Continuous Drain Current[Note1]	T_C= 100 ºC		30	
Pulsed Drain Current [Note2]		I_{DM}	172	
Avalanche Current[Note3]		I_{AS}	10	
Single Pulsed Avalanche Energy[Note3]		E_{AS}	25	mJ
Total Power Dissipation[Note5]	T_C= 25 ºC	P_D	43	W
Junction Temperature		T_J	150	ºC
Storage Temperature		T_{STG}	-55 to 150	ºC

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Ambient [Note6]	$R_{\theta JA}$		55		ºC/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$		2.9		ºC/W

Electrical Characteristics (T_J= 25 ºC, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D= 250uA	100			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}= 100V, V_{GS}=0V			1	uA
Gate-Body Leakage Current	I_{GSS}	V_{GS} = ±20V, V_{DS}= 0V			±100	nA
Gate Threshold Voltage[Note4]	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	1.0	1.7	3.0	V
Static Drain-Source On-Resistance[Note4]	$R_{DS(ON)}$	V_{GS}=10V, I_D= 20A		14	18	mΩ
		V_{GS}=4.5V, I_D= 15A		19	25	
Dynamic Characteristics						
Input Capacitance	C_{ISS}	V_{DS}=50V		1177		pF
Output Capacitance	C_{OSS}	V_{GS}=0V		389		pF
Reverse Transfer Capacitance	C_{RSS}	f=1MHz		21		pF
Total Gate Charge	Q_g	V_{DS}=50V		17.6		nC
Gate-Source Charge	Q_{gs}	V_{GS}=10V		4.2		
Gate-Drain Charge	Q_{gd}	I_D= 20A		0.6		
Gate Resistance	Rg	f = 1MHz, Open drain		2.2		Ω
Switching Parameters						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}= 50V		39		ns
Turn-on Rise Time	t_r	V_{GS}= 10V		11		
Turn-off Delay Time	$t_{d(off)}$	I_D= 25A		50		
Turn-off Fall Time	t_f	R_G=2.2Ω		16		
Diode Characteristics						
Diode Forward Voltage [Note4]	V_{SD}	V_{GS}=0V, I_S= 10A			1.2	V

Notes :

1.The maximum current rating is limited by package.And device mounted on a large heatsink.

2.Pulse Test : Pulse Width ≤ 10μs, duty cycle ≤ 1%.

3.E_{AS} condition: V_{DD} = 50V, V_{GS} = 10V, L = 0.1mH, R_G=25Ω Starting T_J = 25°C.

4.Pulse Test : Pulse Width ≤ 300μs, duty cycle ≤ 2%.

5.The power dissipation P_D is limited by $T_{J(MAX)}$ = 150°C.And device mounted on a large heatsink

6.Device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.

Typical Performance Characteristics

Figure 3: Transfer Characteristics

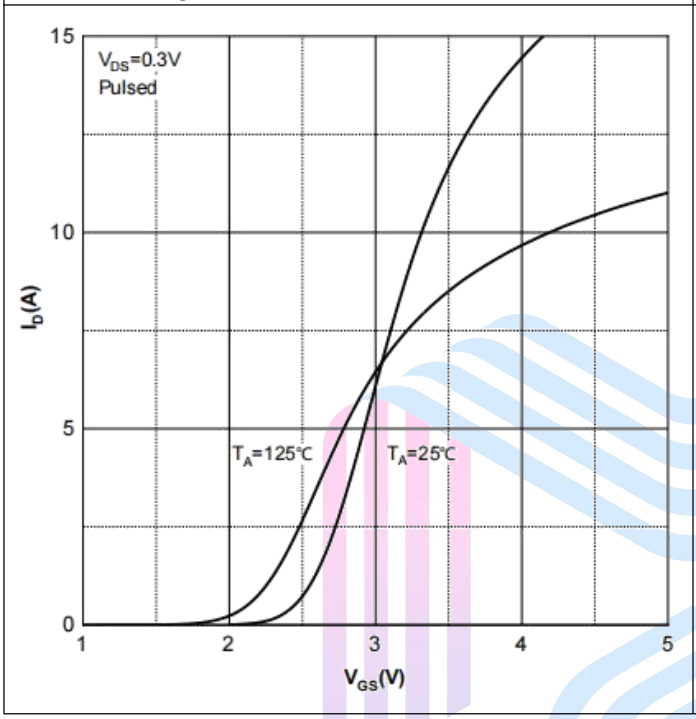


Figure 4: Output Characteristics

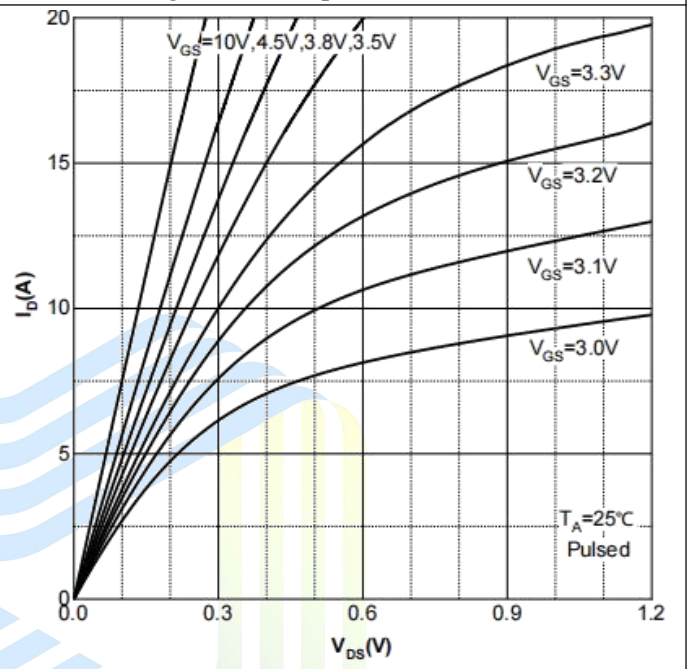


Figure 5: On-Resistance vs. Drain Current

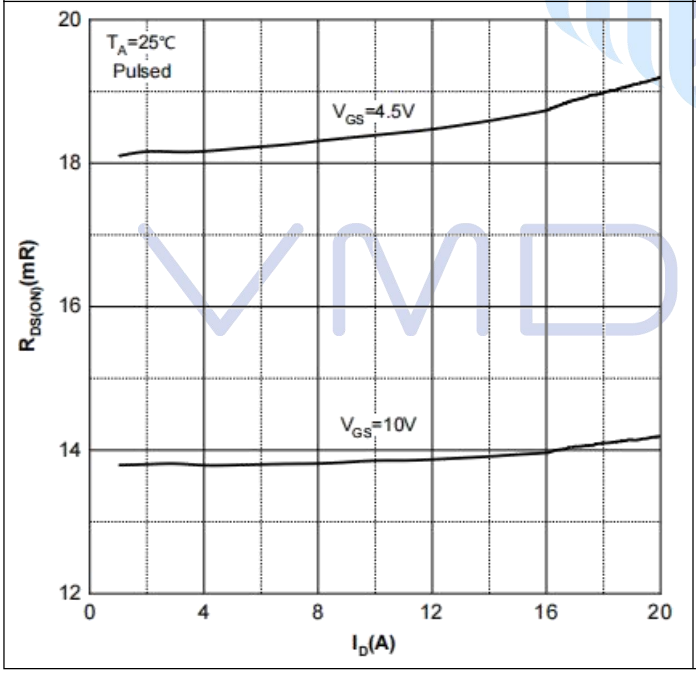


Figure 6: On-Resistance vs. Gate Voltage

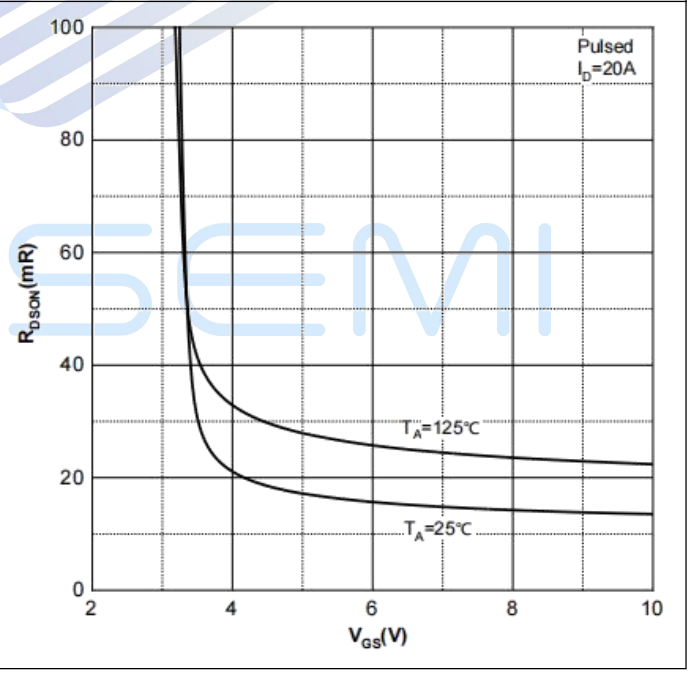


Figure 7: Body Diode Characteristics

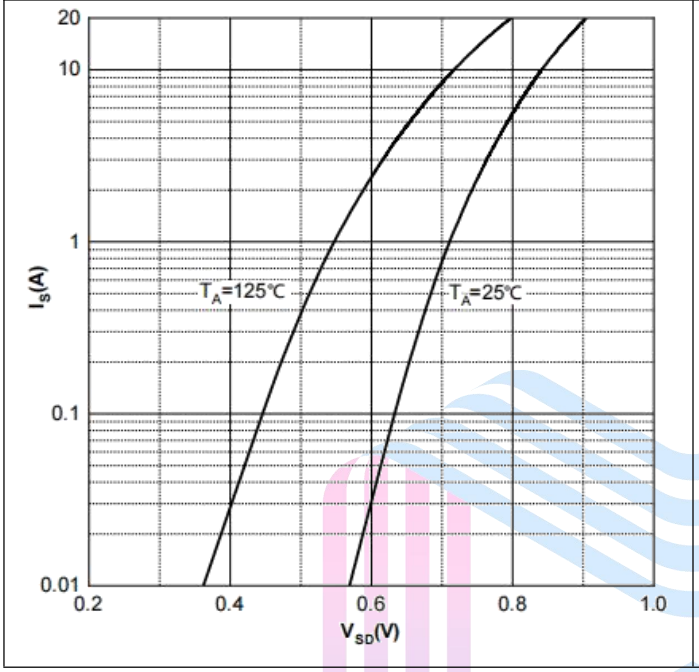


Figure 8: Threshold Voltage

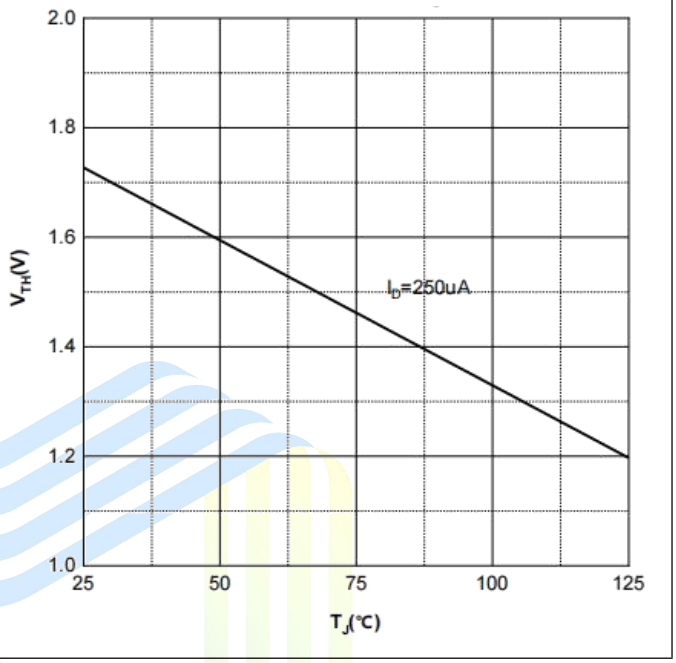


Figure 9: Typical Capacitance

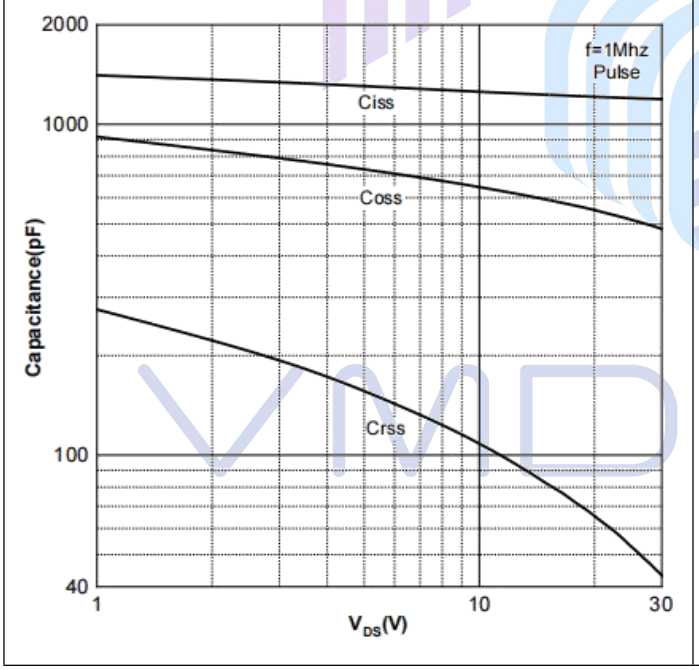


Figure 10: Safe Operation Area

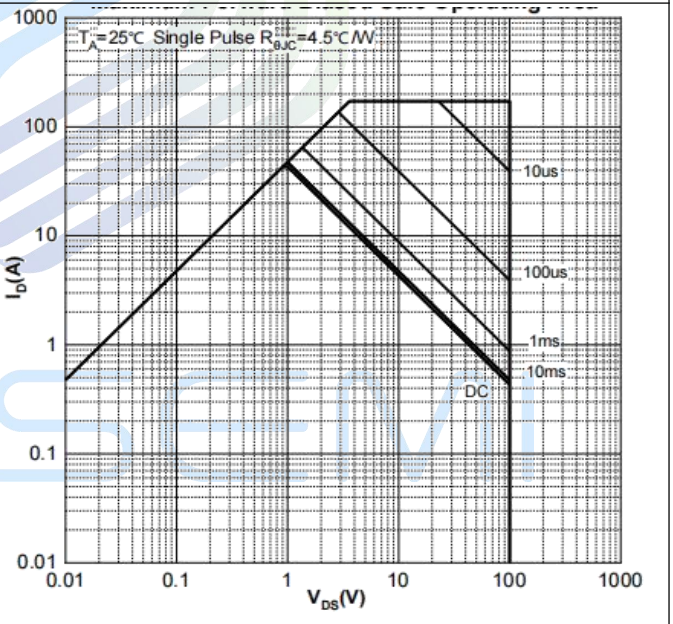
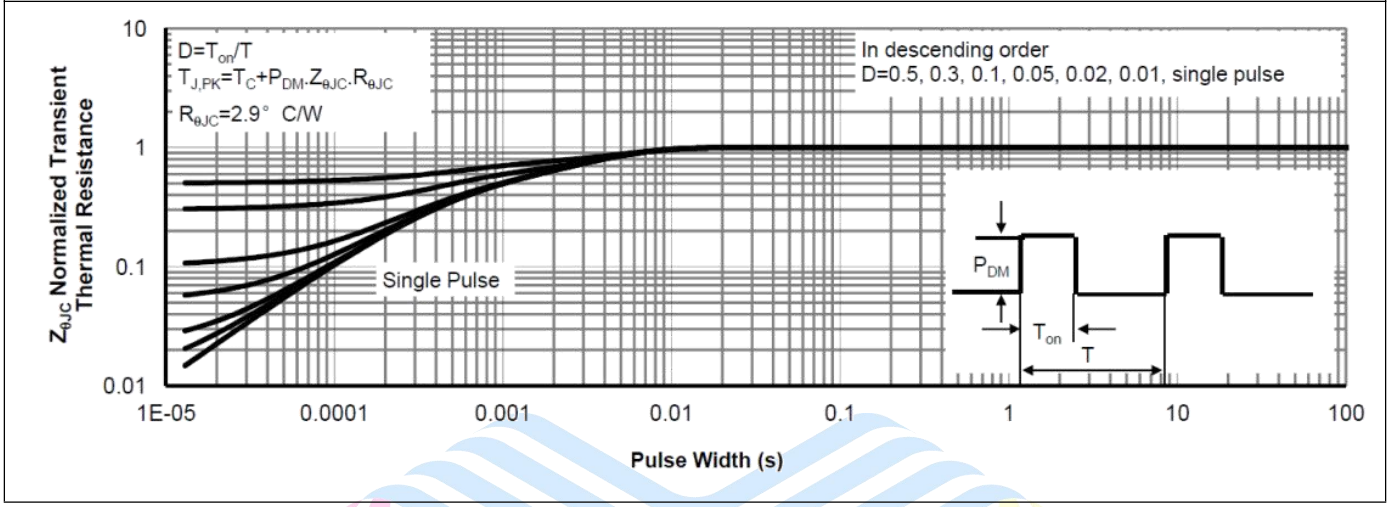
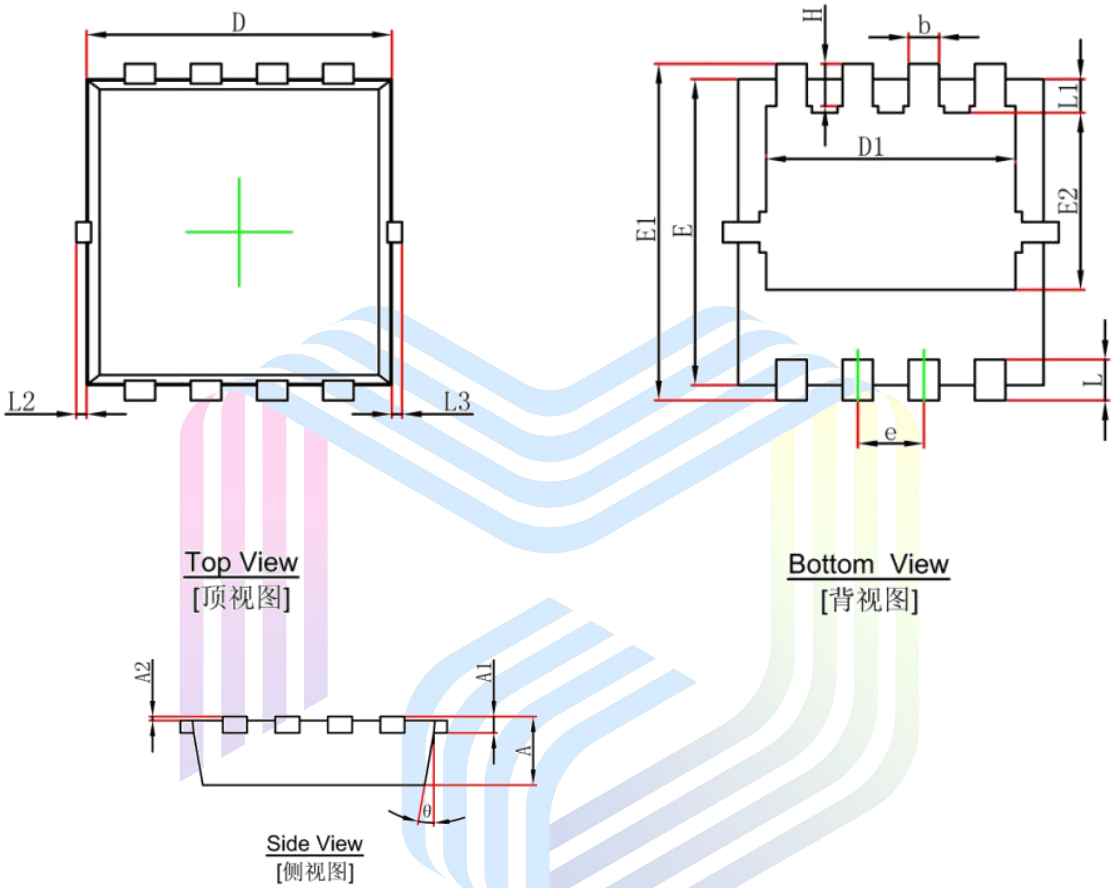


Figure 11: Normalized Maximum Transient Thermal Impedance



Mechanical Dimensions:

PDFN3.3X3.3-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.900	0.028	0.035
A1	0.152REF		0.006REF	
A2	0.000	0.050	0.000	0.002
D	2.900	3.200	0.114	0.126
D1	2.300	2.600	0.091	0.102
E	2.900	3.200	0.114	0.126
E1	3.150	3.450	0.124	0.136
E2	1.535	1.935	0.060	0.076
b	0.200	0.400	0.008	0.016
e	0.550	0.750	0.022	0.030
L	0.300	0.500	0.012	0.020
L1	0.180	0.480	0.007	0.019
L2	0.000	0.100	0.000	0.004
L3	0.000	0.100	0.000	0.004
H	0.315	0.515	0.012	0.020
θ	0°	12°	0°	12°

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