

VFTA010R110NA

Datasheet





VFTA010R110NA

General Description

$V_{(BR)DSS}$	$R_{DS(ON)_max}$	I_D
100V	11mΩ@10V	60A

Symbol

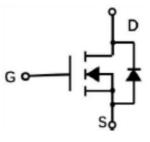
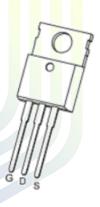


Figure 1 Symbol of VFTA010R110NA

Features

- Split Gate Trench Technology
- Low R_{DS(ON)}
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

Package Type



TO-220-3L-C

Application

Power Switching Application

Figure 2 Package Type of VFTA010R110NA

Ordering Information

Product Name	Package		
VFTA010R110NA	TO-220-3L-C		



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Absolute Maximum Ratings (T_A= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V _{DSS}	100	V	
Gate-Source Voltage	V _{GSS}	±20	V	
Continuous Drain Current ^{Note1} T _C = 25 °C	т	60		
Continuous Drain Current ^{Note1} $T_C = 100 ^{\circ}\text{C}$	I_{D}	42		
Pulsed Drain Current Note2	I_{DM}	240	A	
Avalanche Current ^{Note3}	I _{AS}	9		
Single Pulsed Avalanche Energy ^{Note3}	Eas	20.3	mJ	
Total Power Dissipation ^{Note5} T _C = 25 °C	P _D	89	W	
Junction Temperature	T _J	150	°C	
Storage Temperature	Tstg	-55 to 150	°C	

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Ambient Note6	$R_{\theta JA}$		60		°C/W
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$		1.4		°C/W





11mΩ, 100V, N-Channel Power MOSFET

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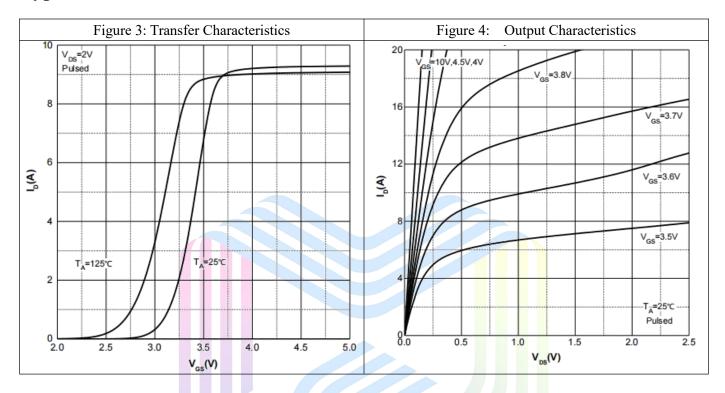
Electrical Characteristics (T_J= 25 °C, unless otherwise specified)

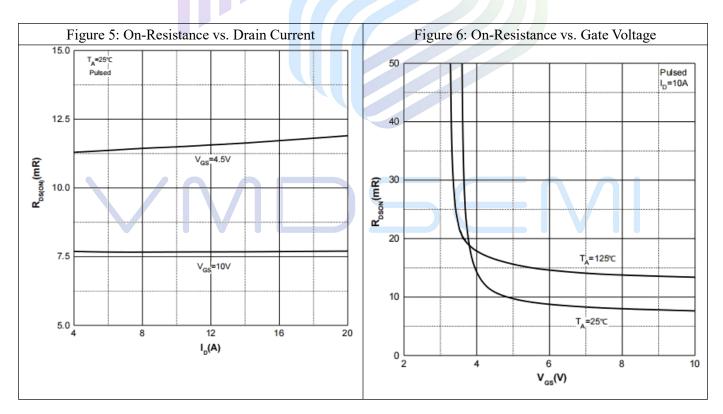
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Statistic Characteristics							
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_{D}=250uA$	100			V	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=100V, V_{GS}=0V$			1	uA	
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA	
Gate Threshold Voltage ^{Note4}	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1.0	2.5	3.5	V	
Static Drain-Source On-Resistance ^{Note4}	R _{DS(ON)}	$V_{GS}=10V, I_{D}=10A$		8	11	mΩ	
Forward Transconductance ^{Note4}	g _{FS}	$V_{DS}=5V, I_{D}=20A$		60		S	
Dynamic Characteristics							
Input Capacitance	C _{ISS}	V _{DS} =50V		1863		pF	
Output Capacitance	Coss	V _{GS} =0V		582		pF	
Reverse Transfer Capacitance	C _{RSS}	f=1MHz		10		pF	
Total Gate Charge	Qg	V _{DS} =50V		37.7			
Gate-Source Charge	Q_{gs}	V _{GS} =10V		5.8		nC	
Gate-Drain Charge	Q_{gd}	$I_D=10A$		10.5			
Gate Resistance	Rg	f = 1MHz, Open drain		1.6		Ω	
Switching Parameters							
Turn-on Delay Time	t _{d(on)}	V _{DD} = 50V		13			
Turn-on Rise Time	$t_{\rm r}$	$V_{GS}=10V$		9			
Turn-off Delay Time	$t_{ m d(off)}$	$R_L=2.5\Omega$		27		ns	
Turn-off Fall Time	t_{f}	$R_G=3\Omega$		4			
Diode Characteristics							
Diode Forward Voltage Note4	V_{SD}	$V_{GS}=0V, I_{S}=10A$			1.2	V	

Notes:

- 1. The maximum current rating is limited by package. And device mounted on a large heatsink.
- 2. Pulse Test : Pulse Width $\leq 10\mu s$, duty cycle $\leq 1\%$.
- 3.E_{AS} condition: $V_{DD} = 50V$, $V_{GS} = 10V$, L = 0.5mH, $R_G = 25\Omega$ Starting $T_J = 25$ °C.
- 4. Pulse Test : Pulse Width $\leq 300 \mu s$, duty cycle $\leq 2\%$.
- 5. The power dissipation P_D is limited by $T_{J(MAX)} = 150^{\circ}C$. And device mounted on a large heatsink
- 6.Device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.

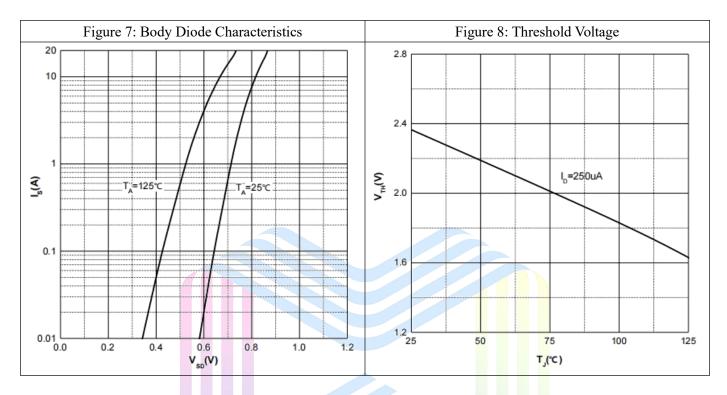
Typical Performance Characteristics

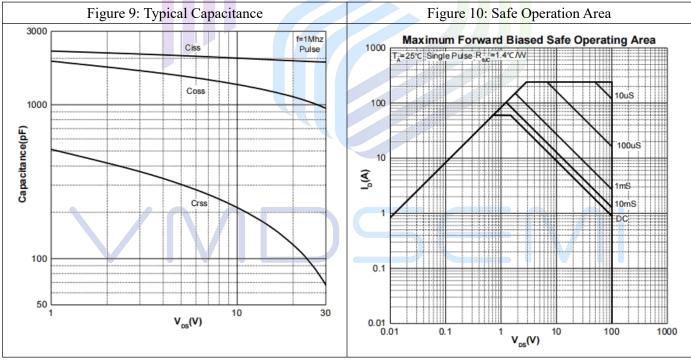






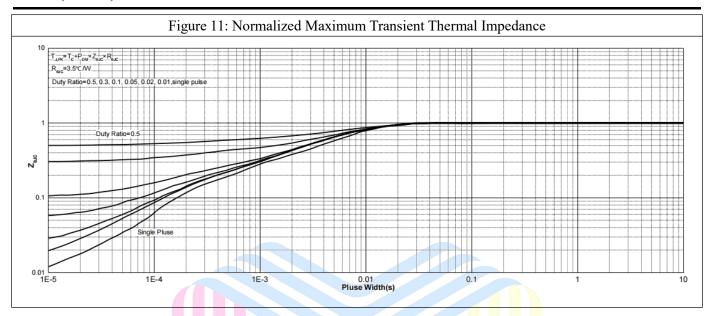
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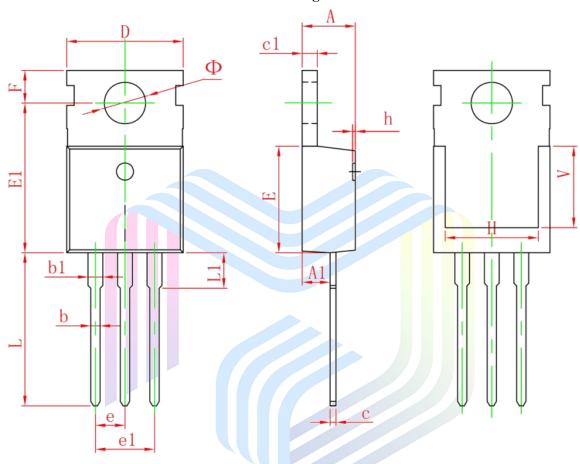






Mechanical Dimensions:

TO-220-3L-C Package Information



Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
E	8.950	9.750	0.352	0.384	
E1	12.650	13.050	0.498	0.514	
е	2.540TYP		0.100TYP		
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	6.900REF		0.272	REF	
Ф	3.400	3.800	0.134	0.150	



11mΩ, 100V, N-Channel Power MOSFET

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