

VFSA010R089NA

Datasheet



General Description

VFSA010R089NA MOSFET is based on unique device design to achieve low $RDS_{(ON)}$, low gate charge, fast switching and excellent avalanche characteristics.

VFSA010R089NA

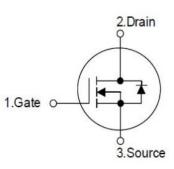


Figure 1 Symbol of VFSA010R089NA

Features

- Low RDS(ON) & FOM
- $\blacksquare R_{DS(ON)_max} = 8.9 m \Omega @V_{GS} = 10V$
- Extremely low switching loss
- Fast switching and soft recovery

Application

- BMS
- Switched mode power supply
- DC-DC converter
- Solar inverter
- UPS and energy inverter

Package Type

Symbol

SOP-8L Top View

SOP-8L Bottom View





Figure 2 Package Type of VFSA010R089NA

Ordering Information

Product Name	Package		
VFSA010R089NA	SOP-8L		



VFSA010R089NA

Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current ^{Note 1} , T _C =25°C	ID	17.6	Α
Pulsed Drain Current Note 2	I _{DM}	50	А
Max Power Dissipation Note 3, T _C =25°C	PD	10.3	W
Avalanche Current, Single Pulse Note 5	I _{AS}	23.2	Α
Avalanche Energy, Single Pulse Note 5	E _{AS}	26.3	mJ
Operation Junction temperature	TJ	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	Min	Тур	Max	Unit
Thermal Resistance, Junction-to-Case	R _{θJC}		12.1		°C/W
Thermal Resistance, Junction-to-Ambient Note4	R _{0JA}		40.5		C/W

Notes:

1) Calculated continuous current based on maximum allowable junction temperature.

2) Repetitive rating; pulse width limited by max. junction temperature.

3) P_D is based on max. junction temperature, using junction-case thermal resistance.

4) The value of $R_{\theta JA}$ is measured with the device mounted on 1 in ² FR-4 board with 2oz. Copper, in a still air environment with Ta=25 °C.

5) V_{DS}=50 V,V_{GS}=10 V, L=0.1 mH, starting T_J=25 °C.



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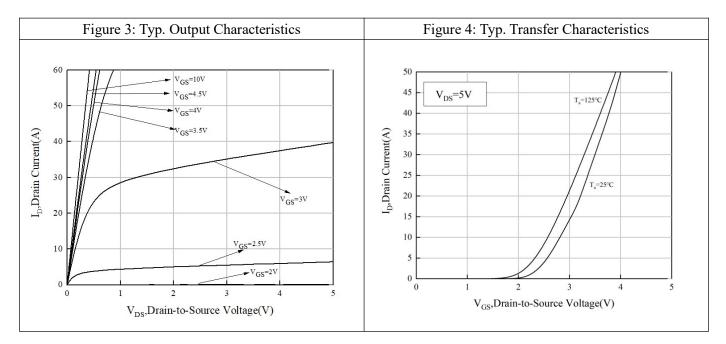
Electrical Characteristics (T_J= 25 °C, unless otherwise specified)

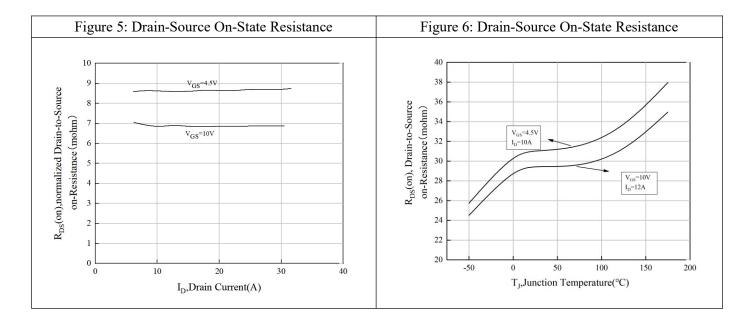
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	100			V
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =80V, V_{GS} =0V			1	uA
Gate-Body Leakage Current	I _{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$			±100	nA
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250uA	1.0	1.6	2.5	V
Static Drain-Source On-Resistance		V _{GS} =10V, I _D =12A		6.8	8.9	mΩ
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =10A		8.5	11	mΩ
Gate Resistance	R _G	V _{GS} =0V,V _{DS} =0V,f=1MHz		2.3		Ω
Dynamic Characteristics						
Input Capacitance	CISS	V _{GS} =0V		2540		pF
Output Capacitance	Coss	$V_{\rm DS}=50V$		780		pF
Reverse Transfer Capacitance	C _{RSS}	f=1MHz		43		pF
Turn-on Delay Time	t _{d(on)}	V _{DS} =50V		12.6		
Rise Time	tr	$V_{GS}=10V$		29.0		
Turn-off Delay Time	t _{d(off)}	R _L =4.1Ω	55.6		ns	
Fall Time	t _f	$R_{\text{GEN}}=15\Omega$		32.4		
Switching Characteristics						
Total Gate Charge (@VGS=10V)	Qg	N 04 10N		52.7		
Total Gate Charge (@VGS=4.5V)	Qg	$V_{GS}=0$ to 10V		27.4		
Gate to Source Charge	Q _{gs}	$V_{DS}=50V$		8.0		nC
Gate to Drain Charge	Q _{gd}	$I_D=12A$		12.0		
Reverse Diode Characteristics			1			
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _{SD} =10A		0.7	1.2	V
Reverse Recovery Time	t _{rr}	VR=30V		49.7		ns
Reverse Recovery Charge	Qrr	I _F =12A		51.8		nC
Peak Reverse Recovery Current	Irrm	di/dt=100A/us		2.1		А



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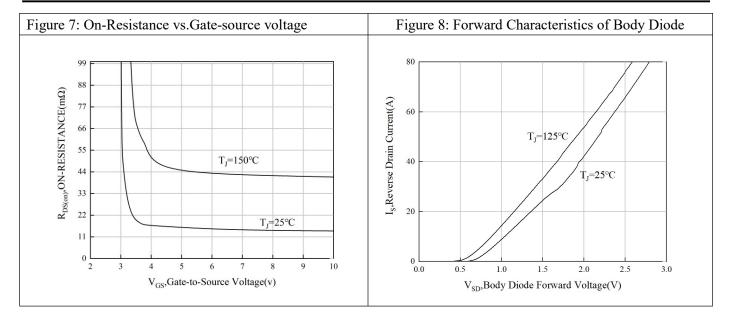
Typical Performance Characteristics

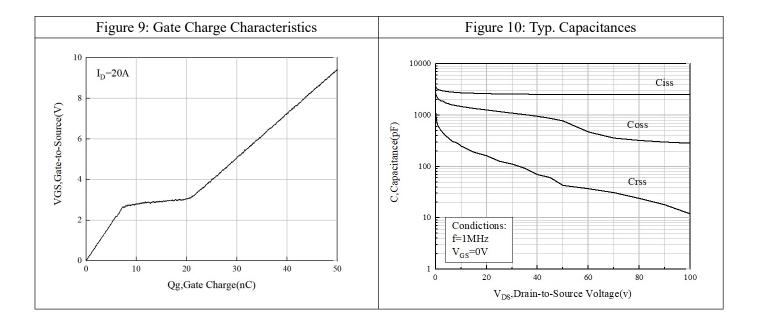






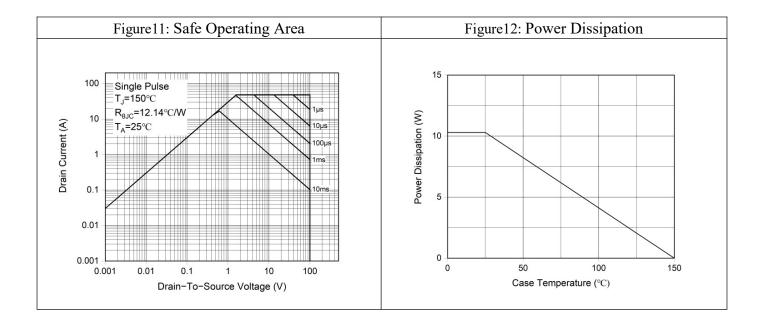
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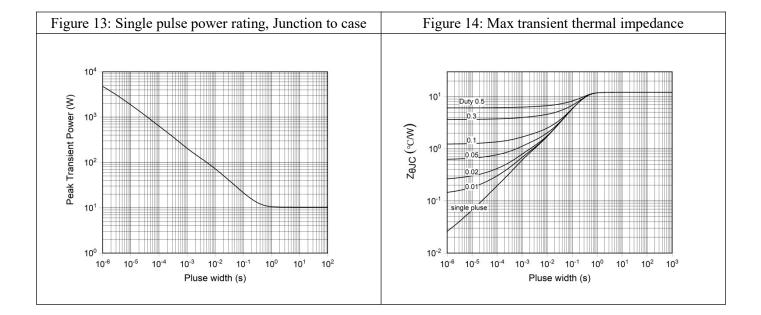






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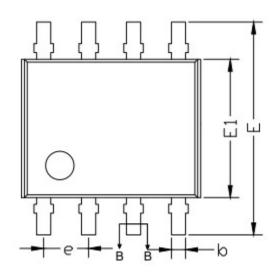


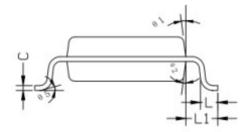


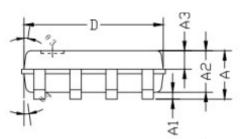


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Mechanical Dimensions (SOP-8L Unit: mm)







S-mak al	MILLIMETER					
Symbol	MIN	NOM	MAX			
А			1.65			
A1	0.10		0.25			
A2	1.40	1.42	1.50			
A3	0.60	0.65	0.70			
b	0.33		0.47			
b1	0.32	0.41	0.44			
с	0.20		0.24			
c1	0.19	0.20	0.21			
D	4.80	4.90	5.00			
Е	5.90	6.00	6.20			
E1	3.85	3.90	4.00			
e		1.27BSC				
L	0.50	0.60	0.70			
L1	1.05BSC					
θ1	6°		12°			
θ2	6°		12°			
θ3	5°		10°			
θ4	5°		10°			
θ5	0°		6°			



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