

# VUPA002R070PA

**Datasheet** 





### VUPA002R070PA

### **General Description**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)_max</sub>	$I_D$
-20V	7.0mΩ@-4.5V	45.4
	9.0mΩ@-2.5V	-45A

## **Symbol**

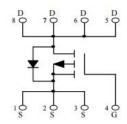


Figure 1 Symbol of VUPA002R070PA

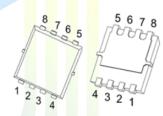
### **Features**

- Trench Technology Power MOSFET
- $\blacksquare$  Low  $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance

## **Application**

- Battery protection applications
- Load switch

## Package Type



PDFN3.3X3.3-8L

Figure 2 Package Type of VUPA002R070PA

## **Ordering Information**

<b>Product Name</b>	Package
VUPA002R070PA	PDFN3.3X3.3-8L



### VUPA002R070PA

## Absolute Maximum Ratings (T<sub>A</sub>= 25 °C, unless otherwise specified)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V <sub>DSS</sub>	-20	V
Gate-Source Voltage		V <sub>GSS</sub>	±12	V
Continuous Drain Current <sup>Note1</sup>	$T_{\rm C}=25~{\rm ^{o}C}$	$I_D$	-45	Α
Pulsed Drain Current Note2		$I_{DM}$	-180	A
Total Power Dissipation <sup>Note4</sup>	$T_{\rm C}=25~{\rm ^{o}C}$	$P_{D}$	83	W
Junction Temperature		T <sub>J</sub>	150	°C
Storage Temperature		T <sub>STG</sub>	-55 to 150	°C

### **Thermal Resistance**

Parameter	Symbol	<mark>M</mark> in	T <mark>y</mark> p	Max	Unit
Thermal Resistance, Junction-to-Ambient <sup>Note5</sup>	$R_{ heta JA}$		55		°C/W
Thermal Resistance, Junction-to-Case	$R_{ heta  m JC}$		1.5		°C/W





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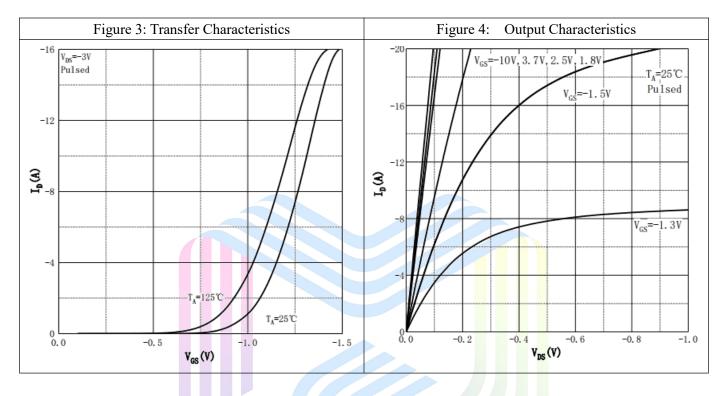
### Electrical Characteristics (T<sub>J</sub>= 25 °C, unless otherwise specified)

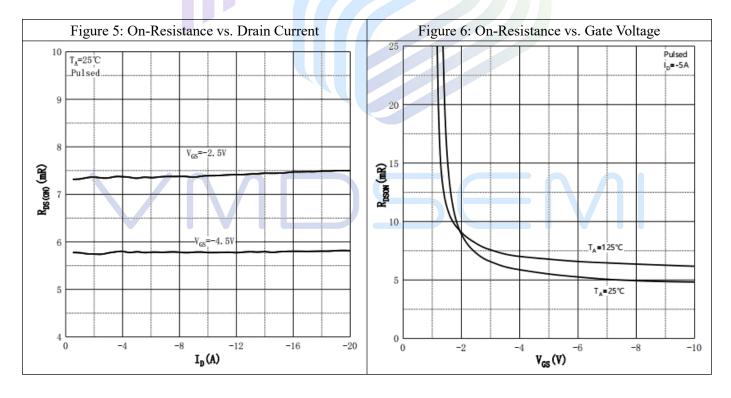
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Statistic Characteristics							
Drain-Source Breakdown Voltage	$\mathrm{BV}_{\mathrm{DSS}}$	V <sub>GS</sub> =0V, I <sub>D</sub> = 250uA	-20			V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = -16V, $V_{GS}$ =0V			-1	uA	
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS} = \pm 12V, V_{DS} = 0V$			±100	nA	
Gate Threshold Voltage <sup>Note3</sup>	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=-250uA$	-0.4	-0.75	-1.0	V	
Static Drain-Source On-Resistance <sup>Note3</sup>	D	$V_{GS}$ =-4.5V, $I_D$ = -20A		5.8	7	— mO	
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}$ =-2.5V, $I_D$ = -20A		7.5	9		
Forward Transconductance <sup>Note3</sup>	gfs	$V_{DS}$ =-5V, $I_{D}$ = -20A	50			S	
Dynamic Characteristics							
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =-10V		3600		pF	
Output Capacitance	Coss	V <sub>GS</sub> =0V		560		pF	
Reverse Transfer Capacitance	C <sub>RSS</sub>	f=1MHz		440		pF	
Total Gate Charge	$Q_{\mathrm{g}}$	V <sub>DS</sub> =-10V		50			
Gate-Source Charge	$Q_{\mathrm{gs}}$	V <sub>GS</sub> =-4.5V		9		nC	
Gate-Drain Charge	$Q_{\mathrm{gd}}$	$I_D = -20A$		14			
Gate Resistance	Rg	f = 1MHz, Open drain		3		Ω	
Switching Parameters							
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = -10V$		16			
Turn-on Rise Time	$t_{\rm r}$	$V_{GS}$ = -4.5V		40			
Turn-off Delay Time	$t_{d(off)}$	$R_L=3\Omega$		83		ns	
Turn-off Fall Time	$t_{\mathrm{f}}$	$R_G=0.5\Omega$		22			
Diode Characteristics							
Diode Forward Voltage Note3	$V_{DS}$	$V_{GS}=0V, I_{S}=-20A$			-1.2	V	

#### Notes:

- 1. The maximum current rating is limited by package. And device mounted on a large heatsink.
- 2. Pulse Test : Pulse Width  $\leq 10\mu s$ , duty cycle  $\leq 1\%$ .
- 3. Pulse Test : Pulse Width  $\leq 300 \mu s$ , duty cycle  $\leq 2\%$ .
- 4. The power dissipation  $P_D$  is limited by  $T_{J(MAX)} = 150$ °C. And device mounted on a large heatsink
- 5.Device mounted on  $1 \text{in}^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^{\circ}\text{C}$ .

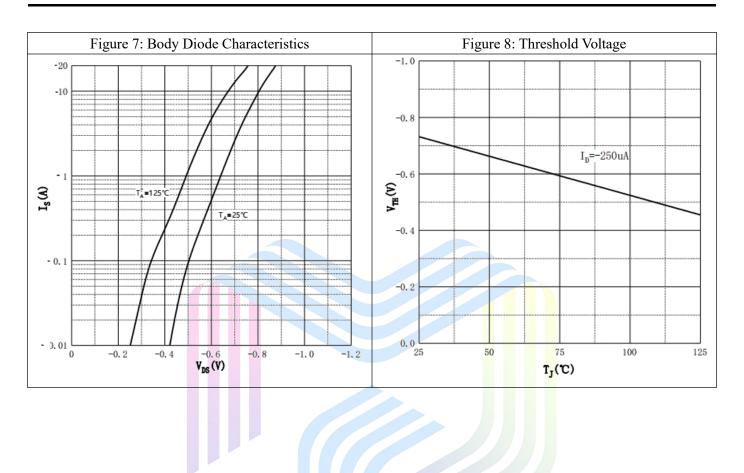
## **Typical Performance Characteristics**







### VUPA002R070PA

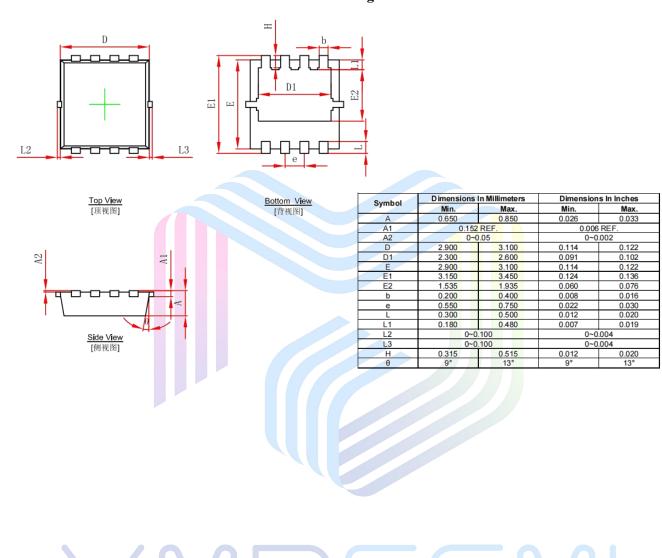




#### VUPA002R070PA

### **Mechanical Dimensions:**

PDFN3.3X3.3-8L Package Information





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