

VUPA1P2R065PA

Datasheet

VMDSEMI



VUPA1P2R065PA

General Description

V _{(BR)DSS}	R _{DS(ON)_max}	I _D
-12V	6.5mΩ@-4.5V	52 4
	9mΩ@-2.5V	-52A



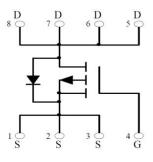


Figure 1 Symbol of VUPA1P2R065PA

Package Type

8

¹ ² ³

4

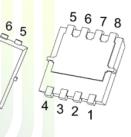
- Trench Technology Power MOSFET
- Low R_{DS(ON)}

Features

- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

Application

- PWM application
- Load switch
- Power Switching Application



PDFN3.3X3.3-8L

Figure 2 Package Type of VUPA1P2R065PA

Ordering Information

Product Name	Package		
VUPA1P2R065PA	PDFN3.3X3.3-8L		



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Absolute Maximum Ratings (T_A= 25 °C, unless otherwise specified)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DSS}	-12	V
Gate-Source Voltage		V _{GSS}	±12	V
Continuous Drain Current ^{Note1}	$T_C = 25 \ ^{\circ}C$	т	-52	
Continuous Drain Current ^{Note1}	$T_{\rm C} = 100 \ ^{\rm o}{\rm C}$	ID	-35	A
Pulsed Drain Current Note2		I _{DM}	-208	
Total Power Dissipation ^{Note4}	$T_C=25 \ ^{\circ}C$	PD	39	W
Junction Temperature		TJ	150	°C
Storage Temperature		T _{STG}	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	<mark>M</mark> in	Т <mark>у</mark> р	Max	Unit
Thermal Resistance, Junction-to-Ambient ^{Note5}	R _{0JA}		38		°C/W
Thermal Resistance, Junction-to-Case	Røjc		3 <mark>.2</mark>		°C/W

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Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Statistic Characteristics			•				
Drain-Source Breakdown Voltage	BV _{DSS}	$V_{GS}=0V, I_D=250uA$	-12			V	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = -12V, V_{GS} =0V			-1	uA	
Gate-Body Leakage Current	I _{GSS}	$V_{GS} = \pm 8V, V_{DS} = 0V$			±100	nA	
Gate Threshold Voltage ^{Note3}	V _{GS(th)}	$V_{DS}=V_{GS}, I_D=-250uA$	-0.4	-0.7	-1	V	
Static Drain-Source On-Resistance ^{Note3}		V_{GS} =-4.5V, I_D = -20A		5	6.5	6.5 9 mΩ	
Static Drain-Source On-Resistance.	R _{DS(ON)}	V_{GS} =-2.5V, I_D = -15A		6.5	9		
Forward Transconductance ^{Note3}	g _{FS}	V_{DS} =-5V, I_{D} = -15A		75		S	
Dynamic Characteristics							
Input Capacitance	CISS	V _{DS} =-6.5V		4643		pF	
Output Capacitance	Coss	V _{GS} =0V		1514		pF	
Reverse Transfer Capacitance	C _{RSS}	f=1MHz		1539		pF	
Total Gate Charge	Qg	V _{DS} =-10V		78			
Gate-Source Charge	Q_{gs}	V _{GS} =-4.5V		5.8		nC	
Gate-Drain Charge	Q_{gd}	I _D = -20A		1.5			
Gate Resistance	Rg	f = 1MHz, Open drain		4.3		Ω	
Switching Parameters							
Turn-on Delay Time	t _{d(on)}	V_{DD} = -10V		13			
Turn-on Rise Time	tr	$V_{GS} = -4.5V$		18			
Turn-off Delay Time	t _{d(off)}	I _D = -15A		92		ns	
Turn-off Fall Time	t _f	$R_{G}=3\Omega$		156			
Diode Characteristics							
Diode Forward Voltage Note3	V _{SD}	$V_{GS}=0V, I_{S}=-20A$			-1.2	V	
Diode Reverse Recovery Time	t _{rr}	$I_F = 15A, dI/dt = 100A/us$ 27			ns		
Diode Reverse Recovery Charge	Q _{rr}	$I_F = 15A, dI/dt = 100A/us$ 26			nC		
Notes :							

Electrical Characteristics (T_J= 25 °C, unless otherwise specified)

Notes : 1.The maximum current rating is limited by package.And device mounted on a large heatsink.

2.Pulse Test : Pulse Width $\leq 10\mu s$, duty cycle $\leq 1\%$.

3.Pulse Test : Pulse Width \leq 300µs, duty cycle \leq 2%.

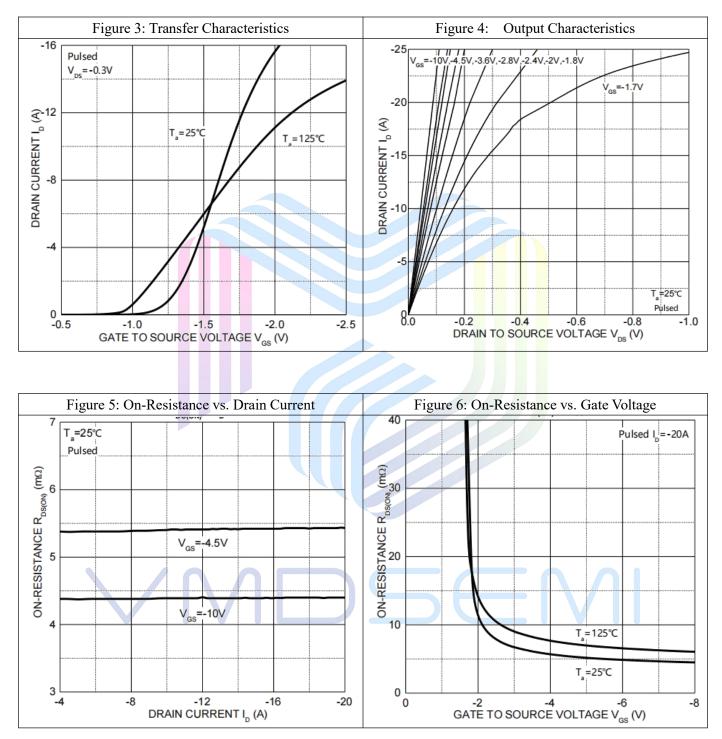
4. The power dissipation P_D is limited by $T_{J(MAX)} = 150^{\circ}$ C. And device mounted on a large heatsink

5.Device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^{\circ}C$.



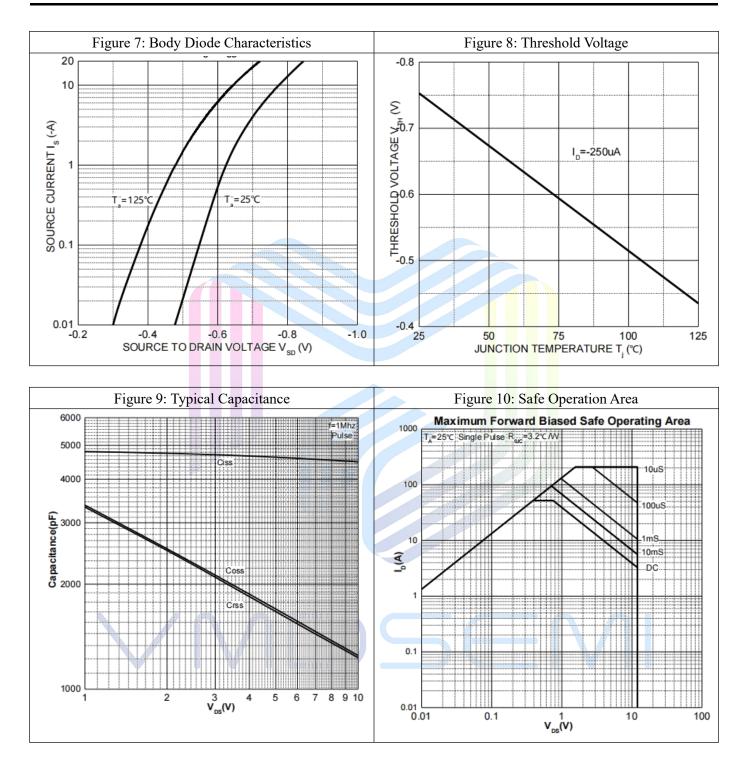
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Typical Performance Characteristics



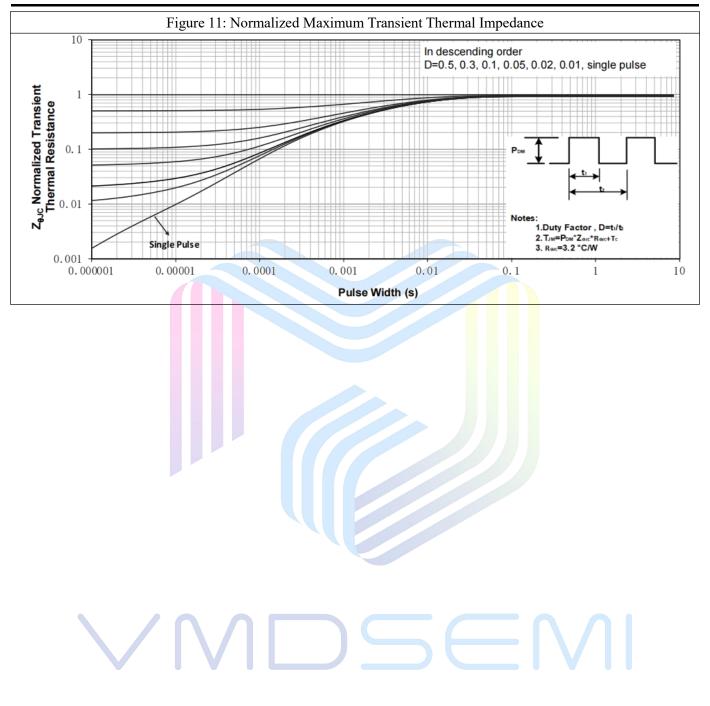


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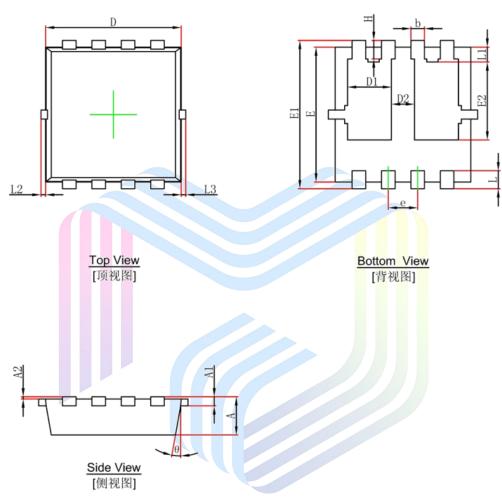
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Mechanical Dimensions:



Question	Dimensions	n Millimeters	Dimension	s In Inches	
Symbol	Min.	Max.	Min.	Max.	
A	0.700	0.900	0.028	0.035	
A1	0.152	2REF	0.006	BREF	
A2	0.000	0.050	0.000	0.002	
D	2.900	3.200	0.114	0.126	
D1	0.935	1.135	0.037	0.045	
D2	0.280	0.480	0.011	0.019	
E	2.900	3.200	0.114	0.126	
E1	3.150	3.450	0.124	0.136	
E2	1.535	1.935	0.060	0.076	
b	0.200	0.400	0.008	0.016	
е	0.550	0.750	0.022	0.030	
L	0.300	0.500	0.012	0.020	
L1	0.180	0.480	0.007	0.019	
L2	0.000	0.100	0.000	0.004	
L3	0.000	0.100	0.000	0.004	
Н	0.315	0.515	0.012	0.020	
θ	0°	12°	0°	12°	

PDFN3.3X3.3-8L Package Information



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