

# VUTY002R52APA

**Datasheet** 





## 520mΩ, -20V, P-Channel Power MOSFET

### VUTY002R52APA

## **General Description**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)_max</sub>	$I_D$
	520mΩ@-4.5V	
-20V	780mΩ@-2.5V	-0.66A
	1100mΩ@-1.8V	

# **Symbol**

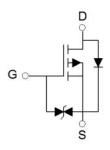
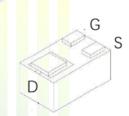


Figure 1 Symbol of VUTY002R52APA

### **Features**

- Trench Technology Power MOSFET
- Low R<sub>DS(ON)</sub>
- Low Gate Charge
- ESD Protected

# Package Type



# **Application**

- Load Switching
- Low Current Inverters
- Low Current DC/DC Converters

### WBFBP-03E

Figure 2 Package Type of VUTY002R52APA

# **Ordering Information**

Product Name	Package		
VUTY002R52APA	WBFBP-03E		



## $520m\Omega$ , -20V, P-Channel Power MOSFET

### VUTY002R52APA

# Absolute Maximum Ratings (T<sub>A</sub>= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DSS</sub>	-20	V
Gate-Source Voltage	V <sub>GSS</sub>	±12	V
Continuous Drain Current <sup>Note1</sup>	$I_D$	-0.66	
Pulsed Drain Current Note2	$I_{DM}$	-2.0	A
Total Power Dissipation <sup>Note4</sup>	$P_{D}$	0.3	W
Junction Temperature	$T_{\rm J}$	150	°C
Storage Temperature	T <sub>STG</sub>	-55 to 150	°C

### **Thermal Resistance**

Parameter	Symbol	<mark>M</mark> in	T <mark>y</mark> p	Max	Unit	
Thermal Resistance, Junction-to-CaseNote5			4 <mark>16</mark>		°C/W	





# 520mΩ, -20V, P-Channel Power MOSFET

#### VUTY002R52APA

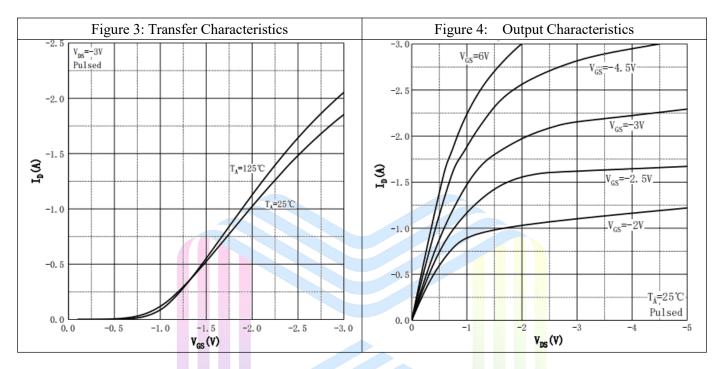
## Electrical Characteristics (T<sub>J</sub>= 25 °C, unless otherwise specified)

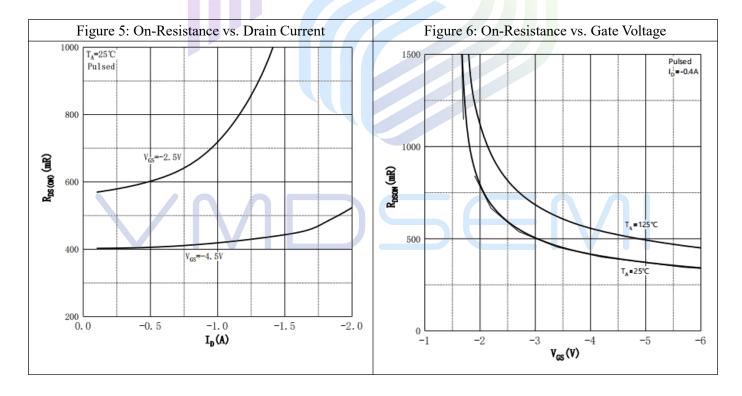
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	$BV_{DSS}$	V <sub>GS</sub> =0V, I <sub>D</sub> = 250uA	-20			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}$ = -16V, $V_{GS}$ =0V			-1	uA
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS} = \pm 10V, V_{DS} = 0V$			±10	uA
Gate Threshold Voltage <sup>Note3</sup>	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA		-0.7	-1.0	V
		$V_{GS}$ =-4.5V, $I_D$ = -0.5A		400	520	
Static Drain-Source On-Resistance <sup>Note3</sup>	R <sub>DS(ON)</sub>	$V_{GS}$ =-2.5V, $I_D$ = -0.3A		570	780	mΩ
		$V_{GS}$ =-1.8V, $I_D$ = -0.12A		810	1100	
Forward Transconductance <sup>Note3</sup>	$g_{\mathrm{FS}}$	$V_{DS}$ =-5V, $I_{D}$ = -0.4A		1		S
Dynamic Characteristics						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =-10V		79		pF
Output Capacitance	Coss	V <sub>GS</sub> =0V		15		pF
Reverse Transfer Capacitance	C <sub>RSS</sub>	f=1MHz		13		pF
Total Gate Charge	Qg	V <sub>DS</sub> =-10V		2.26		
Gate-Source Charge	$Q_{\mathrm{gs}}$	$V_{GS}$ =-4.5V		0.45		nC
Gate-Drain Charge	Q <sub>gd</sub>	$I_D = -0.2A$		0.24		
Switching Parameters						
Turn-on Delay Time	t <sub>d(on)</sub>	$V_{DD} = -10V$		8		
Turn-on Rise Time	$t_{\rm r}$	$V_{GS} = -4.5V$		5.5		<b>12</b> G
Turn-off Delay Time	$t_{d(off)}$	$R_L=50\Omega$		30		ns
Turn-off Fall Time	$t_{\mathrm{f}}$	$R_G=3\Omega$		17		
Diode Characteristics						
Diode Forward Voltage Note3	$V_{\mathrm{SD}}$	$V_{GS}=0V, I_{S}=-0.5A$			-1.2	V

#### Notes:

- 1. The maximum current rating is limited by package. And device mounted on a large heatsink.
- 2. Pulse Test : Pulse Width  $\leq 10\mu s$ , duty cycle  $\leq 1\%$ .
- 4. Pulse Test : Pulse Width  $\leq 300 \mu s$ , duty cycle  $\leq 2\%$ .
- 5. The power dissipation  $P_D$  is limited by  $T_{J(MAX)} = 150$ °C. And device mounted on a large heatsink
- 6.Device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub> =25°C.

# **Typical Performance Characteristics**

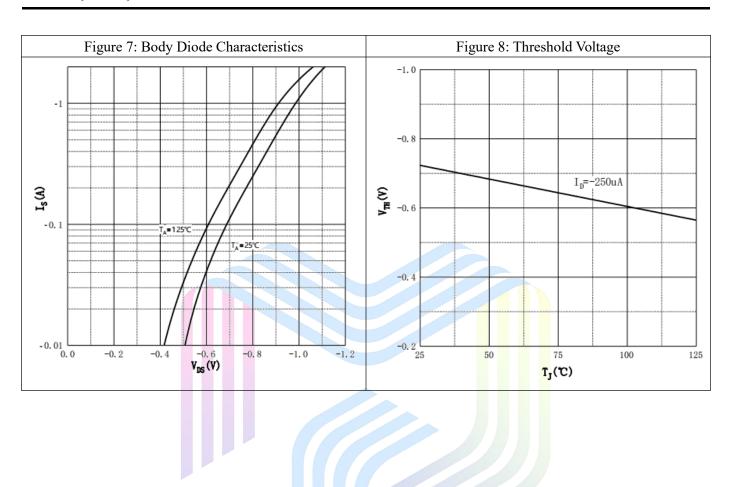






## $520m\Omega$ , -20V, P-Channel Power MOSFET

### VUTY002R52APA



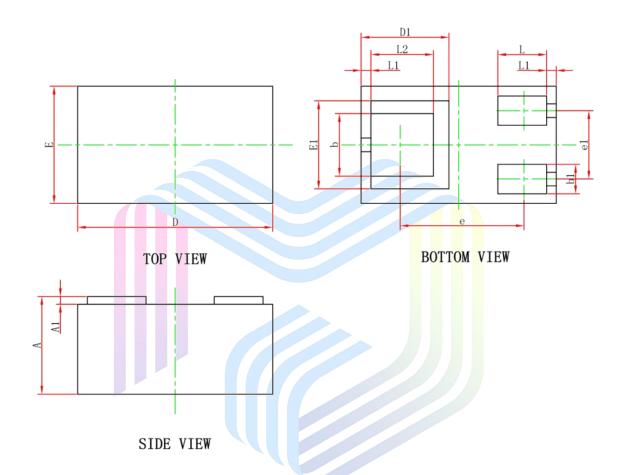




# $520m\Omega$ , -20V, P-Channel Power MOSFET

## **Mechanical Dimensions:**

### **WBFBP-03E Package Information**



Symbol	<b>Dimensions In Millimeters</b>		Dimensions In Inches					
Symbol	Min.	Max.	Min.	Max.				
A	0.450	0.550	0.018	0.022				
A1	0.010	0.100	0.000	0.004				
D	0.950	1.050	0.037	0.041				
Е	0.550	0.650	0.022	0.026				
D1	0.450	REF	0.018REF					
E1	0.450REF		0.018REF					
b	0.270	0.370	0.011	0.015				
b1	0.100	0.200	0.004	0.008				
е	0.635REF		0.025REF					
e1	0.300	0.400	0.012	0.016				
L	0.200	0.300	0.008	0.012				
L1	0.050	REF	0.002REF					
L2	0.270	0.370	0.011	0.015				

#### 520mΩ, -20V, P-Channel Power MOSFET

#### VUTY002R52APA

#### **NOTICE**

Hangzhou VMD Semiconductor Co., Ltd (VMD) reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to VMD's terms and conditions supplied at the time of order acknowledgement.

VMD, its affiliates, agents, and employees, and all persons acting on its or their behalf, disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

VMD disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify VMD's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

VMD warrants performance of its hardware products to the specifications at the time of sale, testing, reliability and quality control are used to the extent VMD deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

VMD does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using VMD's components. To minimize risk, customers must provide adequate design and operating safeguards.

VMD does not warrant or convey any license to any intellectual property rights either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in VMD's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice.

VMD is not responsible or liable for such altered documentation. Resale of VMD's products with statements different from or beyond the parameters stated by VMD for that product or service voids all express or implied warrantees for the associated VMD product or service and is an unfair and deceptive business practice.

All Rights Reserved.





# Via-Media Semiconductor Limited Company

### http://www.vmdsemi.com

#### **Main Sites:**

#### - Headquarters

Hangzhou Via-Media Semiconductor Co., LTD. 1305-1306, Building 71, No. 90, Wensan Road, Xihu District, Hangzhou, Zhejiang Province, P.R. China Tel: +86-0571-8515 0563

#### - Shanghai

Shanghai R&D Center. 1506~1508, Xinyin Building, 888 Yishan Road, Shanghai, P.R of China Tel: +86- 021-54201999

#### - Xi'an

Xi'an R&D Center 1703B, Building A, Greenland Center, Jinye Road, High-Tech Zone, Xi'an, Shaanxi, P.R of China

#### - Chengdu Office

Chengdu Winhi Semiconductor Co., LTD. Floor 15, Building 5, No. 171, Hele 2<sup>nd</sup> Street, Chengdu, Sichuan Province, P.R. China Tel: +86-028-8505 0771

#### Shenzhen

Shenzhen Sales office
Room 4A15, Block AB, Tianxiang Building,
Chegongmiao, Futian District, Shenzhen, P.R of China
Tel: +86-0755-82570682