

VUSG002R750PA

Datasheet



VUSG002R750PA

General Description

V _{(BR)DSS}	R _{DS(ON)_max}	I_D
	75mΩ@-4.5V	
-20V	105mΩ@-2.5V	-1.4A
	156mΩ@-1.8V	

Symbol

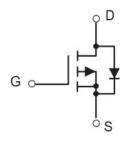


Figure 1 Symbol of VUSG002R750PA

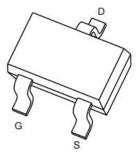
Features

- Leading Trench Technology for Low R_{DS(on)}
- Extending Battery Life

Application

- High Side Load Switch
- Charging Circuit
- Single Cell Battery Applications

Package Type



SOT-323

Figure 2 Package Type of VUSG002R750PA

Ordering Information

Product Name	Package		
VUSG002R750PA	SOT-323		



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Absolute Maximum Ratings (T_A= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DSS}	-20	V
Gate-Source Voltage	V _{GSS}	±8.0	V
Continuous Drain Current Note1,2	I_D	-1.4	A
Pulsed Drain Current	I_{DM}	-5.6	A
Total Power Dissipation Note1	P _D	0.57	W
Junction Temperature	$T_{\rm J}$	150	°C
Storage Temperature	T _{STG}	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	Min	T <mark>y</mark> p	Max	Unit	
Thermal Resistance, Junction-to-Ambient Note1,2	R _θ ЈА		220		°C/W	





75mΩ, -20V, P-Channel Power MOSFET

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Electrical Characteristics (T_A= 25 °C, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_{D}=-250uA$	-20			V
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -16V, V_{GS} = 0V$			-1	uA
Gate-Body Leakage Current	I _{GSS}	$V_{GS} = \pm 8V, V_{DS} = 0V$			±100	nA
Gate Threshold Voltage Note3	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-0.4	-0.7	-1.0	V
		V_{GS} = -4.5V, I_D = -1.0A		50	75	
Static Drain-Source On-Resistance ^{Note3}	R _{DS(ON)}	V_{GS} = -2.5V, I_D = -0.5A		70	105	$m\Omega$
		V_{GS} = -1.8V, I_D = -0.3A		115	156	
Forward tranconductance ^{Note3}	g _{FS}	V_{DS} = -5V, I_{D} = -0.8A	8			S
Dynamic Characteristics						
Input Capacitance	C _{ISS}	$V_{DS} = -10V$		350		pF
Output Capacitance	Coss	V _{GS} =0V		75		pF
Reverse Transfer Capacitance	C _{RSS}	f=1MHz		67		pF
Switching Parameters						
Total Gate Charge	Qg	$V_{DS} = -10V$		8.2		
Gate-source Charge	Q_{gs}	V_{GS} = -4.5 V		1.1		nC
Gate-drain Charge	Q_{gd}	$I_D = -1.4A$		2.0		
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = -10V$		7.0		
Turn-on Rise Time	$t_{\rm r}$	$V_{GS} = -4.5V$		32		***
Turn-off Delay Time	$t_{ m d(off)}$	$I_{D} = -1.4A$		49		ns
Turn-off Fall Time	t _f	$R_G=3\Omega$		55		
Diode Characteristics						
Diode Forward Voltage Note3	V_{SD}	$V_{GS}=0V, I_{S}=-0.3A$			-1.2	V

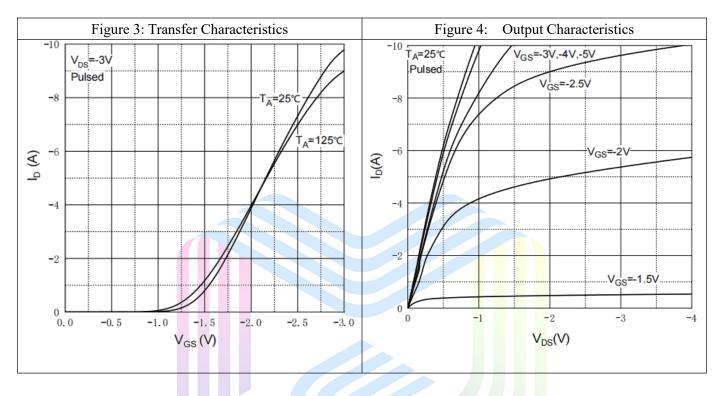
Notes:

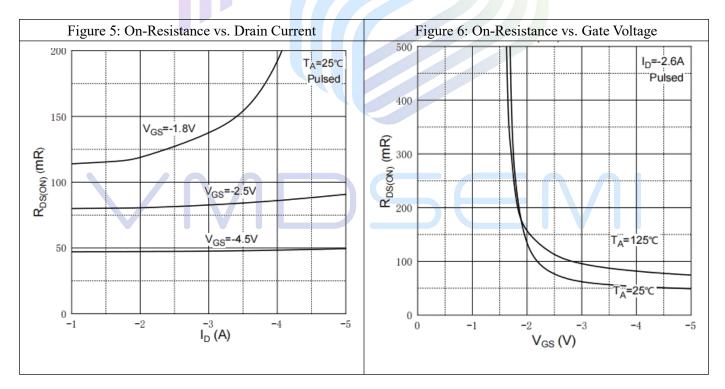
- $1.R_{\theta JA}$ is measured with the device mounted on 1 in² FR4 board with 1oz. single side copper, in a still air environment with $T_A = 25$ °C.
- $2.R_{\theta JA}$ is measured in the steady state
- 3. Pulse test : Pulse width \leq 380µs, duty cycle \leq 2%.

75mΩ, -20V, P-Channel Power MOSFET

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Typical Performance Characteristics

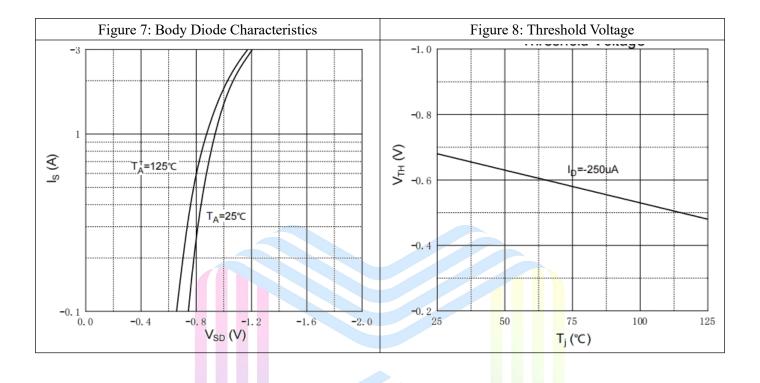


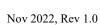




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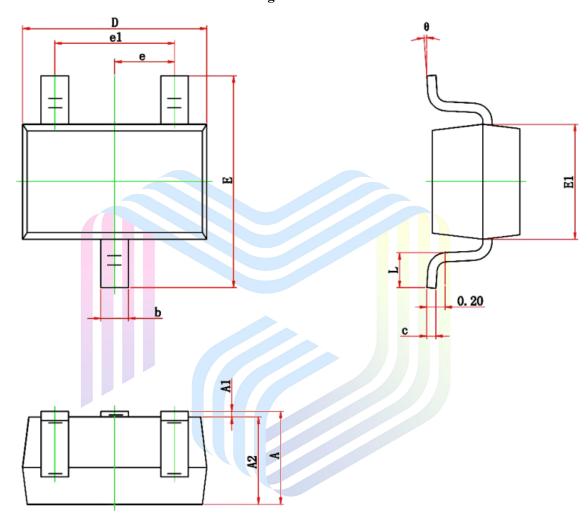






Mechanical Dimensions:

SOT-323 Package Information



Symbol	Dimensions	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	0.900	1.100	0.035	0.043	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.000	0.035	0.039	
b	0.200	0.400	0.008	0.016	
С	0.050	0.150	0.002	0.006	
D	1.900	2.200	0.075	0.087	
E	2.000	2.450	0.079	0.096	
E1	1.150	1.350	0.045	0.053	
е	0.650TYP.		0.026	STYP.	
e1	1.200	1.400	0.047	0.055	
L	0.200	0.460	0.008	0.018	
θ	0°	8°	0°	8°	



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