

VUSG002R11APA

Datasheet

110mΩ, 20V, P-Channel Power MOSFET

General Description

VUSG002R11APA MOSFET is based on unique device design to achieve low R_{DS(ON)}, low gate charge, fast switching and excellent avalanche characteristics.

Symbol

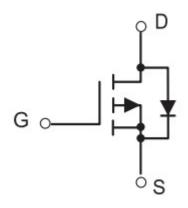


Figure 1 Symbol of VUSG002R11APA

Features

- Leading Trench Technology for Low R_{DS(on)}
- $\blacksquare R_{DS(ON) max} = 110 m\Omega @V_{GS} = -4.5V$
- Extending Battery Life

Application

- Power switching application
- High Side Load Switch
- Charging Circuit
- Single Cell Battery Applications

Package Type

SOT-323

- 1. GATE
- 2. SOURCE
- 3. DRAIN

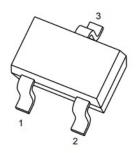


Figure 2 Package Type of VUSG002R11APA

Ordering Information

Product Name	Package
VUSG002R11APA	SOT-323



VUSG002R11APA

Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DSS}	-20	V
Gate-Source Voltage	V _{GSS}	± 8	V
Continuous Drain Current	I_D	-1.4	A
Pulsed Drain Current	I_{DM}	-3.0	A
Max Power Dissipation	P _D	0.29	W
Junction Temperature	$T_{\rm J}$	150	°C
Storage Temperature	T _{STG}	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	Min	Тур	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$		431		°C/W



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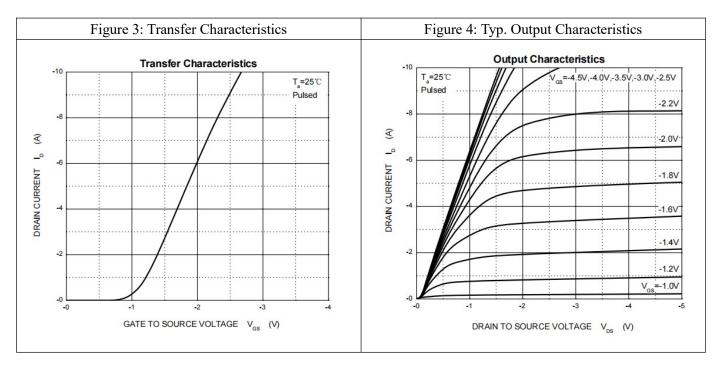
Electrical Characteristics T_J= 25 °C, unless otherwise specified

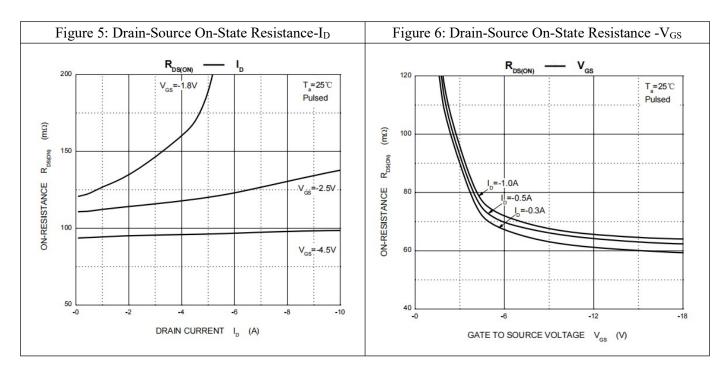
Parameter		Symbol	Test Conditions	Min	Тур	Max	Unit
Statistic Characteristics							
Drain-Source Breakdown Voltag	ge	$\mathrm{BV}_{\mathrm{DSS}}$	V _{GS} =0V, I _D =-250uA	-20			V
Zero Gate Voltage Drain Current		I_{DSS}	V_{DS} =-20V, V_{GS} =0V			-1	uA
Cata Dadre Laglana Comment	Forward	I _{GSSF}	$V_{GS} = -8V$			100	nA
Gate-Body Leakage Current	Reverse	I _{GSSR}	$V_{GS} = 8V$			-100	
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=-250uA$	-0.45	-0.65	-1	V
			V_{GS} = -4.5V, I_{D} = -1.0A		90	110	mΩ
Static Drain-Source On-Resistan	nce	R _{DS(ON)}	V_{GS} = -2.5V, I_{D} = -0.5A		115	140	
			V_{GS} = -1.8V, I_{D} = -0.3A		145	210	
Forward trans conductance		gfs	V_{DS} = -10V, I_{D} = -0.8A		2		S
Dynamic Characteristics							
Input Capacitance		C _{ISS}	$V_{DS} = -8V$		640		pF
Output Capacitance		Coss	$V_{GS}=0V$		120		pF
Reverse Transfer Capacitance		C_{RSS}	f=1MHz		82		pF
Turn-on Delay Time		t _{d(on)}	$V_{DS} = -4V$		6.2		
Rise Time		$t_{\rm r}$	V_{GS} =-4.5V		15		
Turn-off Delay Time		$t_{ m d(off)}$	$I_D = -1.0A$		26		ns
Fall Time		t_{f}	$R_G=6.2\Omega$		18		
Gate Charge Characteristics							
Gate to Source Charge		Q_{gs}	$V_{DS} = -10V$		0.7		
Gate to Drain Charge		Q_{gd}	V_{GS} = -2.5V		1.3		nC
Gate Charge Total		Q_{g}	$I_D = -3.0A$		3.3	6	
Diode Characteristics							
Diode Forward Voltage		V_{SD}	$V_{GS}=0V, I_{SD}=-0.3A$			-1.2	V

Note:

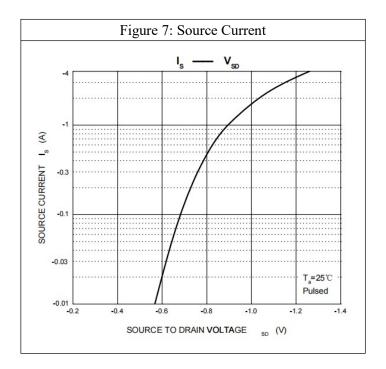
- 1. Pulse Test : pulse width $\leq 300 \mu s$, duty cycle $\leq 2\%$.
- 2. Switching characteristics are independent of operating junction temperatures.
- 3. These parameters have no way to verify.

Typical Performance Characteristics





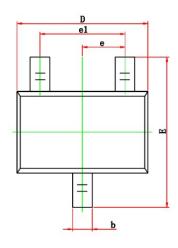
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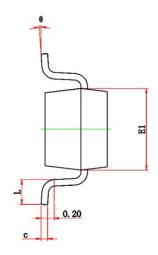


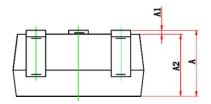


$\overline{110}$ m Ω , 20V, P-Channel Power MOSFET

Mechanical Dimensions:







Cymbal	Dimensions In Millimeters			
Symbol	Min.	Max.		
A	0.900	1.100		
A1	0.000	0.100		
A2	0.900	1.000		
b	0.200	0.400		
c	0.050	0.150		
D	1.900	2.200		
E	2.000	2.450		
E1	1.150 1.350			
e	0.650REF			
e1	1.200	1.400		
L	0.200 0.460			
θ	0° 8°			

Dimensions In Inches					
Min.	Max.				
0.035	0.043				
0.000	0.004				
0.035	0.039				
0.008	0.016				
0.002	0.006				
0.075	0.087				
0.079	0.096				
0.045	0.053				
0.026REF					
0.047	0.055				
0.008	0.018				
0°	8°				



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