

VUTP010R055NB

Datasheet



VUTP010R055NB

General Description

V _{(BR)DSS}	R _{DS(ON)_max}	I_D
100V	5.5mΩ@10V	200A

Symbol

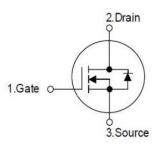


Figure 1 Symbol of VUTP010R055NB

Features

- Low R_{DS(ON)}
- Enhancement mode
- 100% Avalanche Tested
- Pb Free

Application

- PD charger
- Motor driver
- Switching voltage regulator
- DC-DC converter
- Switched mode power supply

Package Type



TO-263

Figure 2 Package Type of VUTP010R055NB

Ordering Information

Product Name	Package		
VUTP010R055NB	TO-263		



VUTP010R055NB

Absolute Maximum Ratings (TA= 25 °C, unless otherwise specified)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		$V_{ m DSS}$	100	V
Gate-Source Voltage		V_{GSS}	±25	V
Continuous Drain Current	$T_C=25$ °C	I_{D}	200	A
Continuous Drain Current	$T_C=100$ °C	ID	142	A
Pulsed Drain Current ^{Note 2}	$T_C=25^{\circ}C$	I _{D.pulse}	800	A
Continuous Diode Forward Current	$T_C=25^{\circ}C$	Is	200	A
Continuous Drain Current	$T_A=25^{\circ}C$	ī	15	A
Continuous Drain Current	$T_A=70$ °C	I _{DSM}	12	A
Max Power Dissipation	$T_{\rm C}=25^{\rm o}{\rm C}$	P _D	375	W
Max Power Dissipation ^{Note 3}	$T_A=25^{\circ}C$	P _{DSM}	2	W
Avalanche Energy, Single Pulse Note 4		Eas	900	mJ
Operation and storage temperature		T _J ,T _{STG}	-55 to 175	°C

Thermal Resistance

Parameter	Symbol	Min	Тур	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$		0.4	0.5	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$		62.5	75	-C/W





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Electrical Characteristics (T_J= 25 °C, unless otherwise specified)

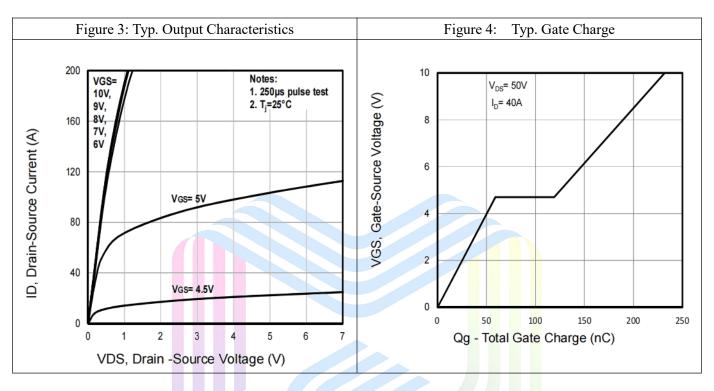
Parameter		Symbol	Test Conditions	Min	Тур	Max	Unit
Statistic Characteristics		•					
Drain-Source Breakdown Voltage		BV _{DSS}	V _{GS} =0V, I _D =250uA	100			V
Zero Gate Voltage Drain Current		I _{DSS}	V _{DS} =100V, V _{GS} =0V			1	uA
Zero Gate Voltage Drain Current T _J = 125 °C			V _{DS} =100V, V _{GS} =0V			100	uA
Gate-Body Leakage Current	Forward	I_{GSSF}	V _{GS} =25V, V _{DS} =0V			100	4
	Reverse	I _{GSSR}	V _{GS} =-25V, V _{DS} =0V			-100	nA
Gate Threshold Voltage		V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250uA	2.4	3	3.6	V
Drain-Source On-Resistance ^{Note1}		D	V 10V I 00 A		4.5	5.5	mΩ
Drain-Source On-Resistance ^{Note}	in-Source On-Resistance ^{Note1} T _J = 100 °C		$V_{GS}=10V, I_{D}=80A$		6.5		
Gate resistance		R_G	f=1 MHz, Open drain	0.2	2.3	5	Ω
Dynamic Characteristics							
Input Capacitance		C _{ISS}	V _{DS} =30V	11065	14755	19625	pF
Output Capacitance		Coss	V _{GS} =0V	500	665	885	pF
Reverse Transfer Capacitance		C _{RSS}	f=1MHz	370	495	660	pF
Turn-on Delay Time		t _{d(on)}	V_{DS} =50V		35		
Rise Time		t _r	I _D =40A		67		
Turn-off Delay Time		$t_{ m d(off)}$	$R_G=3\Omega$		128		ns
Fall Time		$t_{\rm f}$	V _{GS} =10V		64		
Gate Charge Characteristics							
Gate to Source Charge		Q_{gs}	V _{GS} =10V		59	78	
Gate to Drain Charge		Q_{gd}	$V_{DS}=50V$		60	90	nC
Gate Charge Total		Qg	$I_D=40A$		232	309	
Reverse Diode Characteristics	1						
Drain-Source Diode Forward Voltage		V_{SD}	V _{GS} =0V, I _{SD} =80A		0.9	1.2	V
Reverse Recovery Time		t_{rr}	I _{SD} =40A V _{GS} =0V		44	88	ns
Reverse Recovery Charge	Reverse Recovery Charge		di/dt=100A/us		77	154	пC

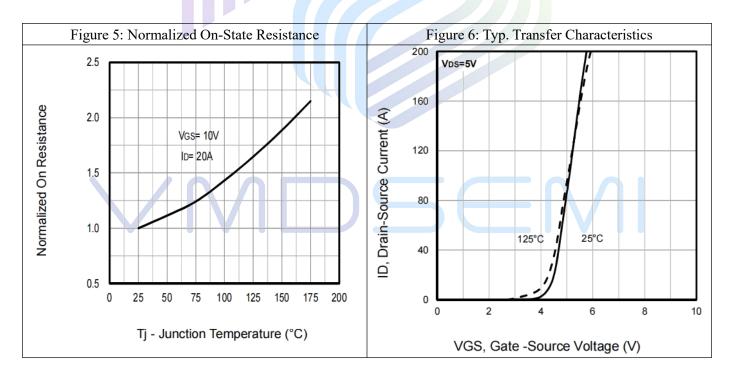
Notes:

- 1. Pulse width≤380µs; duty cycle≤ 2%.
- 2. Repetitive rating; pulse width limited by max junction temperature.
- 3. The power dissipation P_{DSM} is based on $R_{\theta JA}$ and $@T_J = 125^{\circ}C$
- 4. Limited by T_{Jmax} , starting $T_J = 25$ °C, L = 0.5mH, $R_G = 25\Omega$, $I_{AS} = 60$ A, $V_{GS} = 10$ V.

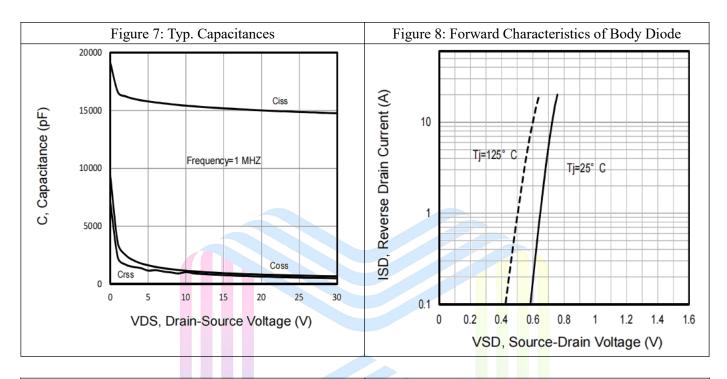
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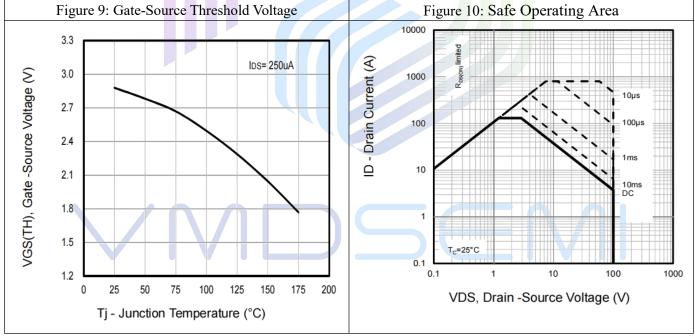
Typical Performance Characteristics





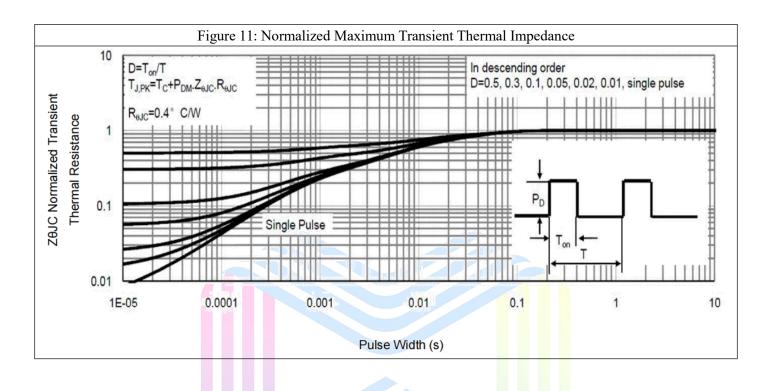
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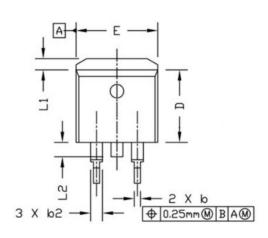
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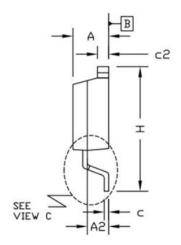


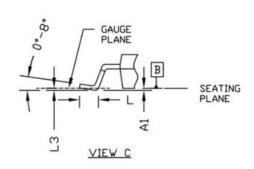


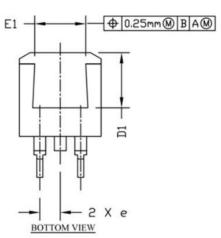
Mechanical Dimensions

Package Information TO-263









Symbol	Dimensions (unit: mm)				
	Min	Тур	Max		
Α	4.400	4.570	4.700		
A1	0.000	0.100	0.200		
A2	2.300	2.400	2.500		
b	0.700	0.800	0.900		
b2	1.200	1.270	1.360		
С	0.381	0.500	0.737		
c2	1.220	1.300	1.350		
D	8.600	9.200	9.300		
D1	6.860				
е	2.540 BSC				
E	9.780	9.880	10.260		
E1	6.225				
Н	14.700	15.100	15.500		
L	2.000	2.550	2.750		
L1	1.000	1.200	1.400		
L2	1.300	1.600	1.700		
L3	0.255 BSC				

Notes:

- 1. Refer to JEDEC TO-263 variation AB
- 2. Dimension "D" & "E" do NOT include mold flash, mold flash shall not exceed 0.127mm per side.



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