

VUTL004R062NA

Datasheet





VUTL004R062NA

General Description

V _{(BR)DSS}	R _{DS(ON)_max}	I_D
40V	6.2mΩ@10V	70.4
	8.5mΩ@4.5V	70A

Symbol

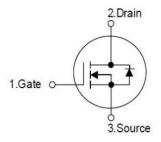


Figure 1 Symbol of VUTL004R062NA

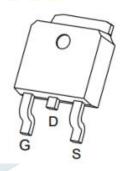
Features

- Trench Technology Power MOSFET
- Low R_{DS(ON)}
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

Application

- Battery protection applications
- Power Switch Application

Package Type



TO-252-2L
Figure 2 Package Type of VUTL004R062NA

Ordering Information

Product Name	Package			
VUTL004R062NA	TO-252-2L			



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Absolute Maximum Ratings (T_A= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current ^{Note1} T _C = 25 °C	I_D	70	
Pulsed Drain Current Note2	I_{DM}	280	A
Avalanche Current ^{Note3}	I _{AS}	29	A
Single Pulsed Avalanche Energy ^{Note3}	Eas	210	mJ
Total Power Dissipation ^{Note5} $T_C=25$ °C	P _D	125	W
Junction Temperature	$T_{\rm J}$	150	°C
Storage Temperature	Tstg	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	Min	T <mark>y</mark> p	Max	Unit
Thermal Resistance, Junction-to-Ambient ^{Note6}	$R_{ heta JA}$		47		°C/W
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$		1		°C/W





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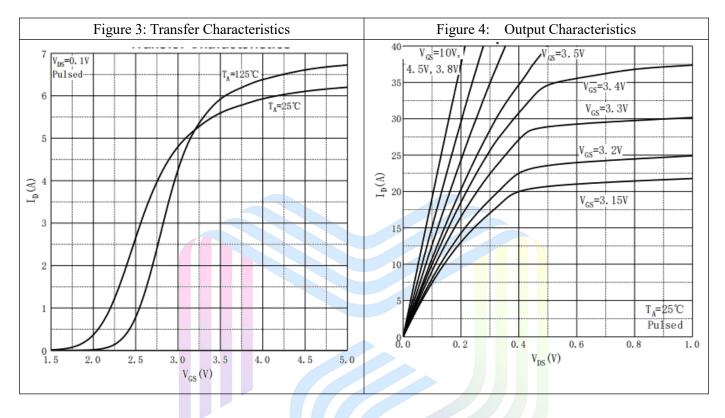
Electrical Characteristics (T_J= 25 °C, unless otherwise specified)

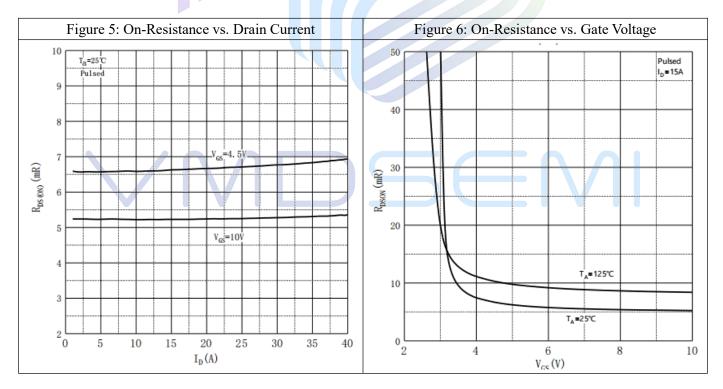
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Statistic Characteristics							
Drain-Source Breakdown Voltage	$\mathrm{BV}_{\mathrm{DSS}}$	$V_{GS}=0V, I_D=250uA$ 40				V	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS}=40V, V_{GS}=0V$			1	uA	
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA	
Gate Threshold Voltage ^{Note4}	$V_{\text{GS(th)}}$	$V_{DS}=V_{GS}$, $I_D=250uA$	1.0	1.7	3.0	V	
Static Drain-Source On-Resistance ^{Note4}		$V_{GS}=10V, I_{D}=15A$		4.8	6.2	mΩ	
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V _{GS} =4.5V, I _D = 10A		5.6	8.5		
Forward Transconductance ^{Note4}	gfs	$V_{DS}=5V, I_{D}=20A$		78		S	
Dynamic Characteristics							
Input Capacitance	C _{ISS}	V _{DS} =20V		3747		pF	
Output Capacitance	Coss	V _{GS} =0V		237		pF	
Reverse Transfer Capacitance	C _{RSS}	f=1MHz		210		pF	
Total Gate Charge	Q_{g}	V _{DS} =20V		67			
Gate-Source Charge	Q_{gs}	$V_{GS}=10V$		12		nC	
Gate-Drain Charge	Q_{gd}	$I_D=15A$		9			
Gate Resistance	Rg	f = 1MHz, Open drain		1.88		Ω	
Switching Parameters							
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=20V$		11			
Turn-on Rise Time	\mathbf{t}_{r}	$V_{GS}=10V$		10		***	
Turn-off Delay Time	$t_{d(off)}$	$R_L=1\Omega$		38		ns	
Turn-off Fall Time	t_{f}	$R_G=3\Omega$		11			
Diode Characteristics							
Diode Forward Voltage Note4	$ m V_{SD}$	$V_{GS}=0V, I_{S}=15A$			1.2	V	

Notes:

- 1. The maximum current rating is limited by package. And device mounted on a large heatsink.
- 2. Pulse Test : Pulse Width $\leq 10\mu s$, duty cycle $\leq 1\%$.
- 3.E_{AS} condition: V_{DD} = 20V, V_{GS} = 10V, L = 0.5mH, R_G =25 Ω Starting T_J = 25 $^{\circ}$ C.
- 4. Pulse Test : Pulse Width $\leq 300 \mu s$, duty cycle $\leq 2\%$.
- 5. The power dissipation P_D is limited by $T_{J(MAX)} = 150^{\circ}C$. And device mounted on a large heatsink
- 6.Device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.

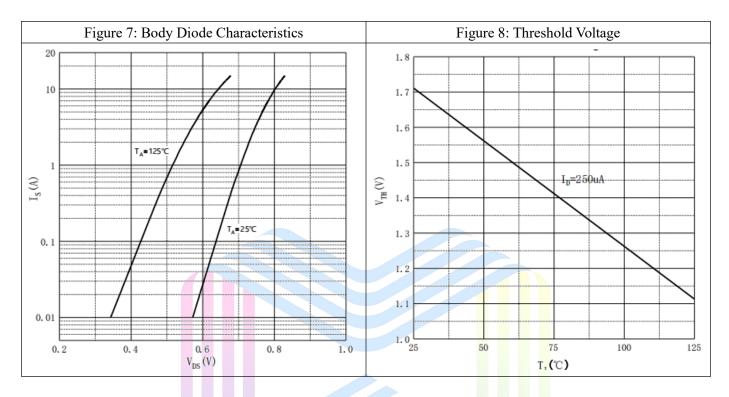
Typical Performance Characteristics

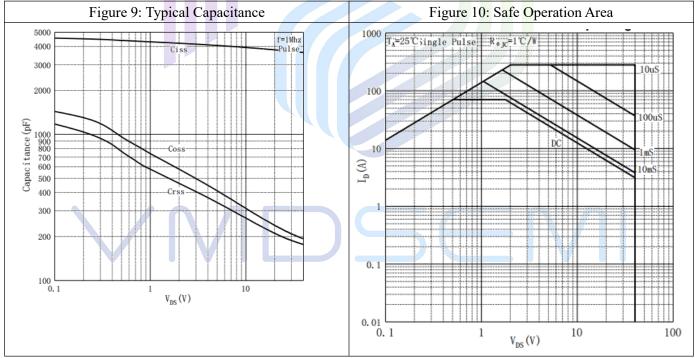






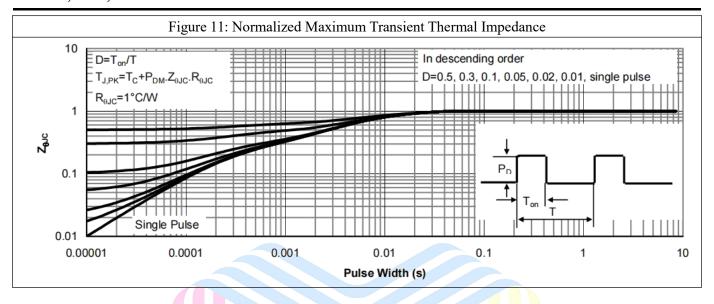
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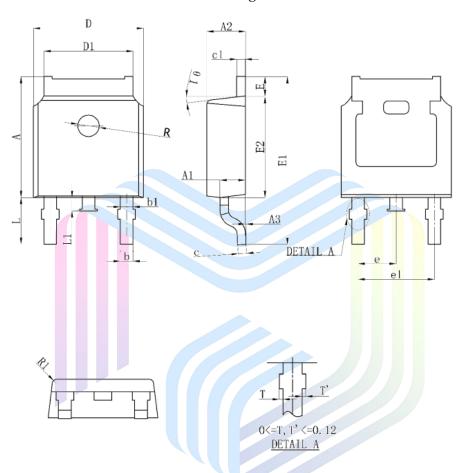






Mechanical Dimensions:

TO-252-2L Package Information



Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	7.050	7.150	0.278	0.281	
A1	0.960	1.060	0.038	0.042	
A2	2.200	2.400	0.087	0.094	
A3	0.000	0.100	0.000	0.004	
b	0.760	OREF	0.03	30REF	
b1	1.000	OREF	0.03	39REF	
С	0.508	BREF	0.02	20REF	
c1	0.508	0.508REF		20REF	
D	6.550	6.650	0.258	0.262	
D1	5.100	5.460	0.201	0.215	
E	0.950	1.050	0.037	0.041	
E1	9.700	10.400	0.382	0.409	
E2	6.000	6.200	0.236	0.244	
е	2.286BSC		0.090BSC		
e1	4.572	4.572REF 0.180RE		30REF	
L	2.650	2.950	0.104	0.116	
L1	0.700	0.900	0.028	0.035	
θ1	7°REF		7°REF		
R	1.300REF		0.051REF		
R1	0.250REF		0.010REF		



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