



VMDSEMI

**VUSC003R520NA**

**Datasheet**



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**General Description**
**Symbol**

$V_{(BR)DSS}$	$R_{DS(ON)_{max}}$	$I_D$
30V	52mΩ@10V	4.0A
	65mΩ@4.5V	
	85mΩ@2.5V	

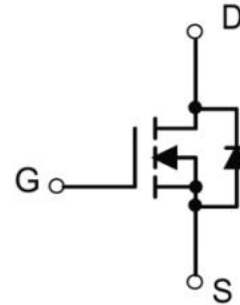


Figure 1 Symbol of VUSC003R520NA

**Features**

- Excellent  $R_{DS(on)}$  and Low Gate Charge
- Trench FET Power MOSFET

**Package Type**
**Application**

- DC/DC Converter
- Load Switch
- Battery Switch

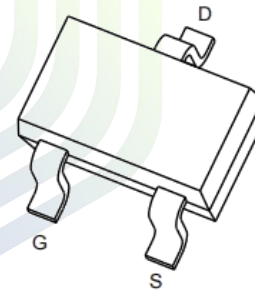

**SOT-23-3L**

Figure 2 Package Type of VUSC003R520NA

**Ordering Information**

Product Name	Package
VUSC003R520NA	SOT-23-3L

**Absolute Maximum Ratings** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DSS}$	30	V
Gate-Source Voltage	$V_{GSS}$	$\pm 12$	V
Continuous Drain Current <sup>Note1</sup>	$I_D$	4	A
Pulsed Drain Current <sup>Note2</sup>	$I_{DM}$	15	A
Total Power Dissipation <sup>Note4</sup>	$P_D$	0.45	W
Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55 to 150	$^\circ\text{C}$

**Thermal Resistance**

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Ambient <sup>Note5</sup>	$R_{\theta JA}$		313		$^\circ\text{C}/\text{W}$



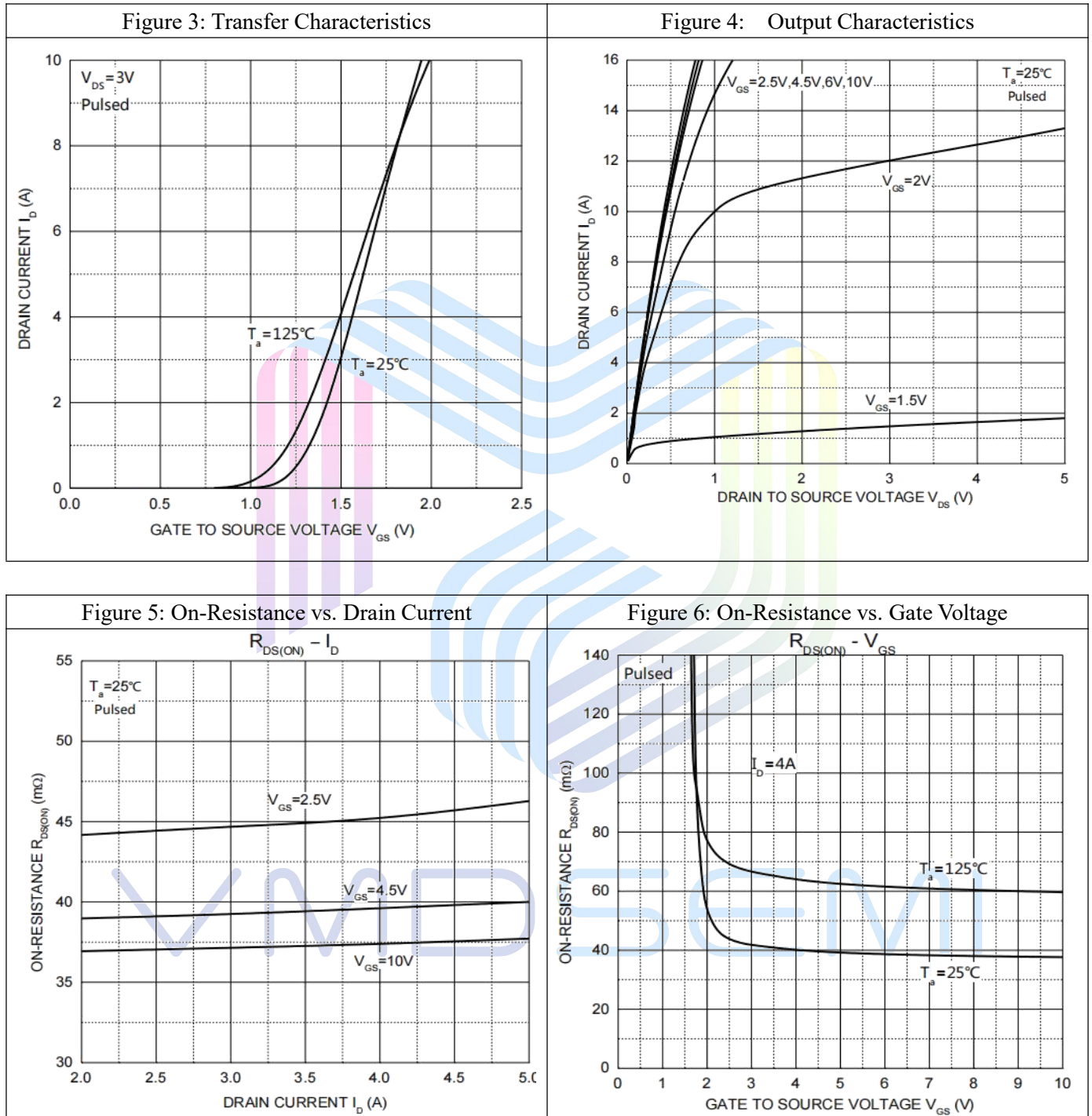
**Electrical Characteristics** ( $T_A = 25\text{ }^\circ\text{C}$ , unless otherwise specified)

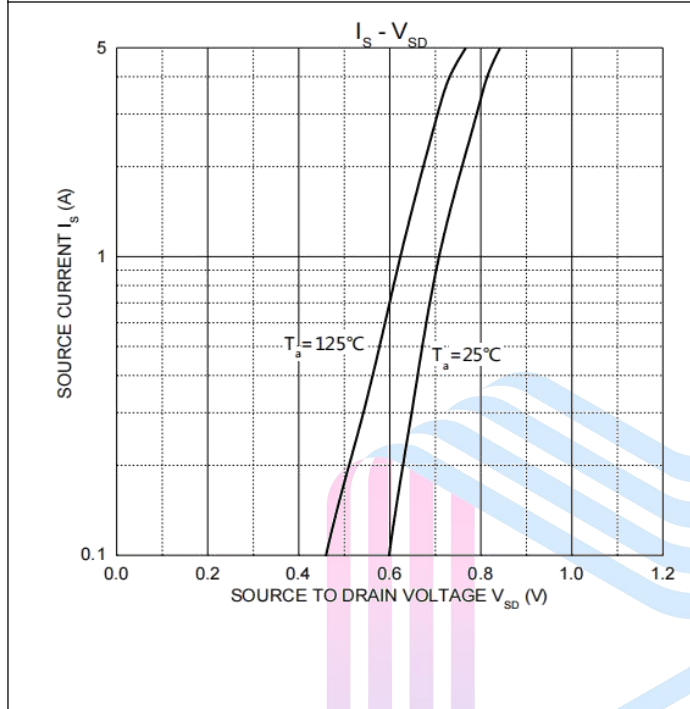
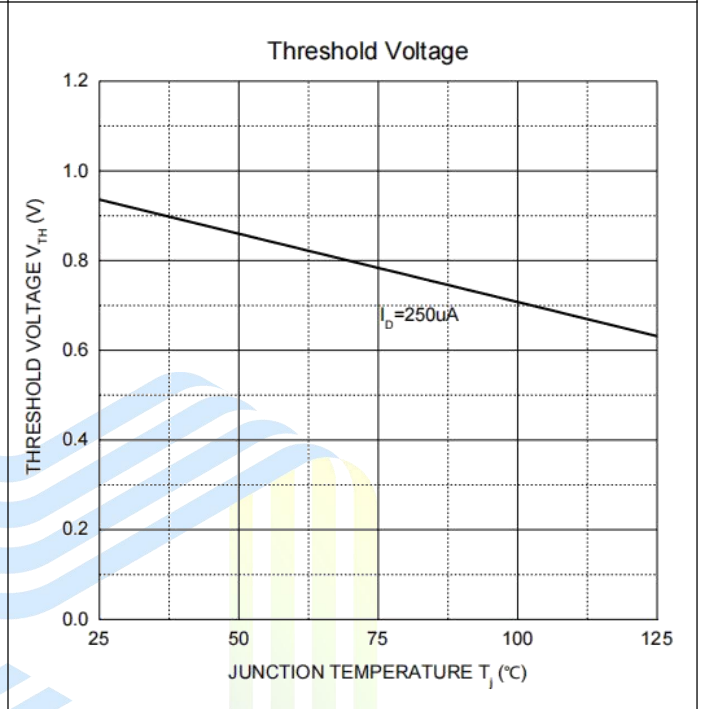
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Statistic Characteristics</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	30			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=30V, V_{GS}=0V$			1	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS} = \pm 12V, V_{DS}=0V$			$\pm 100$	nA
Gate Threshold Voltage <sup>Note3</sup>	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	0.9	1.5	V
Static Drain-Source On-Resistance <sup>Note3</sup>	$R_{DS(on)}$	$V_{GS}=10V, I_D=4A$		38	52	mΩ
		$V_{GS}=4.5V, I_D=3A$		40	65	
		$V_{GS}=2.5V, I_D=2A$		48	85	
Forward Transconductance <sup>Note3</sup>	$g_{FS}$	$V_{DS}=5V, I_D=3.6A$		13		S
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{ISS}$	$V_{DS}=15V$		389		pF
Output Capacitance	$C_{OSS}$	$V_{GS}=0V$		54		pF
Reverse Transfer Capacitance	$C_{RSS}$	$f=1MHz$		40		pF
Total gate charge	$Q_g$	$V_{DS}=15V$		4.4		nC
Gate-source charge	$Q_{gs}$	$V_{GS}=4.5V$		0.7		nC
Gate-drain charge	$Q_{gd}$	$I_D=4A$		1.3		nC
Gate Resistance	$R_g$	$f=1MHz, \text{open drain}$		3.5		Ω
<b>Switching Parameters</b>						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=15V$		3.5		ns
Turn-on Rise Time	$t_r$	$V_{GS}=10V$		1.2		
Turn-off Delay Time	$t_{d(off)}$	$R_L=3.75\Omega$		22		
Turn-off Fall Time	$t_f$	$R_G=6\Omega$		2.2		
<b>Diode Characteristics</b>						
Diode Forward Voltage <sup>Note3</sup>	$V_{SD}$	$V_{GS}=0V, I_S=1.0A$			1.0	V
Source-Drain Diode Current	$I_S$	$T_C=25\text{ }^\circ\text{C}$			1.5	A

Notes :

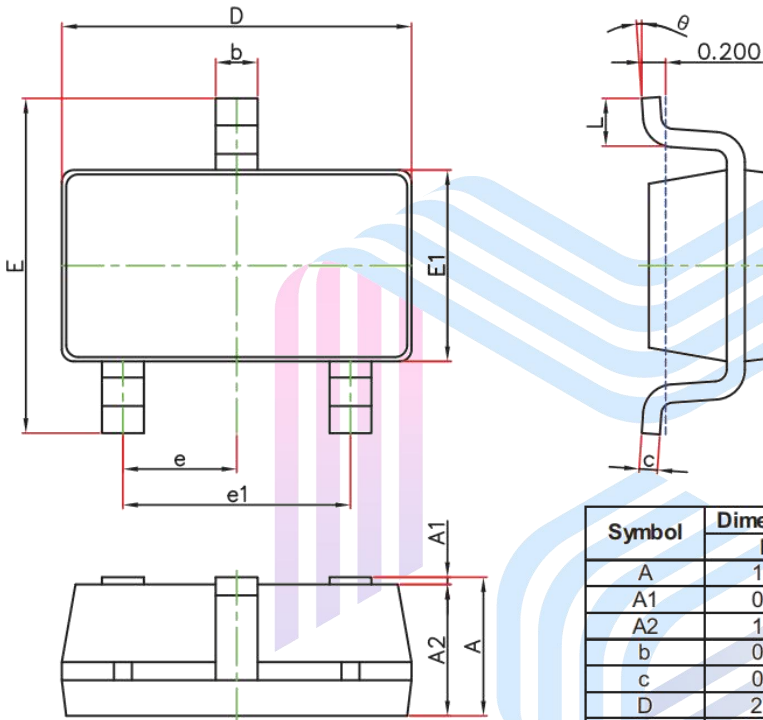
- 1.The maximum current rating is limited by package.
- 2.Pulse Test : Pulse Width  $\leq 10\mu s$ , duty cycle  $\leq 1\%$ .
- 3.Pulse Test : Pulse Width  $\leq 380\mu s$ , duty cycle  $\leq 2\%$ .
- 4.The power dissipation  $P_D$  is limited by  $T_{J(MAX)} = 150^\circ\text{C}$ .
- 5.Device mounted on  $1in^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .

## Typical Performance Characteristics



**Figure 7: Body Diode Characteristics**

**Figure 8: Threshold Voltage**


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**Mechanical Dimensions:**
**SOT-23-3L Package Information**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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