

VUTL003R070NA

Datasheet





VUTL003R070NA

General Description

V _{(BR)DSS}	R _{DS(ON)_max}	I_D
30V	7.0mΩ@10V	50 4
	11.5mΩ@4.5V	50A

Symbol

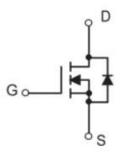


Figure 1 Symbol of VUTL003R070NA

Features

- Trench Technology Power MOSFET
- Low Gate Charge
- Low Gate Resistance
- Low R_{DS(ON)}
- 100% UIS Tested

Package Type



Application

- Power Switch
- DC/DC Converter

TO-252-2L

Figure 2 Package Type of VUTL003R070NA

Ordering Information

Product Name	Package		
VUTL003R070NA	TO-252-2L		



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Absolute Maximum Ratings (T_A= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	$V_{ m DSS}$	30	V	
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current Notel	I_D	50		
Continuous Drain Current Notel		15	A	
Pulsed Drain Current Note2	I_{DM}	130		
Avalanche Current ^{Note3}	I_{AS}	20	A	
Single Pulsed Avalanche Energy ^{Note3}	Eas	100	mJ	
Total Power Dissipation Note5	$T_{\rm C}=25~{\rm ^{o}C}$	D	35	W
Total Power Dissipation Note6	$T_A=25$ °C	P_{D}	3.5	W
Junction Temperature		TJ	150	°C
Storage Temperature		T _{STG}	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Ambient Note6	$R_{\theta JA}$		35		°C/W
Thermal Resistance, Junction-to-Case	Rejc		5		°C/W





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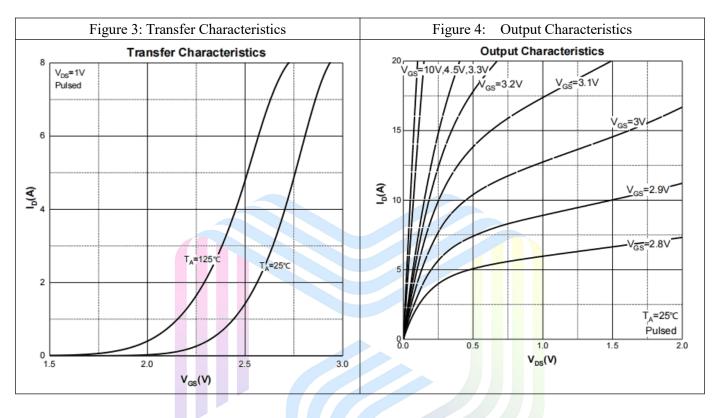
Electrical Characteristics (T_J= 25 °C, unless otherwise specified)

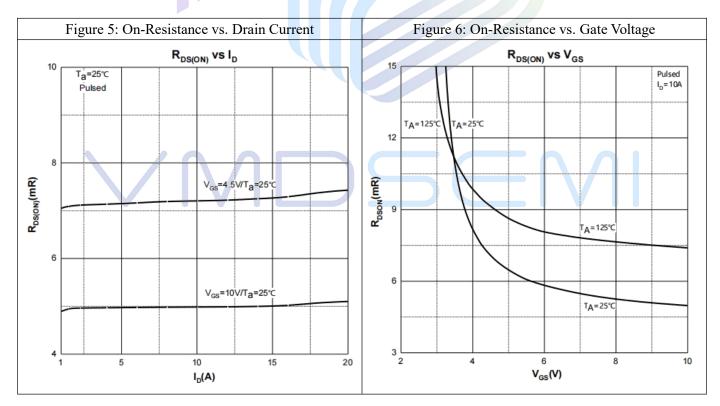
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Statistic Characteristics							
Drain-Source Breakdown Voltage	$\mathrm{BV}_{\mathrm{DSS}}$	$V_{GS}=0V, I_{D}=250uA$	30			V	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 24V, V_{GS} = 0V$			1	uA	
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA	
Gate Threshold Voltage ^{Note4}	$V_{\text{GS}(\text{th})}$	$V_{DS}=V_{GS}$, $I_D=250uA$	1.0	1.6	3.0	V	
Static Drain-Source On-Resistance ^{Note4}	D	$V_{GS}=10V, I_{D}=10A$		5.5	7.0	mΩ	
Static Diani-Source On-Resistance	$R_{DS(ON)}$	V_{GS} =4.5V, I_{D} = 10A		7.5	11.5		
Dynamic Characteristics							
Input Capacitance	C _{ISS}	$V_{DS}=15V$		1666		pF	
Output Capacitance	Coss	V _{GS} =0V		222		pF	
Reverse Transfer Capacitance	C_{RSS}	f=1MHz		176		pF	
Total Gate Charge	Q_{g}	V _{DS} =15V		15			
Gate-Source Charge	Q_{gs}	V _{GS} =4.5V		3.8		nC	
Gate-Drain Charge	Q_{gd}	$I_D=20A$		5.5			
Gate Resistance	Rg	f = 1MHz, Open drain		1.48		Ω	
Switching Parameters							
Turn-on Delay Time	$t_{d(on)}$	V _{DD} = 15V		6			
Turn-on Rise Time	$t_{\rm r}$	$V_{GS}=10V$		10		ng	
Turn-off Delay Time	$t_{d(off)}$	$R_L=0.75\Omega$		16		ns	
Turn-off Fall Time	t_{f}	$R_G=3\Omega$		4			
Diode Characteristics							
Diode Forward Voltage Note4	V_{SD}	$V_{GS}=0V, I_{S}=10A$			1.2	V	

Notes:

- 1. The maximum current rating is limited by package. And device mounted on a large heatsink
- 2. Pulse Test : Pulse Width $\leq 10\mu s$, duty cycle $\leq 1\%$.
- $3.E_{AS}$ condition: $V_{DD} = 25V$, $V_{GS} = 10V$, L = 0.5mH, $R_G = 25\Omega$ Starting $T_J = 25$ °C.
- 4. Pulse Test : Pulse Width $\leq 300 \mu s$, duty cycle $\leq 2\%$.
- 5. The power dissipation P_D is limited by $T_{J(MAX)} = 150^{\circ}C$. And device mounted on a large heatsink
- 6.Device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.

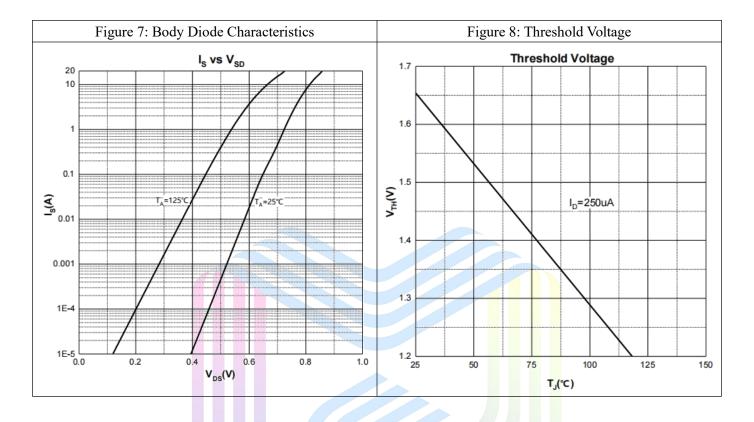
Typical Performance Characteristics

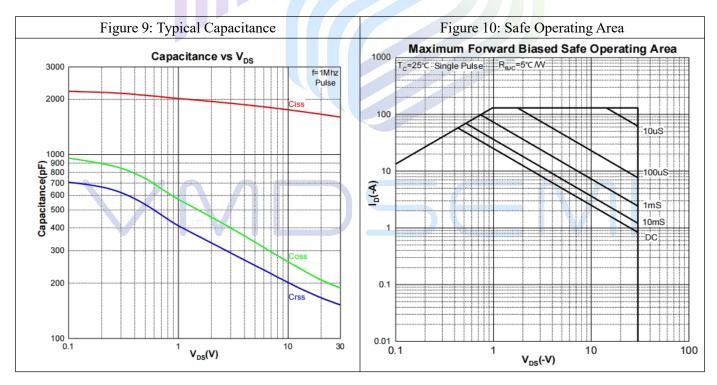






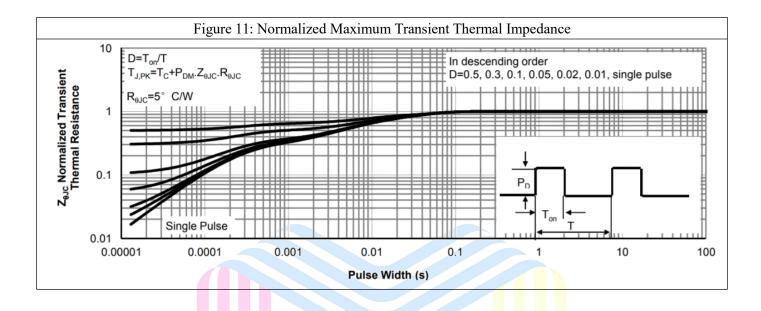
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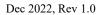






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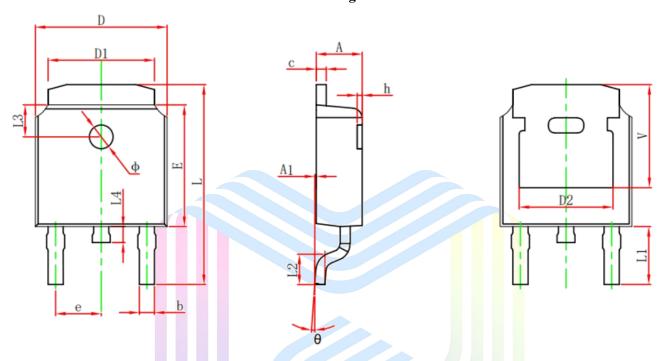






Mechanical Dimensions:

TO-252-2L Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.635	0.770	0.025	0.030	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830	REF.	0.190	REF.	
E	6.000	6.200	0.236	0.244	
e	2.186	2.386	0.086	0.094	
L	9.712	10.312	0.382	0.406	
L1	2.900	REF.	0.114	REF.	
L2	1.400	1.700	0.055	0.067	
L3	1.600	REF.	0.063	REF.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.250	REF.	0.207	REF.	



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