

VUDF003R060NA

Datasheet

6.0mΩ, 30V, N-Channel Power MOSFET

General Description

VUDF003R060NA MOSFET is based on unique device design to achieve low $R_{DS(ON)}$, excellent package for heat dissipation.

Symbol

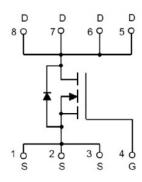


Figure 1 Symbol of VUDF003R060NA

Features

- $\blacksquare R_{DS(ON) max} = 6.0 \text{m}\Omega @V_{GS} = 10V$
- $\blacksquare R_{DS(ON) max} = 8.0 \text{m}\Omega(@)V_{GS} = 8V$
- Trench Power LV MOSFET technology
- Moisture Sensitivity Level 3
- High density cell design for low R_{DS(ON)}
- Epoxy Meets UL 94 V-0 Flammability Rating

Package Type



Figure 2 Package Type of VUDF003R060NA

Application

- High current load applications
- Load Switch
- Hard switched and high frequency circuits
- Uninterruptible power supply

Ordering Information

Product Name	Package
VUDF003R060NA	DFN3333-8L



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Absolute Maximum Ratings (T_A= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DSS}	30	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current $T_C = 25^{\circ}C$	т	50	A
Continuous Drain Current $T_C = 100$ °C	I_{D}	32	A
Pulsed Drain Current ^{Note1}	I_{DM}	190	A
Total Power Dissipation $T_C = 25^{\circ}C$	D	25	W
Total Power Dissipation T _C = 100°C	$P_{\rm D}$	10	W
Single Pulse Avalanche Energy Note2	Eas	112.5	mJ
Junction Temperature	$T_{\rm J}$	150	°C
Storage Temperature	T _{STG}	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	Min	Тур	Max	Unit
Thermal Resistance, Junction-to-Case Note3	$R_{ heta JC}$		5		°C/W

Notes:

- 1. Pulse Test: Pulse Width≤300us, Duty Cycle≤0.5%
- 2. $T_J=25$ °C, $V_{DD}=25$ V, $V_G=10$ V, L=1mH, IAS=15A
- 3. $R_{\theta JA}$ is the sum of the junction-to-Case and Case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design, while $R_{\theta JA}$ is determined by the board design. The maximum rating presented here is based on mounting on a 1 in² pad of 2oz copper.



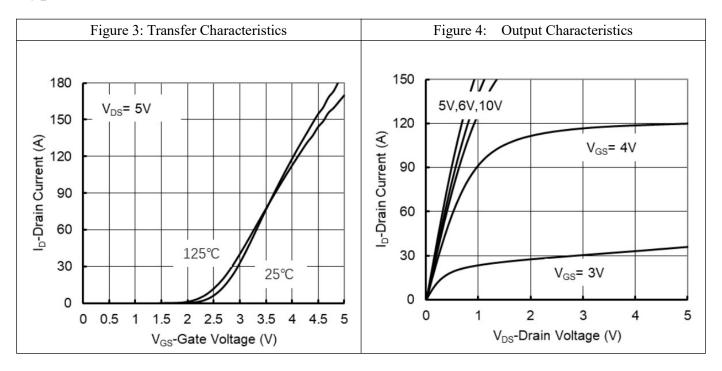
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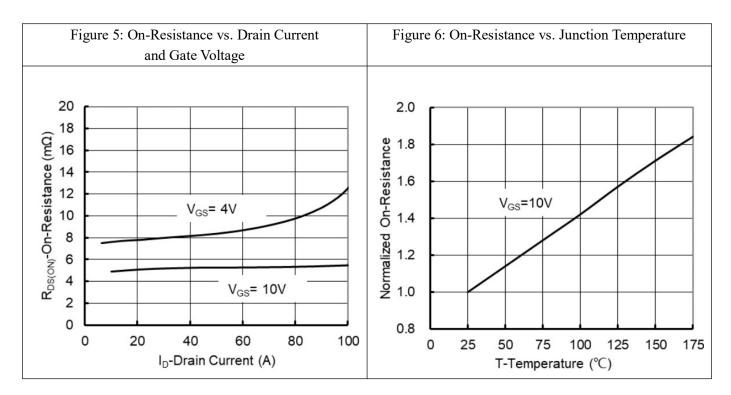
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Electrical Characteristics (T_J= 25 °C, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D = 250uA	30			V
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30V, V_{GS} = 0V$			1	uA
Gate-Body Leakage Current	I _{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_D=250uA$	1.0	1.5	2.5	V
Static Drain-Source On-Resistance	D	$V_{GS} = 10V, I_D = 15A$		3.9	6.0	mΩ
Static Drain-Source On-Resistance	R _{DS(ON)}	V_{GS} = 4.5V, I_{D} = 15A		6.0	8.0	
Dynamic Characteristics						
Input Capacitance	C _{ISS}	$V_{DS}=15V$		2191		pF
Output Capacitance	Coss	$V_{GS}=0V$		300		pF
Reverse Transfer Capacitance	C _{RSS}	f=1MHz		247		pF
Switching Parameters						
Gate to Source Charge	Q_{gs}	$V_{DS}=15V$		8.8		
Gate to Drain Charge	Q_{gd}	$V_{GS}=10V$		9.2		nC
Gate Charge Total	Qg	$I_D=20A$		46.3		
Reverse Recovery Charge	Qrr	$I_F = 20A$		1.6		пC
Reverse Recovery Time	t_{rr}	di/dt=500A/us		11		
Turn-on Delay Time	t _{d(on)}	$V_{DD}=15V$		11		
Turn-on Rise Time	$t_{\rm r}$	$V_{GS}=10V$		80		ns
Turn-off Delay Time	t _{d(off)}	$R_L=0.75\Omega$		39		
Turn-off Fall Time	t_{f}	$R_{GEN}=3\Omega$		92		
Diode Characteristics						
Diode Forward Voltage Note3	V_{SD}	$V_{GS}=0V, I_{S}=20A$			1.2	V
Maximum Body-Diode Continuous Current	Is				50	A

Typical Performance Characteristics







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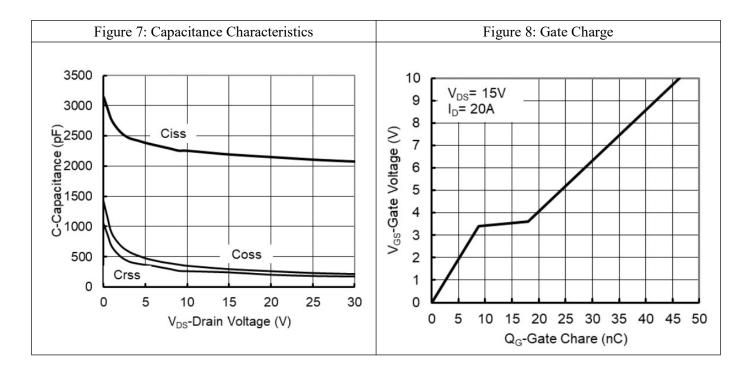
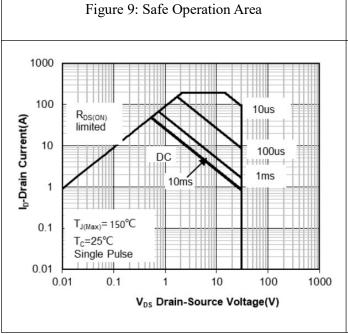
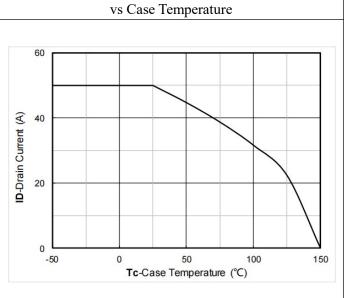


Figure 10:



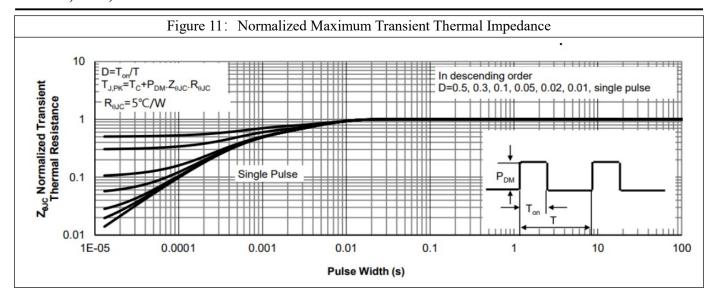


Maximum Continuous Drain Current



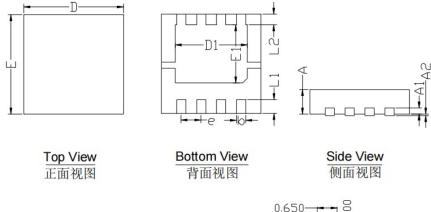
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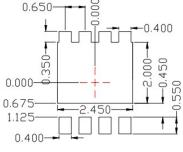




Mechanical Dimensions:



- Note: 1.Controlling dimension:in millimeters.
- 2.General tolerance:+/-0.10mm.
- 3. The pad layout is for reference purposes only.



Suggested Solder Pad Layout Top View

Symbol	Dimens	nit:mm)			
Symbol	Min.	Max.			
D	3.15	3.25	3.35		
Е	3.15	3.25	3.35		
A	0.70	0.80	0.90		
A1	0.20BSC				
A2			0.10		
D1	2.20	2.35	2.50		
E1	1.80	1.90	2.00		
L1	0.35	0.45	0.55		
L2	0.35BSC				
b	0.20	0.30	0.40		
e	0.65BSC				



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