

### VUTL003R021NA

Datasheet

# VMDSEMI



#### **General Description**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)_max</sub>	I <sub>D</sub>
30V	2.1mΩ@10V	150 4
	2.9mΩ@4.5V	150A

#### Symbol

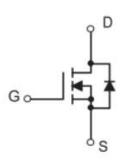


Figure 1 Symbol of VUTL003R021NA

#### **Features** Package Type Trench Technology Power MOSFET Low Gate Charge Low Gate Resistance Low R<sub>DS(ON)</sub> С 100% UIS Tested Application TO-252-2L Power Switch Application DC/DC Converter Package Type of VUTL003R021NA Figure 2 **Ordering Information**

Product Name	Package
VUTL003R021NA	TO-252-2L

#### VUTL003R021NA



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#### Absolute Maximum Ratings (T<sub>A</sub>= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage		V <sub>DSS</sub>	30	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
Continuous Drain Current Note1	$T_{\rm C}=25$ °C	т	150	
Continuous Drain Current Note1	$T_A = 25 \text{ °C}$	ID	100	A
Pulsed Drain Current Note2		I <sub>DM</sub>	600	
Avalanche Current <sup>Note3</sup>		I <sub>AS</sub>	69	A
Single Pulsed Avalanche Energy <sup>Note3</sup>		E <sub>AS</sub>	1190	mJ
Total Power Dissipation Note5	$T_{\rm C}=25$ °C	PD	156	W
Junction Temperature		TJ	150	°C
Storage Temperature		Tstg	-55 to 150	°C

#### **Thermal Resistance**

Parameter	Symbol	Min	Т <mark>у</mark> р	Max	Unit
Thermal Resistance, Junction-to-Ambient Note6	R <sub>0JA</sub>		50		°C/W
Thermal Resistance, Junction-to-Case	Røjc		0.8		°C/W

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Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Statistic Characteristics				•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS}=0V, I_D=250uA$	30			V	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS}= 24V, V_{GS}=0V$			1	uA	
Gate-Body Leakage Current	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA	
Gate Threshold Voltage <sup>Note4</sup>	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	1.0	1.5	3.0	V	
Static Drain-Source On-Resistance <sup>Note4</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A		1.8	2.1	mΩ	
Static Drain-Source On-Resistance		V <sub>GS</sub> =4.5V, I <sub>D</sub> = 10A		2.2	2.9		
Forward Transconductance <sup>Note4</sup>	g <sub>FS</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> = 10A	10	20		S	
Dynamic Characteristics							
Input Capacitance	CISS	V <sub>DS</sub> =15V		7560			
Output Capacitance	Coss	V <sub>GS</sub> =0V		1064		pF	
Reverse Transfer Capacitance	Crss	f=1MHz		1081		_	
Total Gate Charge	Qg	V <sub>DS</sub> =15V		150			
Gate-Source Charge	Qgs	V <sub>GS</sub> =10V		20		nC	
Gate-Drain Charge	Qgd	$I_D = 20A$		45			
Gate Resistance	Rg	f = 1MHz, Open drain		1.2		Ω	
Switching Parameters							
Turn-on Delay Time	t <sub>d(on)</sub>	$V_{DD}=15V$		28			
Turn-on Rise Time	tr	$V_{GS}=10V$		36			
Turn-off Delay Time	t <sub>d(off)</sub>	$R_{L}=0.75\Omega$		75	5 ns		
Turn-off Fall Time	tf	$R_{G}=3\Omega$		25			
Diode Characteristics							
Diode Forward Voltage Note4	V <sub>SD</sub>	$V_{GS}=0V, I_{S}=10A$			1.2	V	

#### Electrical Characteristics (T<sub>J</sub>= 25 °C, unless otherwise specified)

Notes :

1. The maximum current rating is limited by package. And device mounted on a large heatsink

2.Pulse Test : Pulse Width  $\leq 10\mu s$ , duty cycle  $\leq 1\%$ .

3.E<sub>AS</sub> condition:  $V_{DD} = 25V$ ,  $V_{GS} = 10V$ , L = 0.5mH,  $R_G = 25\Omega$  Starting  $T_J = 25^{\circ}C$ .

4.Pulse Test : Pulse Width  $\leq$  300µs, duty cycle  $\leq$  2%.

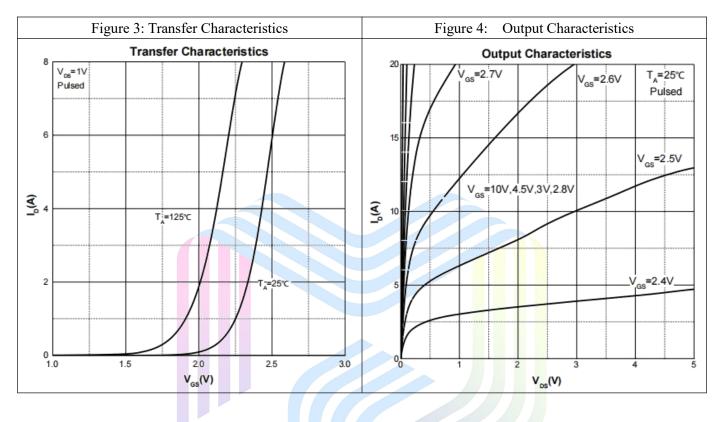
5. The power dissipation  $P_D$  is limited by  $T_{J(MAX)} = 150^{\circ}$ C. And device mounted on a large heatsink

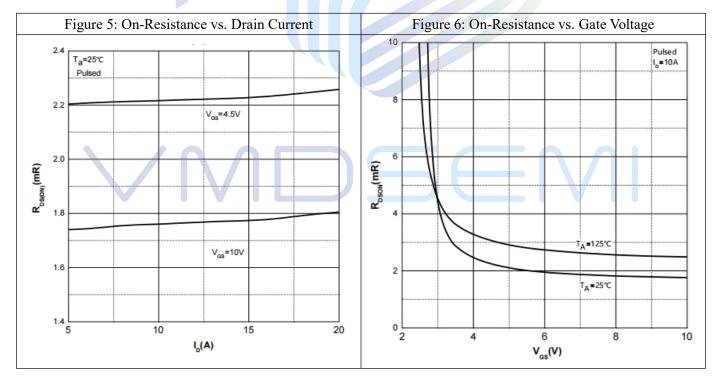
6.Device mounted on  $1in^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^{\circ}C$ .



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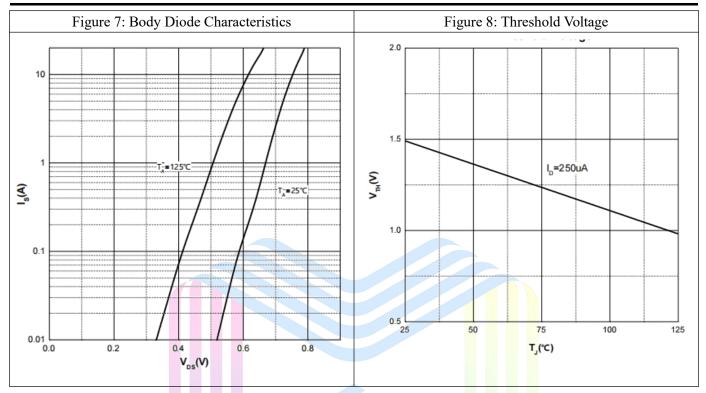
#### **Typical Performance Characteristics**

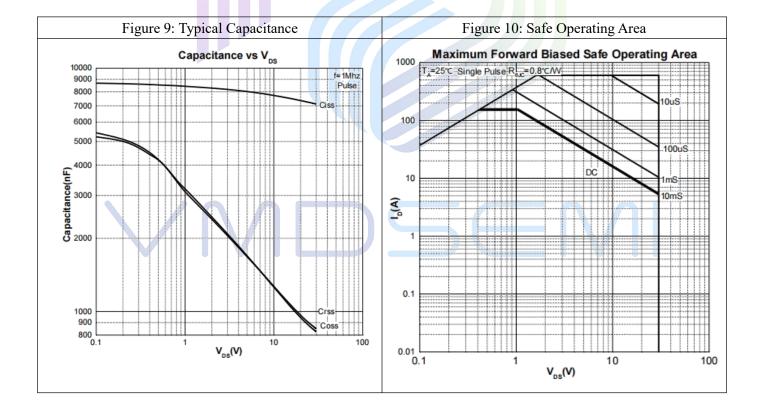






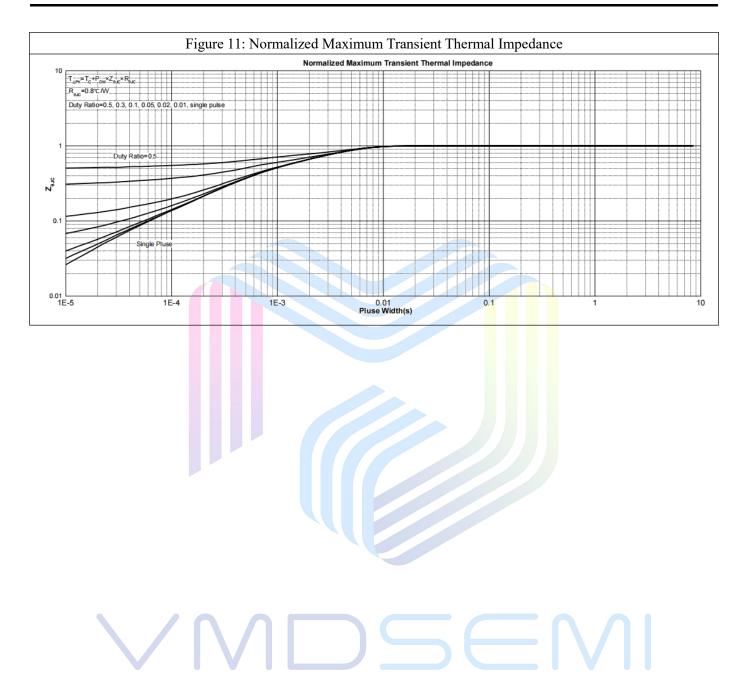
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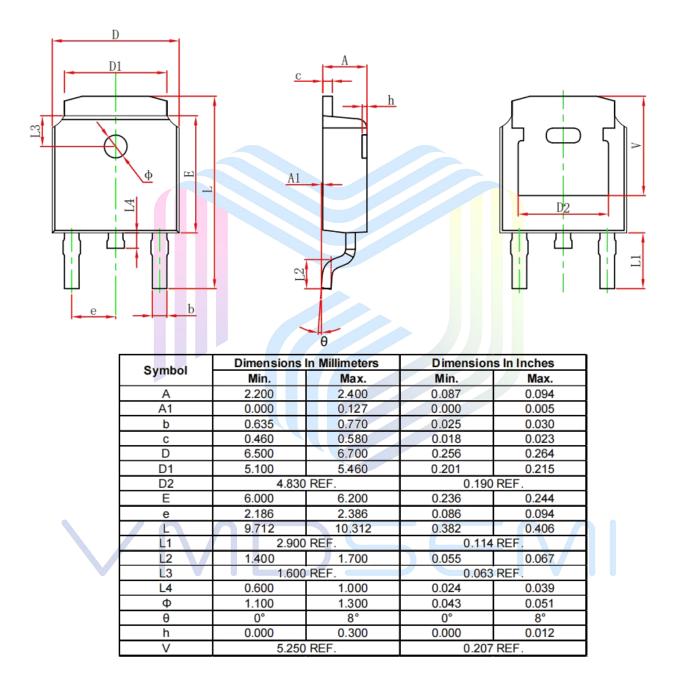




#### VUTL003R021NA

#### **Mechanical Dimensions:**

**TO-252-2L Package Information** 





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