VUPB003R041NA

Datasheet

VMDSEMI

General Description

$V_{(BR)DSS}$	$R_{DS(ON)_max}$	I_D
30V	4.1mΩ@10V	80A
	6.5mΩ@4.5V	

Symbol

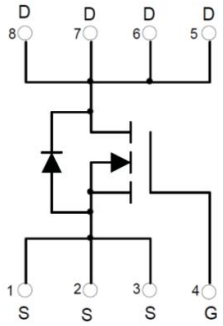
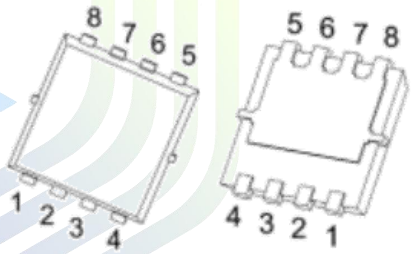


Figure 1 Symbol of VUPB003R041NA

Features

- Trench Technology Power MOSFET
- Low Gate Charge
- Low Gate Resistance
- Low $R_{DS(ON)}$
- 100% UIS Tested

Package Type



PDFN5X6-8L

Figure 2 Package Type of VUPB003R041NA

Application

- Power Switch
- Battery protection applications

Ordering Information

Product Name	Package
VUPB003R041NA	PDFN5X6-8L

Absolute Maximum Ratings (T_A= 25 ºC, unless otherwise specified)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	30	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current Note1	T_C= 25 ºC	I_D	80	A
Continuous Drain Current Note1	T_C= 100 ºC		55	
Continuous Drain Current Note6	T_A= 25 ºC		22	
Pulsed Drain Current Note2		I_{DM}	240	
Single Pulsed Avalanche Energy Note3		E_{AS}	196	mJ
Avalanche Current Note3		I_{AS}	28	A
Total Power Dissipation Note5	T_C= 25 ºC	P_D	84	W
Total Power Dissipation Note6	T_A= 25 ºC	P_D	2.5	W
Junction Temperature		T_J	150	ºC
Storage Temperature		T_{STG}	-55 to 150	ºC

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Ambient Note6	$R_{\theta JA}$		50		ºC/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$		1.5		ºC/W

Electrical Characteristics (T_J= 25 ºC, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D= 250uA	30			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}= 24V, V_{GS}=0V			1	uA
Gate-Body Leakage Current	I_{GSS}	V_{GS} = ±20V, V_{DS}= 0V			±100	nA
Gate Threshold Voltage[Note4]	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	1.0	1.7	3.0	V
Static Drain-Source On-Resistance[Note4]	$R_{DS(ON)}$	V_{GS}=10V, I_D= 10A		3.1	4.1	mΩ
		V_{GS}=4.5V, I_D= 10A		4.6	6.5	
Forward Transconductance[Note4]	g_{FS}	V_{DS}=10V, I_D= 10A	10	20		S
Dynamic Characteristics						
Input Capacitance	C_{ISS}	V_{DS}=15V		3380		pF
Output Capacitance	C_{OSS}	V_{GS}=0V		620		pF
Reverse Transfer Capacitance	C_{RSS}	f=1MHz		300		pF
Total Gate Charge	Q_g	V_{DS}=15V		33		nC
Gate-Source Charge	Q_{gs}	V_{GS}=4.5V		10.6		
Gate-Drain Charge	Q_{gd}	I_D= 20A		20		
Gate Resistance	Rg	f = 1MHz, Open drain		1.9		Ω
Switching Parameters						
Turn-on Delay Time	$t_{d(on)}$	V_{DD}= 15V		13		ns
Turn-on Rise Time	t_r	V_{GS}= 10V		17		
Turn-off Delay Time	$t_{d(off)}$	R_L=0.75Ω		42		
Turn-off Fall Time	t_f	R_G=3Ω		16		
Diode Characteristics						
Diode Forward Voltage [Note4]	V_{SD}	V_{GS}=0V, I_S= 10A			1.2	V

Notes :

1.The maximum current rating is limited by package.And device mounted on a large heatsink

2.Pulse Test : Pulse Width ≤ 10μs, duty cycle ≤ 1%.

3.E_{AS} condition: V_{DD} = 25V,V_{GS} = 10V, L = 0.5mH, R_G = 25Ω Starting T_J = 25°C.

4.Pulse Test : Pulse Width ≤ 300μs, duty cycle ≤ 2%.

5.The power dissipation P_D is limited by $T_{J(MAX)}$ = 150°C.And device mounted on a large heatsink

6.Device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.

Typical Performance Characteristics

Figure 3: Transfer Characteristics

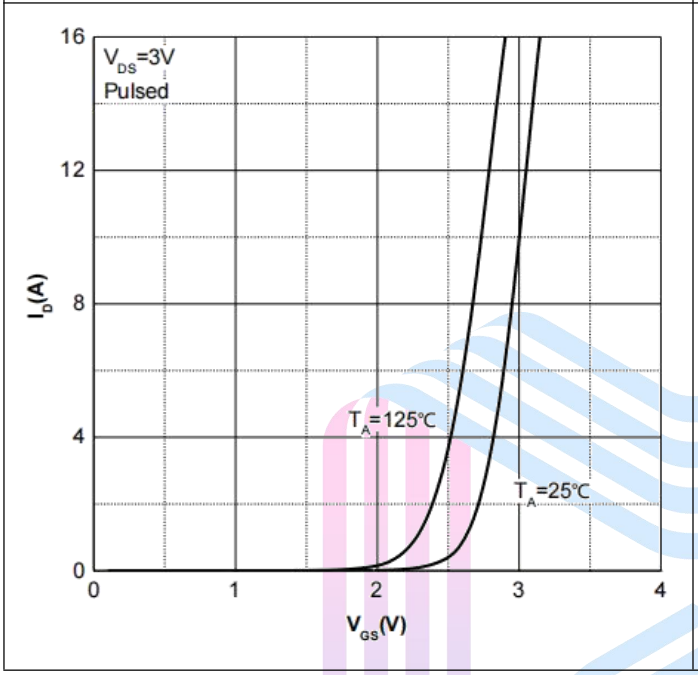


Figure 4: Output Characteristics

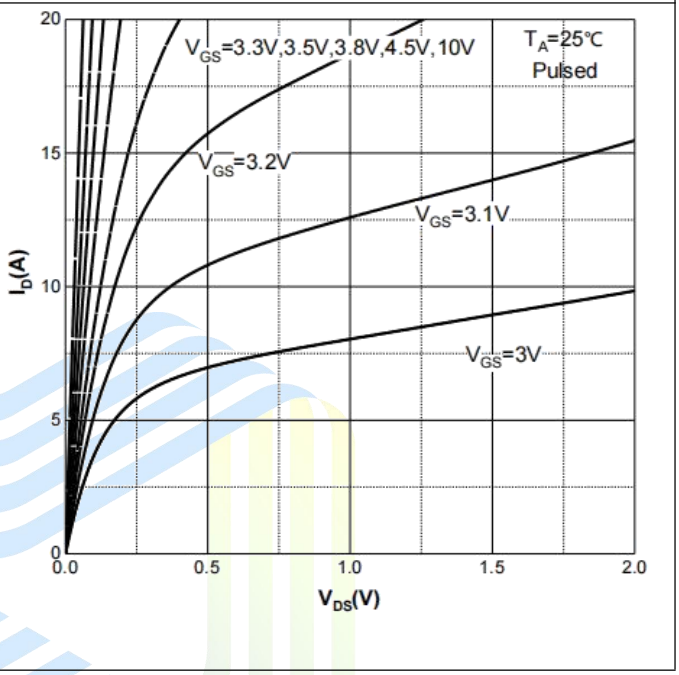


Figure 5: On-Resistance vs. Drain Current

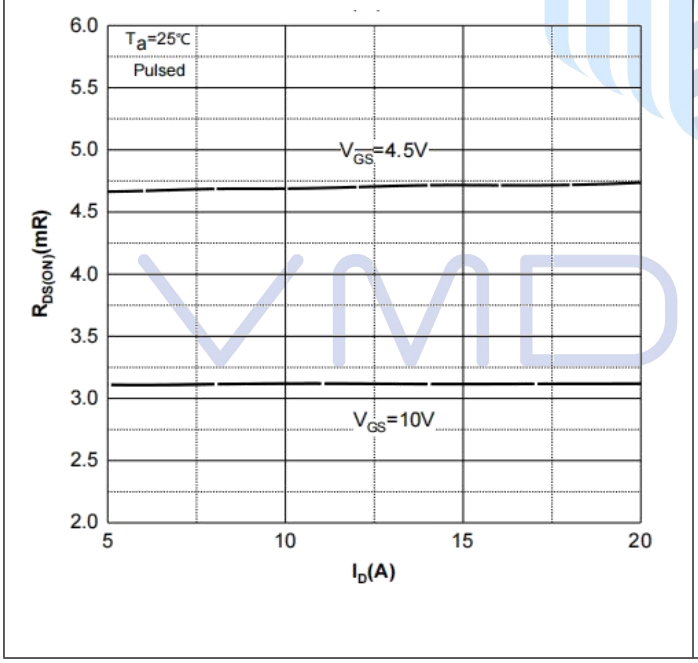


Figure 6: On-Resistance vs. Gate Voltage

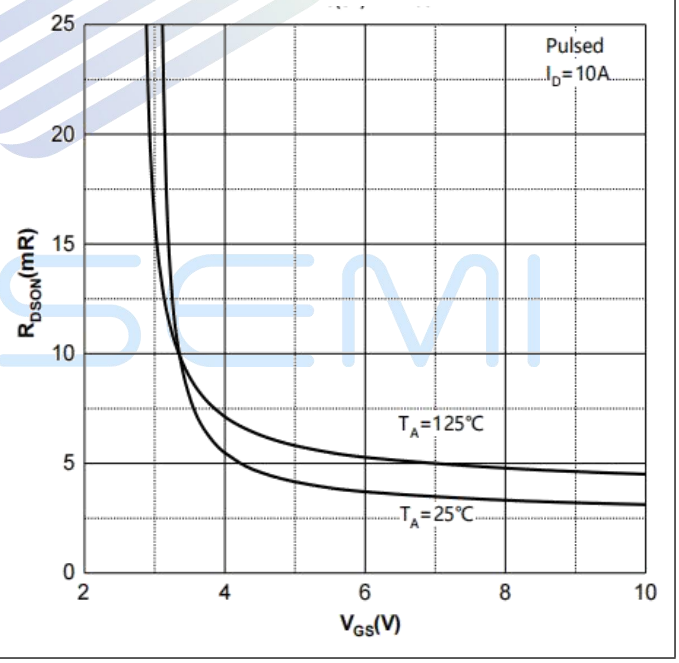


Figure 7: Body Diode Characteristics

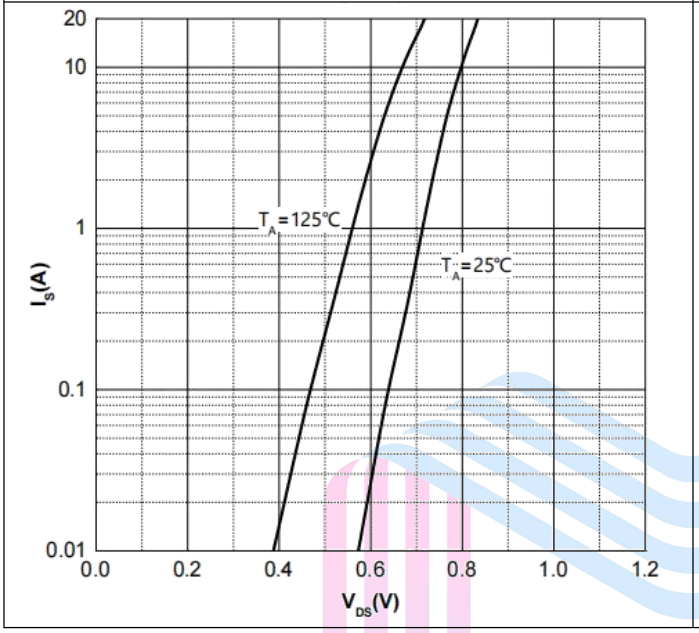


Figure 8: Threshold Voltage

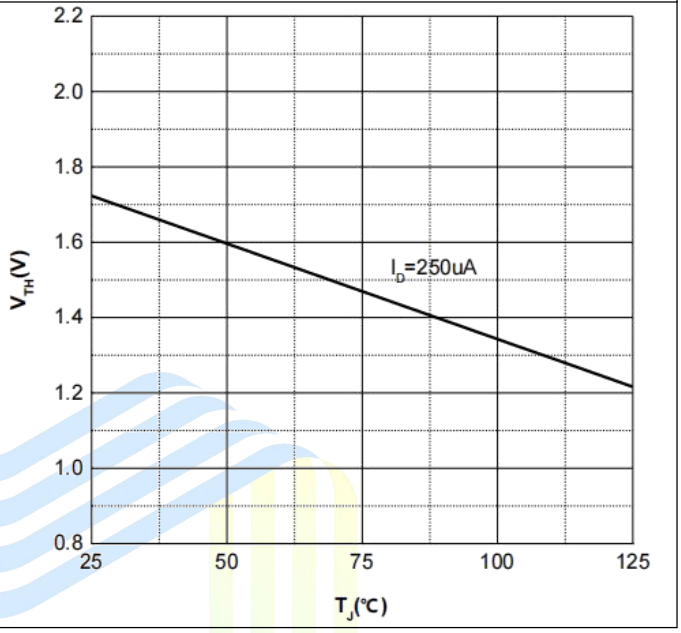


Figure 9: Typical Capacitance

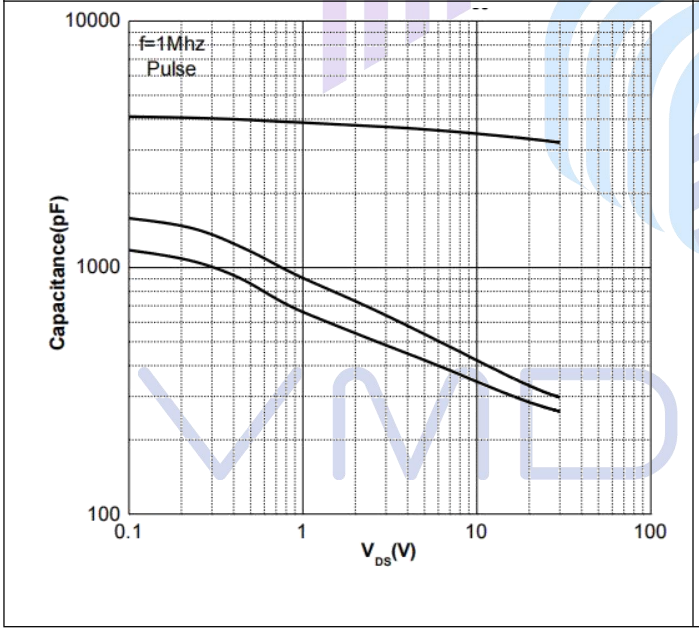


Figure 10: Safe Operating Area

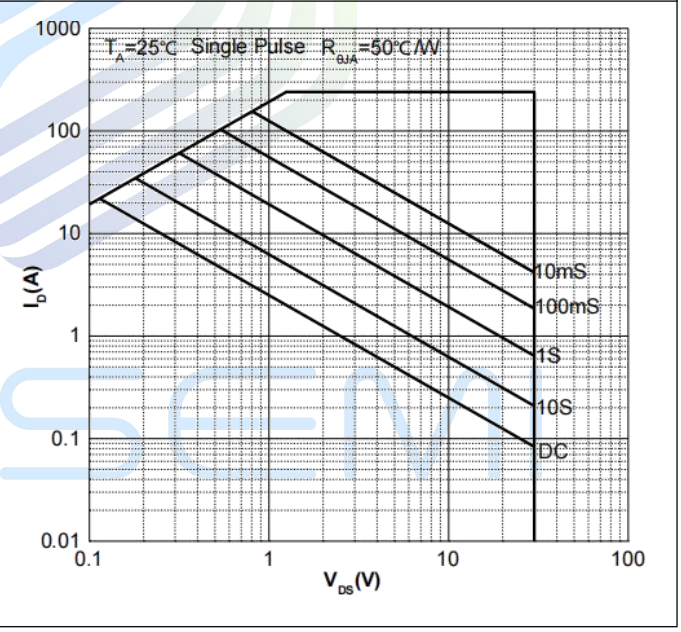
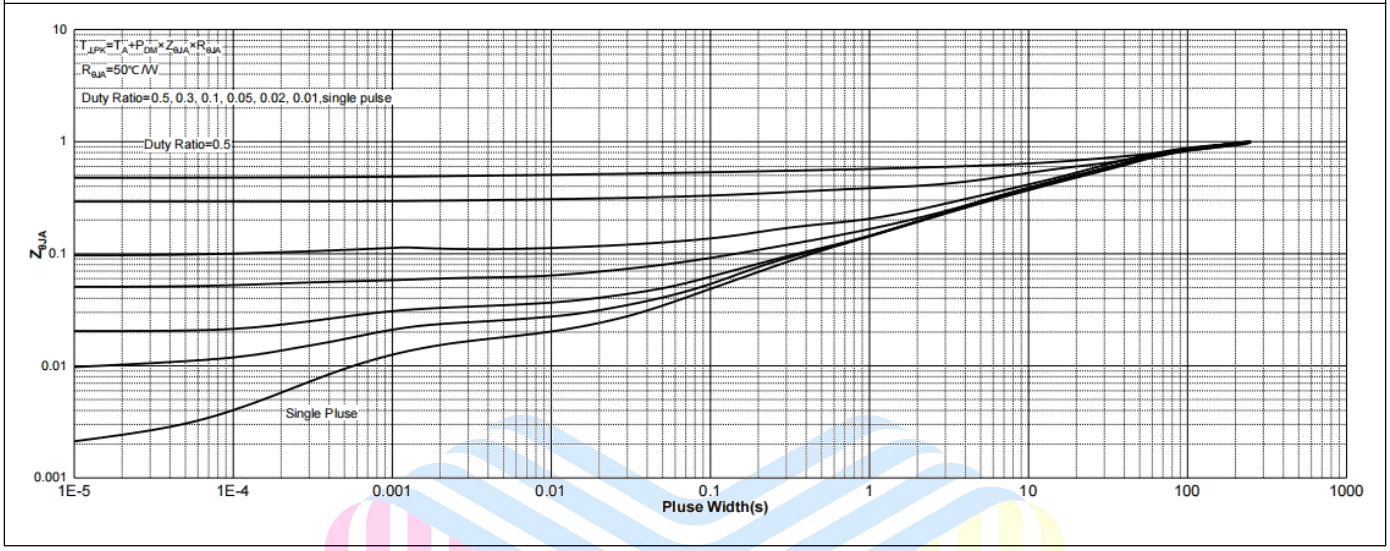


Figure 11: Normalized Maximum Transient Thermal Impedance



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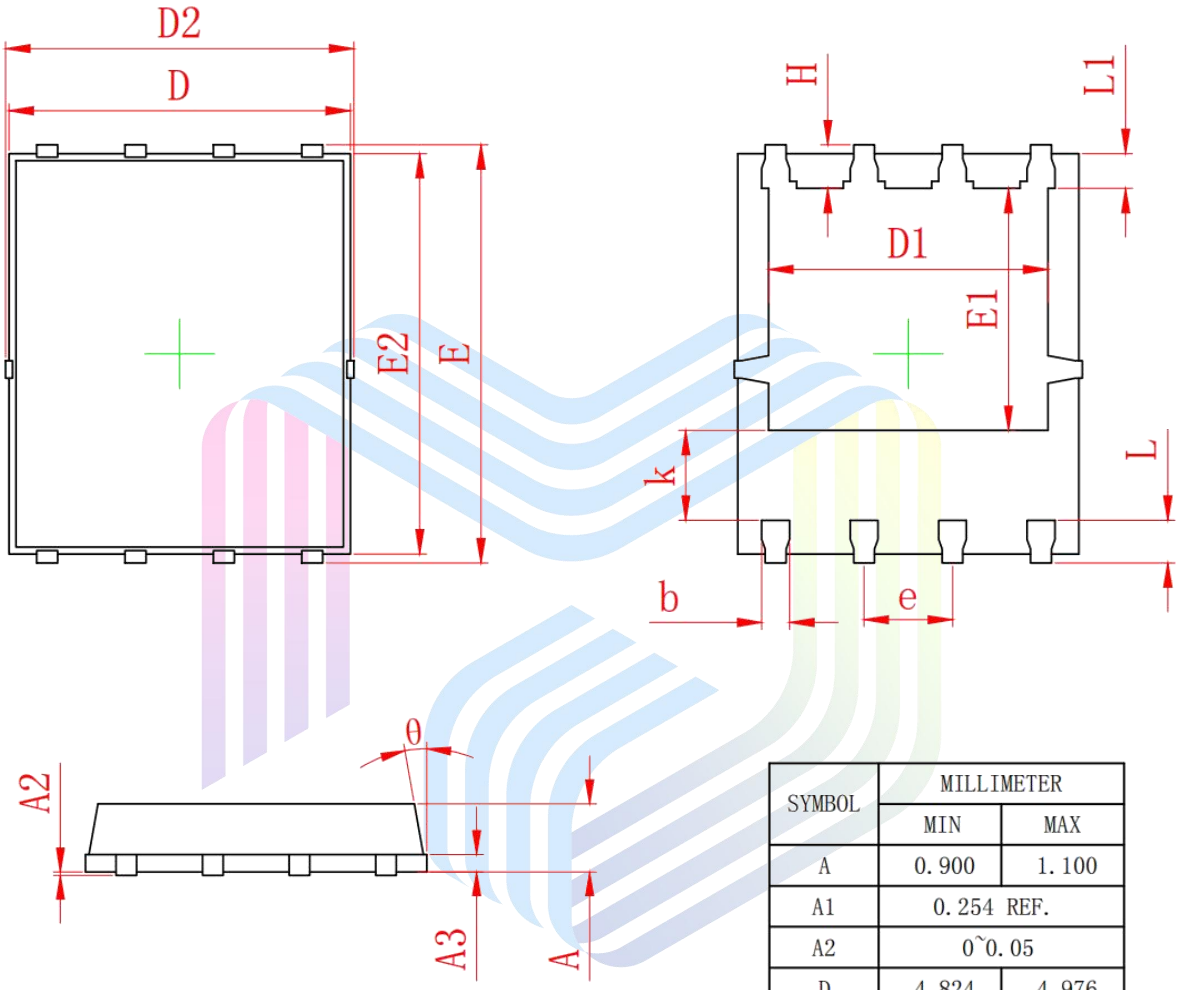
Mechanical Dimensions:

PDFN5X6-8L Package Information



SYMBOL	MILLIMETER	
	MIN	MAX
A	0.900	1.100
A1	0.254 REF.	
A2	0~0.05	
D	4.824	4.976
D1	3.910	4.110
D2	4.944	5.076
E	5.924	6.076
E1	3.375	3.575
E2	5.674	5.826
b	0.350	0.450
e	1.270 TYP.	
L	0.534	0.686
L1	0.424	0.576
k	1.190	1.390
H	0.549	0.701
θ	8°	12°

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