

VUSB8P5R15ANA

Datasheet





VUSB8P5R15ANA

General Description

V _{(BR)DSS}	R _{DS(ON)_max}	I_D	
85V	150mΩ@10V	2.5.4	
	180mΩ@4.5V	3.5A	

Symbol

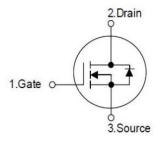
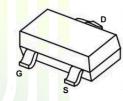


Figure 1 Symbol of VUSB8P5R15ANA

Features

- Surface Mount Package
- High Density Cell Design for Extremely Low R_{DS(ON)}
- Voltage Controlled Small Signal Switch
- Rugged and Reliable

Package Type



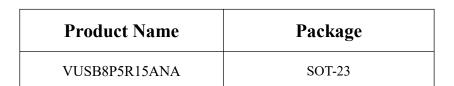
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Application

- Small Servo Motor Controls
- Power MOSFET Gate Drivers
- Switching Application

Figure 2 Package Type of VUSB8P5R15ANA

Ordering Information





150m Ω , 85V, N-Channel Power MOSFET

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Absolute Maximum Ratings (T_A= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DSS}	85	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current ^{Note1}	I_D	3.5	
Pulsed Drain Current Note2	I_{DM}	14	A
Total Power Dissipation ^{Note4}	P_{D}	1.25	W
Junction Temperature	T_{J}	150	°C
Storage Temperature	T _{STG}	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	<mark>M</mark> in	Typ	Max	Unit	
Thermal Resistance, Junction-to-Ambient ^{Note5}			100		°C/W	





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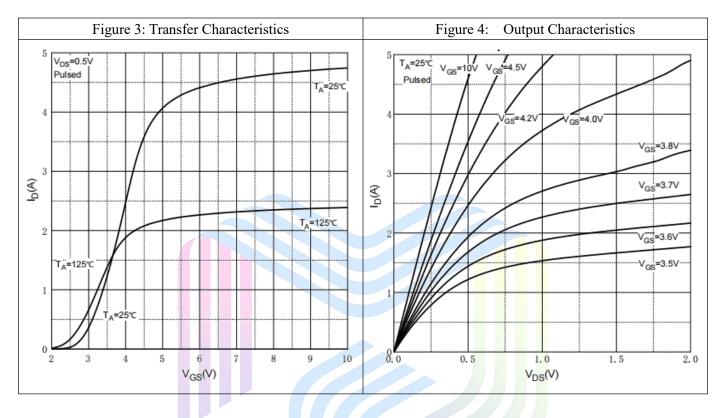
Electrical Characteristics (T_A= 25 °C, unless otherwise specified)

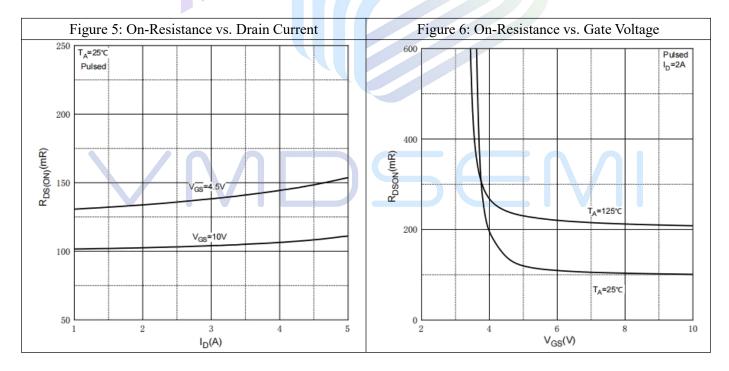
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V, I _D = 250uA	85			V
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 85V, V_{GS} = 0V$			1	uA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA
Gate Threshold Voltage ^{Note3}	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250uA$	1	2	3	V
Statis Dusin Saura On Basistana Note3	D	$V_{GS}=10V$, $I_D=2A$		98	150	mΩ
Static Drain-Source On-Resistance ^{Note3}	$R_{\mathrm{DS(ON)}}$	V_{GS} =4.5V, I_{D} = 2A		116	180	
Forward Transconductance ^{Note3}	g _{FS}	$V_{DS}=5V$, $I_D=3A$		5		S
Dynamic Characteristics						
Input Capacitance	C _{ISS}	V _{DS} =42.5V		335		pF
Output Capacitance	Coss	V _{GS} =0V		21.5		pF
Reverse Transfer Capacitance	C _{RSS}	f=1MHz		17.3		pF
Total Gate Charge	Qg	V _{DS} =43V		9.8		
Gate-Source Charge	Q_{gs}	V _{GS} =10V		1.62		nC
Gate-Drain Charge	Qgd	$I_D=2A$		3.15		
Switching Parameters						
Turn-on Delay Time	t _{d(on)}	$V_{DD} = 50V$		6		
Turn-on Rise Time	t _r	$V_{GS}=10V$		4		
Turn-off Delay Time	$t_{ m d(off)}$	$R_L=19\Omega$		20		ns
Turn-off Fall Time	t_{f}	$R_G=3\Omega$		4		
Diode Characteristics						
Diode Forward Voltage Note3	V_{SD}	$V_{GS}=0V$, $I_S=2A$			1.2	V

Notes:

- 1. The maximum current rating is limited by package. And device mounted on a large heatsink.
- 2. Pulse Test : Pulse Width $\leq 10\mu s$, duty cycle $\leq 1\%$.
- 3. Pulse Test : Pulse Width \leq 380 µs, duty cycle \leq 2%.
- 4. The power dissipation P_D is limited by $T_{J(MAX)} = 150$ °C. And device mounted on a large heatsink
- 5.Device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25°C.

Typical Performance Characteristics

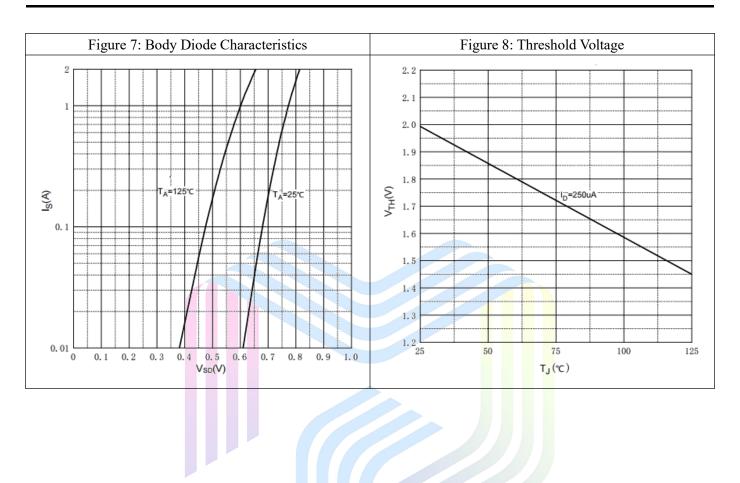






150m Ω , 85V, N-Channel Power MOSFET

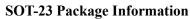
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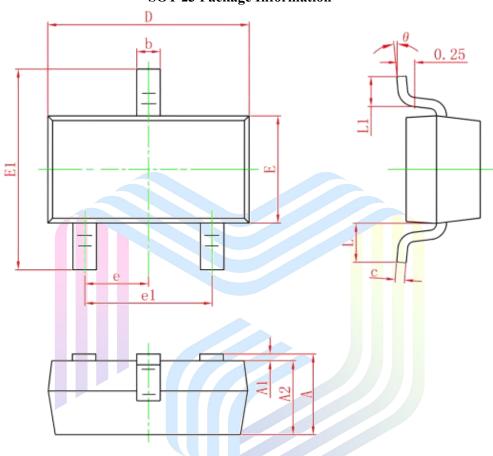






Mechanical Dimensions:





Symbol	Dimensions In Millimeters		Dimensions In Inches					
Symbol	Min.	Max.	Min.	Max.				
Α	0.900	1.150	0.035	0.045				
A1	0	0.100	0	0.004				
A2	0.900	1.050	0.035	0.041				
b	0.300	0.500	0.012	0.020				
С	0.080	0.150	0.003	0.006				
D	2.800	3.000	0.110	0.118				
E	1.150	1.500	0.045	0.059				
E1	2.250	2.650	0.089	0.104				
е	0.950TYP		0.037TYP					
e1	1.800	2.000	0.071	0.079				
L	0.550REF		0.022REF					
L1	0.300	0.500	0.012	0.020				
θ	0°	8°	0°	8°				



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