

# VUDD002R080NA

# **Datasheet**

### VUDD002R080NA

## **General Description**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)_max</sub>	$I_D$
	8.0mΩ@4.5V	
	8.5mΩ@4.0V	
20V	9.0mΩ@3.8V	14A
	10mΩ@3.1V	
	11mΩ@2.5V	

## **Symbol**

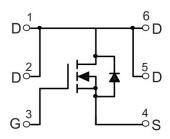
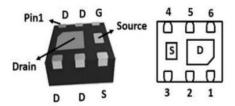


Figure 1 Symbol of VUDD002R080NA

#### **Features**

- Trench Technology Power MOSFET
- Low Gate Charge
- Low Gate Resistance
- High Power and Current Handing Capability

### Package Type



### DFN-2X2-6L

Figure 2 Package Type of VUDD002R080NA

## **Application**

- Load / Power Switch
- Battery Protection
- Power Management

## **Ordering Information**

Product Name	Package		
VUDD002R080NA	DFN2X2-6L		



### VUDD002R080NA

## Absolute Maximum Ratings (T<sub>A</sub>= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V <sub>DSS</sub>	20	V
Gate-Source Voltage	V <sub>GSS</sub>	±12	V
Continuous Drain Current Note2	$I_D$	14	A
Pulsed Drain Current	$I_{DM}$	56	A
Total Power Dissipation	$P_{D}$	0.75	W
Junction Temperature	$T_{\rm J}$	150	°C
Storage Temperature	T <sub>STG</sub>	-55 to 150	°C

### **Thermal Resistance**

Parameter	Symbol	Min	Тур	Max	Unit
Thermal Resistance, Junction-to-Ambient Note1	$R_{\theta JA}$		167		°C/W



### VUDD002R080NA

## **Electrical Characteristics** (T<sub>A</sub>= 25 °C, unless otherwise specified)

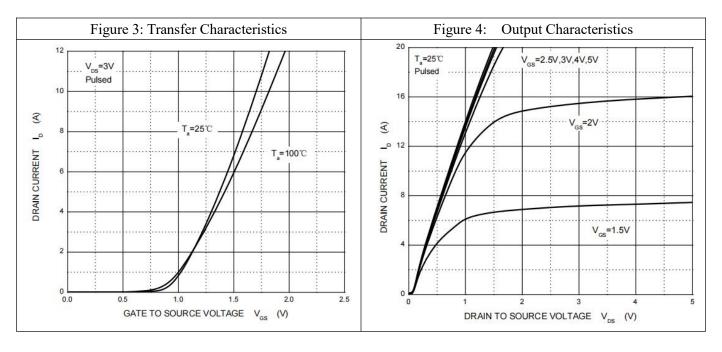
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Statistic Characteristics							
Drain-Source Breakdown Voltage	$\mathrm{BV}_{\mathrm{DSS}}$	$V_{GS}=0V, I_{D}=250uA$	20			V	
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 16V, V_{GS} = 0V$			1	uA	
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS} = \pm 12V, V_{DS} = 0V$			±100	nA	
Gate Threshold Voltage <sup>Note3</sup>	$V_{\text{GS(th)}}$	$V_{DS}=V_{GS}$ , $I_D=250uA$	0.4	0.7	1.1	V	
		$V_{GS}$ =4.5V, $I_{D}$ = 5A		6.5	8.0		
		V <sub>GS</sub> =4.0V, I <sub>D</sub> = 5A		6.6	8.5	mΩ	
Static Drain-Source On-Resistance <sup>Note3</sup>	$R_{DS(ON)}$	$V_{GS}=3.8V, I_{D}=5A$		6.7	9.0		
		$V_{GS}=3.1V, I_{D}=5A$		7.2	10		
		$V_{GS}$ = 2.5V, $I_{D}$ = 5A		8.8	11.0		
Forward tranconductance <sup>Note3</sup>	gfs	$V_{DS}=5V$ , $I_D=5A$	16			S	
Dynamic Characteristics							
Input Capacitance	$C_{ISS}$	V <sub>DS</sub> =10V		1750		pF	
Output Capacitance	Coss	$V_{GS}=0V$		230		pF	
Reverse Transfer Capacitance	$C_{RSS}$	f=1MHz		200		pF	
Total Gate Charge	$Q_{\mathrm{g}}$	V <sub>DS</sub> =10V		16			
Gate-Source Charge	$Q_{\mathrm{gs}}$	$V_{GS}$ =4.5 $V$		2.4		nC	
Gate-Drain Charge	$Q_{\mathrm{gd}}$	$I_D = 7A$		6.3			
Switching Parameters							
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=10V$		2.0			
Turn-on Rise Time	t <sub>r</sub>	$V_{GS} = 5V$		6			
Turn-off Delay Time	t <sub>d(off)</sub>	$R_L=1.2\Omega$		35		ns	
Turn-off Fall Time	$t_{\mathrm{f}}$	$R_{GEN}=3\Omega$		77			
Diode Characteristics							
Diode Forward Voltage Note3	$V_{\mathrm{SD}}$	$V_{GS}=0V, I_{S}=10A$			1.2	V	
Diode Forward Current					14	A	

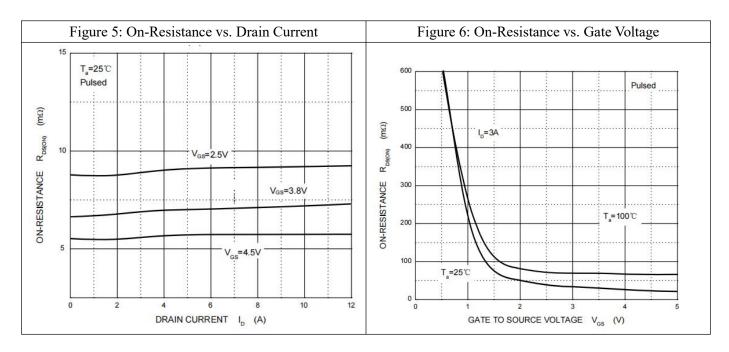
#### Notes:

- 1.  $R_{\theta JA}$  is measured with the device mounted on 1 in<sup>2</sup> FR4 board with 1oz. single side copper, in a still air environment with  $T_A = 25$  °C.
- 2.  $R_{\theta JA}$  is measured in the steady state.
- 3. Pulse test : Pulse width  $\leq$  380µs, duty cycle  $\leq$  2%.

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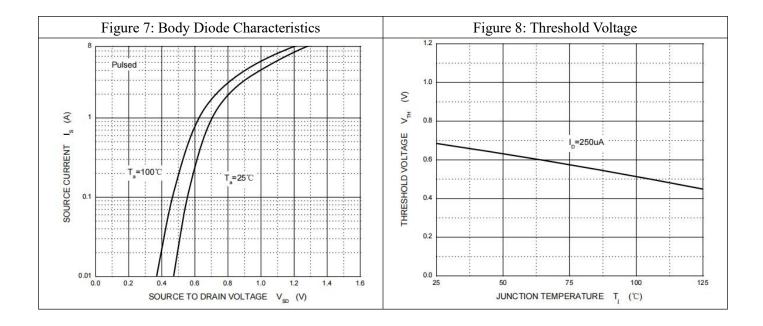
## **Typical Performance Characteristics**







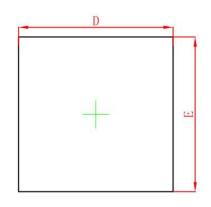
### VUDD002R080NA

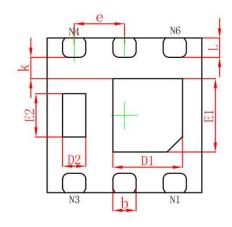


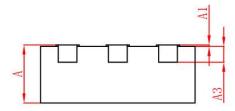
### VUDD002R080NA

## **Mechanical Dimensions:**

**DFN2X2-6L Package Information** 







Symbol	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	0.700	0.800		0.032	
A1	0.000	0.050	0.000	0.002	
A3	0.203	REF.	0.008	REF.	
D	1.924	2.076	0.076	0.082	
E	1.924	2.076	0.076	0.082	
D1	0.800	1.000	0.031	0.039	
E1	0.850	1.050	0.033	0.041	
D2	0.200	0.400	0.008	0.016	
E2	0.460	0.660	0.018	0.026	
k	0.200MIN.		0.008	BMIN.	
b	0.250	0.350	0.010	0.014	
е	0.650TYP.		0.026TYP.		
L	0.174	0.326	0.007	0.013	



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