

VMDSEMI

VUSC1P5R100NA

Datasheet

General Description

$V_{(BR)DSS}$	$R_{DS(ON)_{max}}$	I_D
15V	10mΩ@10V	8A
	11mΩ@4.5V	
	12mΩ@3.8V	
	15mΩ@2.5V	

Symbol

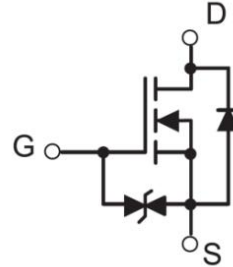
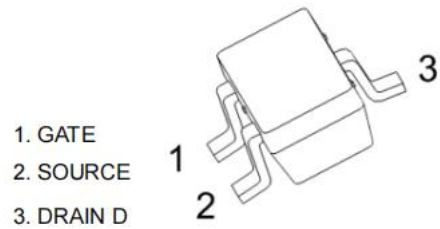


Figure 1 Symbol of VUSC1P5R100NA

Features

- Exceptional on-resistance and maximum DC current
- ESD Protected Gate
- High dense cell design for low $R_{DS(on)}$

Package Type



Application

- Load / Power Switch
- Interfacing Switching

SOT-23-3L

Figure 2 Package Type of VUSC1P5R100NA

Ordering Information

Product Name	Package
VUSC1P5R100NA	SOT-23-3L

Absolute Maximum Ratings ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DSS}	15	V
Gate-Source Voltage	V_{GSS}	± 12	V
Continuous Drain Current ^{Note1}	I_D	8	A
Pulsed Drain Current	I_{DM}	24	A
Total Power Dissipation ^{Note2}	P_D	0.45	W
Junction Temperature	T_J	150	$^\circ\text{C}$
Storage Temperature	T_{STG}	-55 to 150	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Min	Typ	Max	Unit
Thermal Resistance, Junction-to-Ambient ^{Note4}	$R_{\theta JA}$		277		$^\circ\text{C}/\text{W}$

Electrical Characteristics ($T_A = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	15			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=12V, V_{GS}=0V$			1	μA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 10V, V_{DS}=0V$			± 5	μA
Gate Threshold Voltage ^{Note3}	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5	0.8	1	V
Static Drain-Source On-Resistance ^{Note3}	$R_{DS(on)}$	$V_{GS}=10V, I_D=3A$		8.7	10	mΩ
		$V_{GS}=4.5V, I_D=3A$		9.7	11	
		$V_{GS}=3.8V, I_D=3A$		10.2	12	
		$V_{GS}=2.5V, I_D=3A$		12.8	15	
Forward tranconductance ^{Note3}	g_{FS}	$V_{DS}=5V, I_D=3A$		20		S
Dynamic Characteristics						
Input Capacitance	C_{ISS}	$V_{DS}=10V$		1800		pF
Output Capacitance	C_{OSS}	$V_{GS}=0V$		230		pF
Reverse Transfer Capacitance	C_{RSS}	$f=1MHz$		200		pF
Total Gate Charge	Q_g	$V_{DS}=10V$		17.9		nC
Gate-Source Charge	Q_{gs}	$V_{GS}=4.5V$		1.5		
Gate-Drain Charge	Q_{gd}	$I_D=3A$		4.7		
Switching Parameters						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD}=10V$		2.5		ns
Turn-on Rise Time	t_r	$V_{GS}=10V$		7.2		
Turn-off Delay Time	$t_{d(off)}$	$R_L=1.2\Omega$		49		
Turn-off Fall Time	t_f	$R_{GEN}=3\Omega$		10.8		
Diode Characteristics						
Diode Forward Voltage ^{Note3}	V_{SD}	$V_{GS}=0V, I_S=2A$		0.75	1.2	V

Notes :

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. This test is performed with no heat sink at $T_A=25^\circ\text{C}$.
3. Pulse Test: Pulse With $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

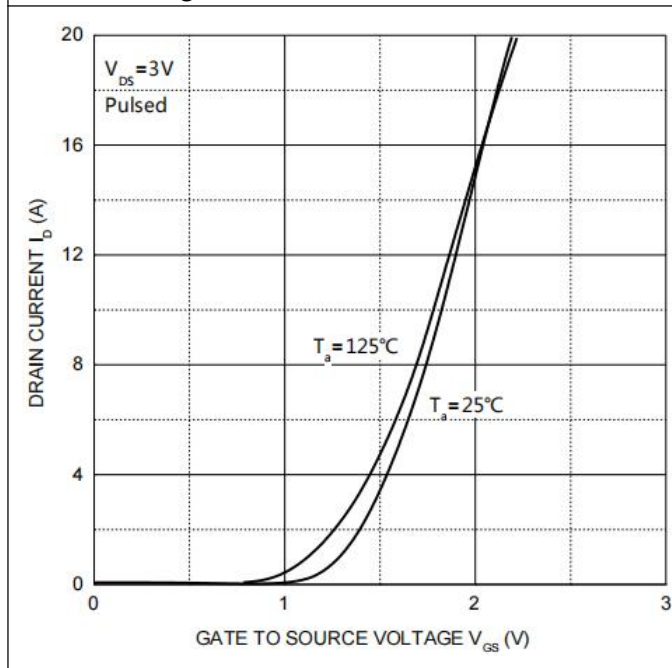
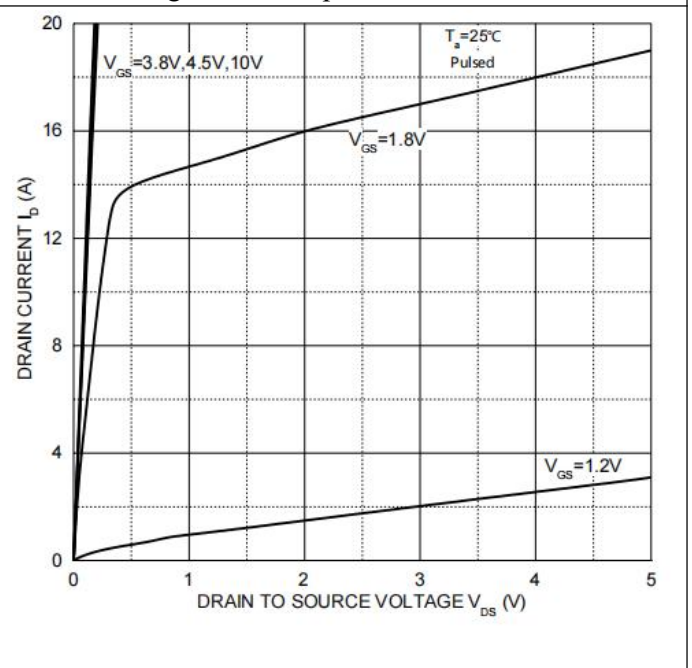
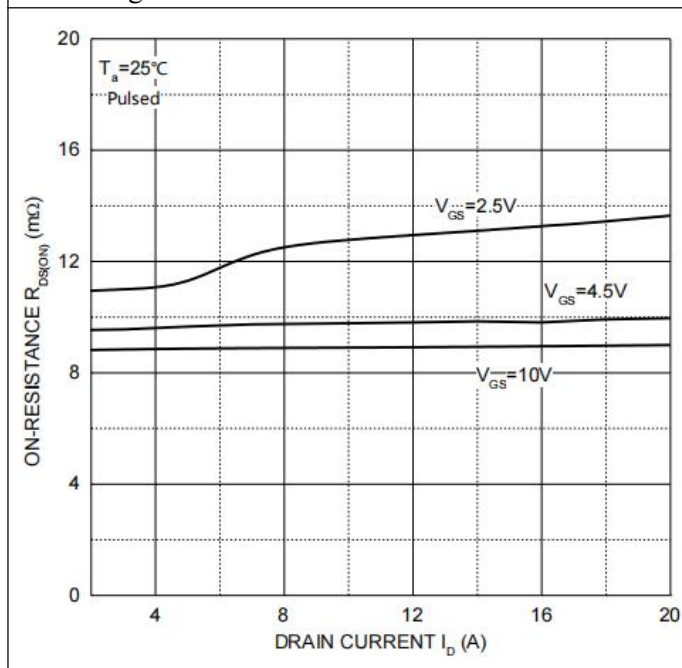
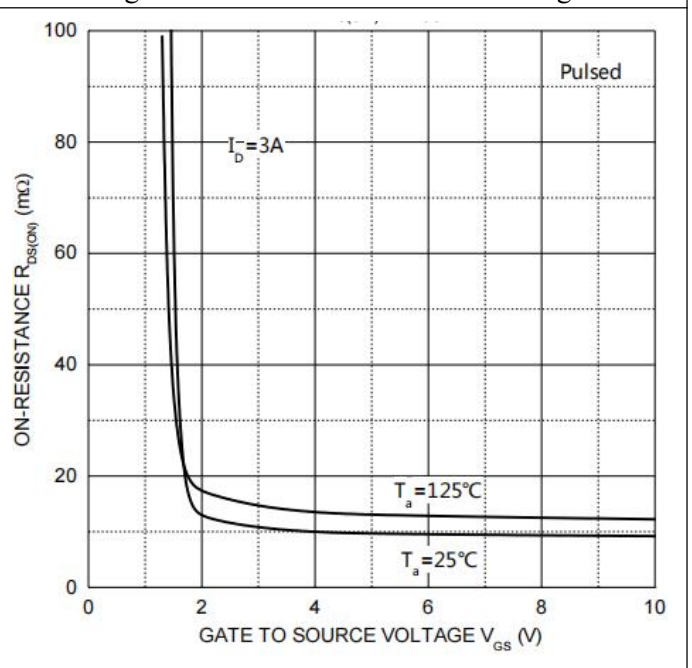
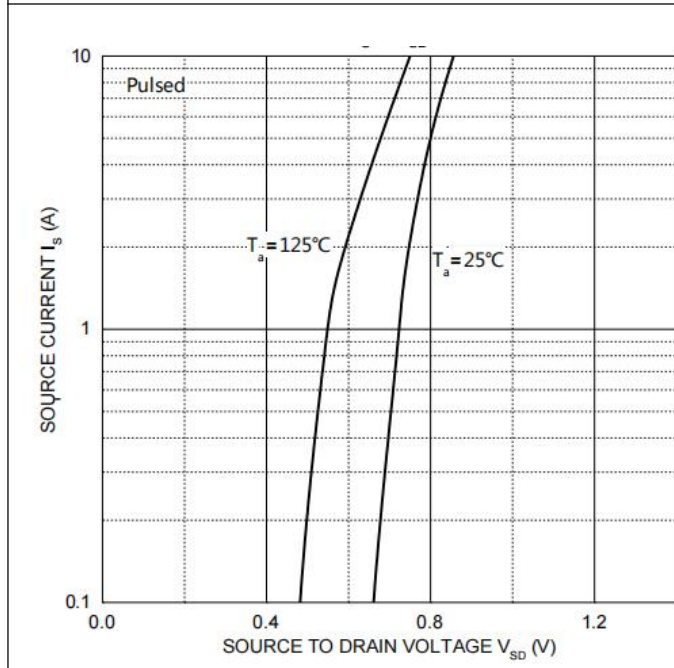
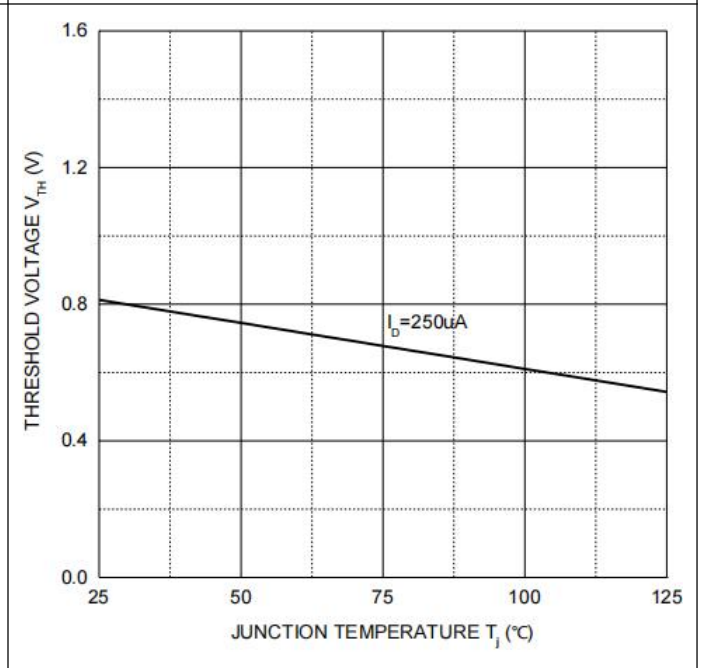
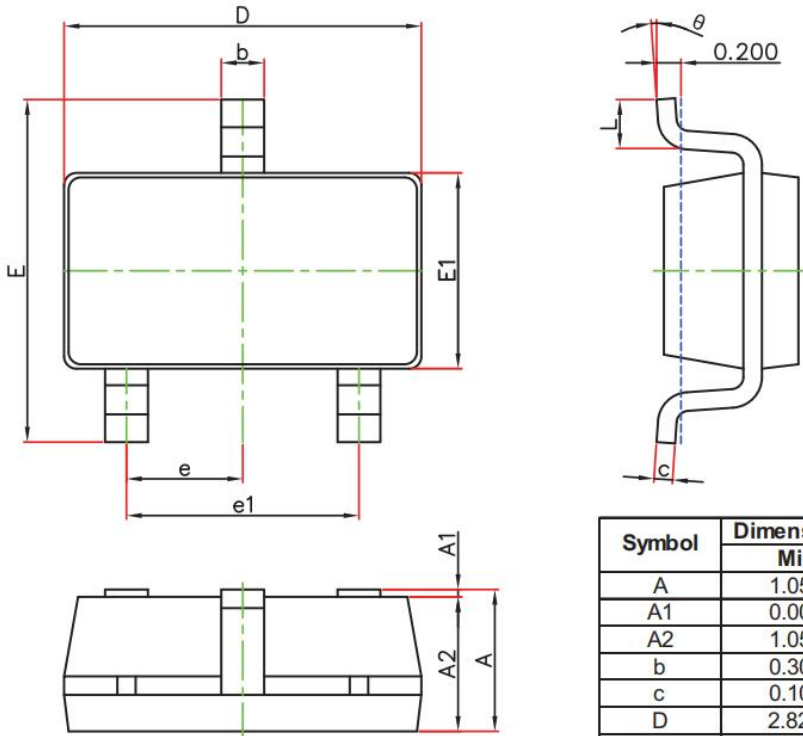
Typical Performance Characteristics
Figure 3: Transfer Characteristics

Figure 4: Output Characteristics

Figure 5: On-Resistance vs. Drain Current

Figure 6: On-Resistance vs. Gate Voltage


Figure 7: Body Diode Characteristics

Figure 8: Threshold Voltage


Mechanical Dimensions:
SOT-23-3L Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

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