

VUDD002R150NC

Datasheet

VMDSEMI



VUDD002R150NC

6

Δ

1

General Summary

Symbol

V _{(BR)DSS}	R _{DS(ON)_max}	ID
	15mΩ@4.5V	
20V	18mΩ@2.5V	12A
	30mΩ@1.8V	

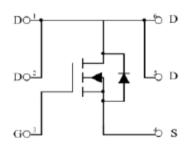


Figure 1 Symbol of VUDD002R150NC

Features Package Type Small package DFNWB2×2-6L-J Trench FET Power MOSFET 5 DDG 4 Pin1 N-Channel Switch Low RDS(ON) Source Δ Π Operated at Low Logic Level Gate Drive S Drain Application 3 2 S D D DFNWB2X2-6L-J Interfacing switching Load Switch for Portable Applications Figure 2 Package Type of VUDD002R150NC **PWM Switch**

Ordering Inf	formation		
	Product Name	Package	
	VUDD002R150NC	DFNWB2X2-6L-J	



VUDD002R150NC

Absolute Maximum Ratings (T_A= 25 °C, unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DSS}	20	V
Gate-Source Voltage	V _{GSS}	±10	V
Continuous Drain Current ^{Note1,2}	ID	12	А
Pulsed Drain Current	I _{DM}	40	А
Max Power Dissipation ^{Note1}	PD	0.75	W
Junction Temperature	TJ	150	°C
Storage Temperature	T _{STG}	-55 to 150	°C

Thermal Resistance

Parameter	Symbol	Min	Тур	Max	Unit
Thermal Resistance, Junction-to-Ambient Note1,2	Røja		1 <mark>6</mark> 7		°C/W



VMDSEMI



VUDD002R150NC

Symbol	Test Conditions	Min	Тур	Max	Unit
BV _{DSS}	V _{GS} =0V, I _D =250uA	20			V
I _{DSS}	$V_{DS} = 16V, V_{GS} = 0V$			1	uA
I _{GSS}	$V_{GS} = \pm 10V, V_{DS} = 0V$			±100	nA
V _{GS(TH)}	V _{DS} =V _{GS} , I _D =250uA	0.4	0.7	1.0	V
	V_{GS} = 4.5V, I_D =3A		10	15	
R _{DS(ON)}	$V_{GS} = 2.5V, I_D = 3A$		14	18	mΩ
	V_{GS} = 1.8V, I_D =3A		23	30	
g _{FS}	$V_{DS} = 4V, I_D = 10A$	10			S
			1	II	
C _{ISS}	$V_{DS} = 4V$		1900		pF
Coss	V _{GS} =0V		700		pF
C _{RSS}	f=1MHz		480		pF
Qg	$V_{DS} = 4V$		20		
Qgs	$V_{GS} = 5V$		2.5		nC
Q _{gd}	$I_D = 10A$		6.5		
t _{d(on)}	$V_{DD} = 4V$		15		
tr	$V_{\text{GEN}} = 4.5 \text{V}$		10		
t _{d(off)}	$R_L=0.4\Omega$		70		ns
tf	$R_G=1\Omega$		15		
V _{SD}	$V_{GS}=0V, I_{SD}=1A$			1.2	V
Is				12	А
	IDSS IGSS VGS(TH) RDS(ON) gFS CISS COSS CRSS Qg Qgs Qgd td(on) tr td(off) tf VSD	$\begin{tabular}{ c c c c } \hline I_{DSS} & V_{DS} = 16V, V_{GS} = 0V \\ \hline I_{GSS} & V_{GS} = \pm 10V, V_{DS} = 0V \\ \hline V_{GS(TH)} & V_{DS} = V_{GS}, I_D = 250uA \\ \hline V_{GS} = 4.5V, I_D = 3A \\ \hline V_{GS} = 1.8V, I_D = 3A \\ \hline V_{GS} = 1.8V, I_D = 3A \\ \hline V_{GS} = 1.8V, I_D = 10A \\ \hline \\ $	$\begin{tabular}{ c c c c c } \hline I_{DSS} & V_{DS} = 16V, V_{GS} = 0V & & & \\ \hline I_{GSS} & V_{GS} = \pm 10V, V_{DS} = 0V & & & \\ \hline V_{GS(TH)} & V_{DS} = V_{GS}, I_D = 250uA & 0.4 & & \\ \hline V_{GS} = 4.5V, I_D = 3A & & & \\ \hline V_{GS} = 1.8V, I_D = 3A & & & \\ \hline V_{GS} = 1.8V, I_D = 10A & 10 & & \\ \hline C_{ISS} & V_{DS} = 4V, I_D = 10A & 10 & & \\ \hline C_{RSS} & V_{GS} = 0V & & & \\ \hline C_{RSS} & V_{GS} = 0V & & & \\ \hline C_{RSS} & f = 1MHz & & & \\ \hline Q_{g} & V_{DS} = 4V & & & \\ \hline Q_{gd} & I_D = 10A & & & \\ \hline U_{GS} = 5V & & & \\ \hline Q_{gd} & I_D = 10A & & & \\ \hline U_{GEN} = 4.5V & & & \\ \hline U_{GOH} & V_{DD} = 4V & & & \\ \hline U_{GIOH} & V_{DD} = 4V & & & \\ \hline V_{SD} & V_{GS} = 0V, I_{SD} = 1A & & \\ \hline \end{tabular}$	$\begin{tabular}{ c c c c c c } \hline I_{DSS} & V_{DS} = 16V, V_{GS} = 0V & & & & \\ \hline I_{GSS} & V_{GS} = \pm 10V, V_{DS} = 0V & & & & \\ \hline V_{GS}(TH) & V_{DS} = V_{GS}, I_D = 250uA & 0.4 & 0.7 & \\ \hline V_{GS} = 4.5V, I_D = 3A & 10 & \\ \hline V_{GS} = 2.5V, I_D = 3A & 14 & \\ \hline V_{GS} = 1.8V, I_D = 3A & 23 & \\ \hline g_{FS} & V_{DS} = 4V, I_D = 10A & 10 & \\ \hline \hline C_{ISS} & V_{DS} = 4V, I_D = 10A & 10 & \\ \hline \hline C_{RSS} & f = 1MHz & 480 & \\ \hline \hline Q_g & V_{DS} = 4V & 20 & \\ \hline Q_{gs} & V_{GS} = 5V & 2.5 & \\ \hline Q_{gd} & I_D = 10A & 6.5 & \\ \hline t_{d(on)} & V_{DD} = 4V & 15 & \\ \hline t_r & V_{GEN} = 4.5V & 10 & \\ \hline t_{d(off)} & R_L = 0.4\Omega & 70 & \\ \hline t_f & R_G = 1\Omega & & \\ \hline \hline V_{SD} & V_{GS} = 0V, I_{SD} = 1A & & \\ \hline \end{tabular}$	$\begin{tabular}{ c c c c c c } \hline I_{DSS} & V_{DS} = 16V, V_{GS} = 0V & \pm 100 \\ \hline I_{GSS} & V_{GS} = \pm 10V, V_{DS} = 0V & \pm 100 \\ \hline V_{GS(TH)} & V_{DS} = V_{GS}, I_D = 250uA & 0.4 & 0.7 & 1.0 \\ \hline V_{GS} = 4.5V, I_D = 3A & 10 & 15 \\ \hline V_{GS} = 2.5V, I_D = 3A & 14 & 18 \\ \hline V_{GS} = 1.8V, I_D = 3A & 23 & 30 \\ \hline g_{FS} & V_{DS} = 4V, I_D = 10A & 10 & & & \\ \hline \hline C_{ISS} & V_{DS} = 4V, I_D = 10A & 10 & & & \\ \hline C_{CSS} & V_{GS} = 0V & 700 & & \\ \hline C_{RSS} & f = 1MHz & 480 & & & \\ \hline \hline Q_{gs} & V_{DS} = 4V & 20 & & \\ \hline Q_{gs} & V_{GS} = 5V & 2.5 & & \\ \hline Q_{gd} & I_D = 10A & 6.5 & & \\ \hline t_{d(on)} & V_{DD} = 4V & 15 & & \\ \hline t_r & V_{GEN} = 4.5V & 10 & & \\ \hline t_r & V_{GEN} = 4.5V & 10 & & \\ \hline t_r & R_G = 1\Omega & 15 & & \\ \hline V_{SD} & V_{GS} = 0V, I_{SD} = 1A & 1.2 & \\ \hline \end{tabular}$

Electrical Characteristics TA= 25 °C, unless otherwise specified

Notes :

 $1.R_{\theta JA}$ is measured with the device mounted on 1 in² FR4 board with 1oz. single side copper, in a still air environment with $T_A = 25^{\circ}C$.

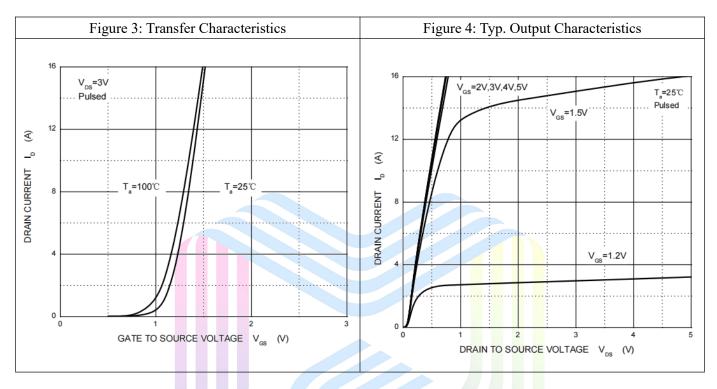
 $2.R_{\theta JA}$ is measured in the steady state

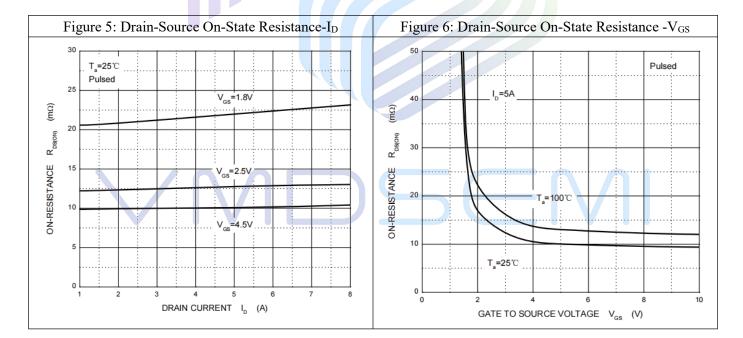
3.Pulse test : Pulse width \leq 380µs, duty cycle \leq 2%.



VUDD002R150NC

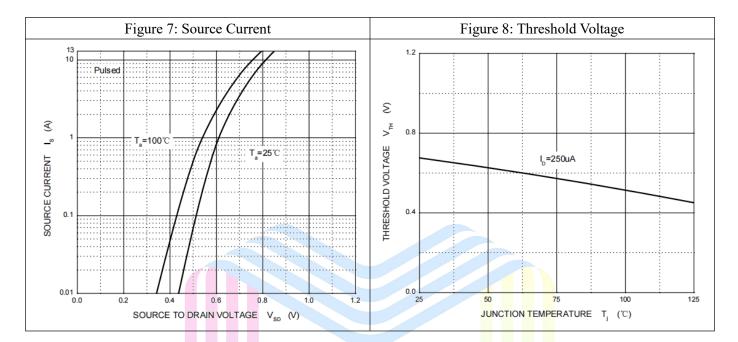
Typical Performance Characteristics







VUDD002R150NC





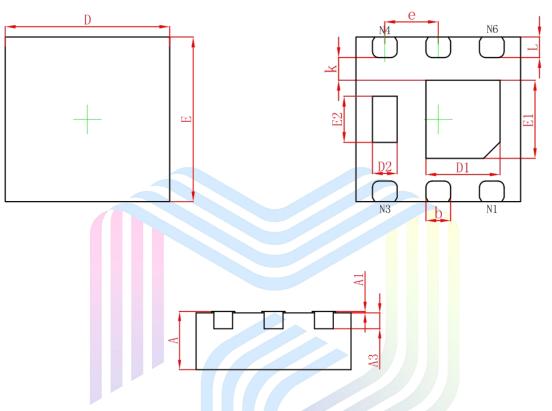
VNDSEMI



VUDD002R150NC

Mechanical Dimensions:

DFNWB2×2-6L-J Package Information



Symbol	Dimensions (Unit:mm)		imensions (Unit:mm) Dimension (Unit:inch	
	Min.	Max.	Min.	Max.
А	0.700	0.800		0.032
A1	0.000	0.050	0.000	0.002
A3	0.20	0.203REF		8REF
D	1.924	2.076	0.076	0.082
Е	1.924	2.076	0.076	0.082
D1	0.800	1.000	0.031	0.039
E1	0.850	1.050	0.033	0.041
D2	0.200	0.400	0.008	0.016
E2	0.460	0.660	0.018	0.026
k	0.200MIN		0.00	8MIN
b	0.250	0.350	0.010	0.014
e	0.650TYP		0.02	6TYP
L	0.174	0.326	0.007	0.013



VUDD002R150NC

NOTICE

Hangzhou VMD Semiconductor Co., Ltd (VMD) reserves the right to make changes without notice in order to improve reliability, function or design and to discontinue any product or service without notice. Customers should obtain the latest relevant information before orders and should verify that such information is current and complete. All products are sold subject to VMD's terms and conditions supplied at the time of order acknowledgement.

VMD, its affiliates, agents, and employees, and all persons acting on its or their behalf, disclaim any and all liability for any errors, inaccuracies or incompleteness contained herein or in any other disclosure relating to any product.

VMD disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify VMD's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

VMD warrants performance of its hardware products to the specifications at the time of sale, testing, reliability and quality control are used to the extent VMD deems necessary to support this warrantee. Except where agreed upon by contractual agreement, testing of all parameters of each product is not necessarily performed.

VMD does not assume any liability arising from the use of any product or circuit designs described herein. Customers are responsible for their products and applications using VMD's components. To minimize risk, customers must provide adequate design and operating safeguards.

VMD does not warrant or convey any license to any intellectual property rights either expressed or implied under its patent rights, nor the rights of others. Reproduction of information in VMD's data sheets or data books is permissible only if reproduction is without modification or alteration. Reproduction of this information with any alteration is an unfair and deceptive business practice.

VMD is not responsible or liable for such altered documentation. Resale of VMD's products with statements different from or beyond the parameters stated by VMD for that product or service voids all express or implied warrantees for the associated VMD product or service and is an unfair and deceptive business practice.

All Rights Reserved.

VMD5EMI



VUDD002R150NC



Via-Media Semiconductor Limited Company

http://www.vmdsemi.com

Main Sites:

- Headquarters

Hangzhou Via-Media Semiconductor Co., LTD. 1305-1306, Building 71, No. 90, Wensan Road, Xihu District, Hangzhou, Zhejiang Province, P.R. China Tel: +86-0571-8515 0563

- Shanghai

Shanghai R&D Center. 1506~1508, Xinyin Building, 888 Yishan Road, Shanghai, P.R of China Tel: +86- 021-54201999

- Xi'an

Xi'an R&D Center 1703B, Building A, Greenland Center, Jinye Road, High-Tech Zone, Xi'an, Shaanxi, P.R of China

- Chengdu Office

Chengdu Winhi Semiconductor Co., LTD. Floor 15, Building 5, No. 171, Hele 2nd Street, Chengdu, Sichuan Province, P.R. China Tel: +86-028-8505 0771

Shenzhen

Shenzhen Sales office

Room 4A15, Block AB, Tianxiang Building, Chegongmiao, Futian District, Shenzhen, P.R of China Tel: +86-0755- 82570682