

VUDE003R120NA

Datasheet



VUDE003R120NA

General Description

V _{(BR)DSS}	R _{DS(ON)_max}	I_D
30V	12mΩ@10V	25 A
	18mΩ@4.5V	35A

Symbol

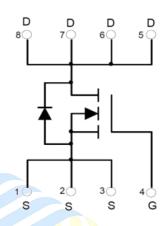
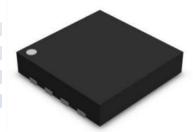


Figure 1 Symbol of VUDE003R120NA

Features

- Low Gate Charge
- Low Gate Resistance
- Advanced high cell density Trench technology

Package Type



Application

- Load / Power Switch
- Battery protection applications

DFN3030-8L

Figure 2 Package Type of VUDE003R120NA

Ordering Information

Product Name	Package		
VUDE003R120NA	DFN3030-8L		



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Absolute Maximum Ratings (T_A= 25 °C, unless otherwise specified)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		$V_{ m DSS}$	30	V
Gate-Source Voltage		V _{GSS}	±20	V
Continuous Drain Current	T _A = 25 °C	I_{D}	15	Α
Continuous Drain Current	T _C = 25 °C	I_D	35	A
Pulsed Drain Current Note2		I_{DM}	60	A
Total Power Dissipation Note3		P _D	3	W
Junction Temperature		T _J	150	°C
Storage Temperature		T _{STG}	-55 to 150	°C

Thermal Resistance

Par <mark>ameter</mark>	Symbol	Min	T <mark>y</mark> p	Max	Unit	
Thermal Resistance, Junction-to-Ambient Note1	$R_{ heta JA}$		42		°C/W	





12mΩ, 30V, N-Channel Power MOSFET

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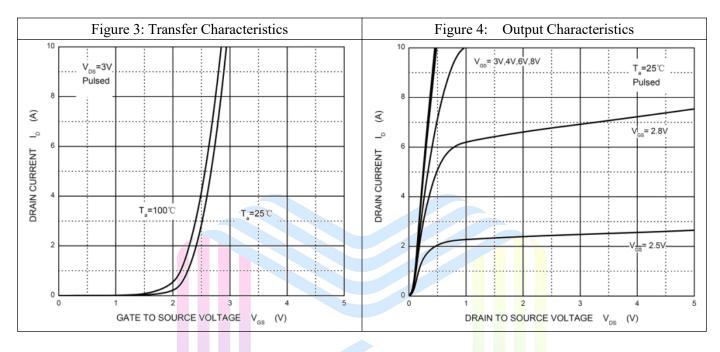
Electrical Characteristics (T_A= 25 °C, unless otherwise specified)

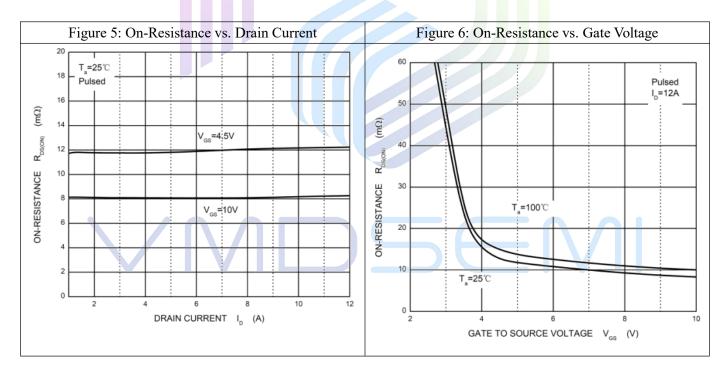
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Statistic Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V, I _D = 250uA	30			V
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 30V, V_{GS} = 0V$			1	uA
Gate-Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA
Gate Threshold Voltage ^{Note4}	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1.0	1.7	3.0	V
Static Drain-Source On-Resistance ^{Note4}	D	$V_{GS}=10V, I_{D}=10A$		8	12	mΩ
Static Drain-Source On-Resistance	$R_{\mathrm{DS(ON)}}$	V_{GS} =4.5V, I_{D} = 10A		12	18	
Forward tranconductance ^{Note4}	gfs	$V_{DS} = 5V, I_{D} = 10A$	5	12		S
Dynamic Characteristics						
Input Capacitance	C _{ISS}	V _{DS} =15V		1570		pF
Output Capacitance	Coss	V _{GS} =0V		320		pF
Reverse Transfer Capacitance	C _{RSS}	f=1MHz		190		pF
Total Gate Charge	Q_{g}	V _{DS} =15V		13.5		
Gate-Source Charge	Q_{gs}	V _{GS} =5V		5.6		пC
Gate-Drain Charge	Q_{gd}	$I_D=10A$		3.7		
Switching Parameters						
Turn-on Delay Time	t _{d(on)}	V _{DD} = 25V		31		
Turn-on Rise Time	t _r	$V_{GS}=10V$		22		
Turn-off Delay Time	$t_{ m d(off)}$	$R_L=6.7\Omega$		105		ns
Turn-off Fall Time	t_{f}	$R_{GEN}=63\Omega$, $I_D=1A$		82		
Diode Characteristics						
Diode Forward Voltage Note4	V_{SD}	$V_{GS}=0V, I_{S}=10A$		0.85	1.2	V
Continuous Survey Comment	т	$T_J = 25$ °C			1.5	
Continuous Source Current	Is	$V_G=V_D=0V$			15	A
Pulsed Source Current	I_{SM}	Force Current			45	

Notes:

- 1. The data tested by surface mounted on a 1 inch2 FR-4 board with 2OZ copper
- 2.Pulse Test:Pulse Width < 10us, Duty Cycle < 0.5%.
- 3. The power dissipation is limited by 150°C junction temperature
- 4. Pulse Test : Pulse width≤300μs, duty cycle≤0.5%.

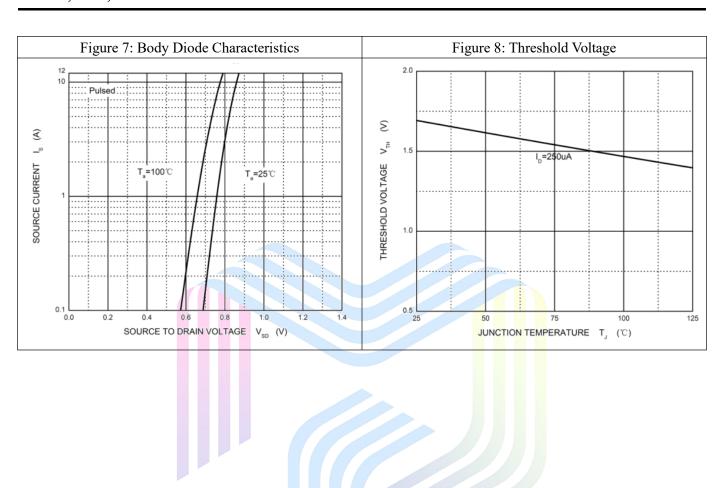
Typical Performance Characteristics







VUDE003R120NA

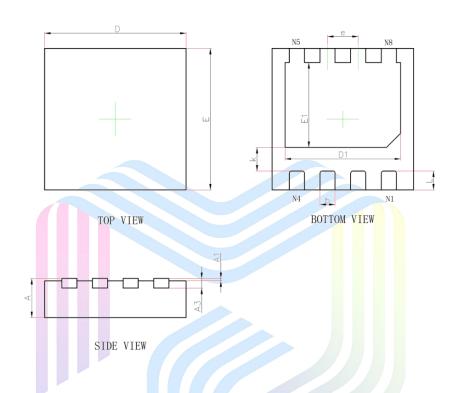




VUDE003R120NA

Mechanical Dimensions:

DFN3030-8L Package Information



Symbol	Symbol Dimensions In Millimeters		Dimension	s In Inches
Symbol	Min.	Max.	Min.	Max.
Α	0.700/0.800	0.800/0.900	0.028/0.031	0.031/0.035
A1	0.000	0.050	0.000	0.002
A3	0.203	REF.	0.008	REF.
D	2.924	3.076	0.115	0.121
Ē	2.924	3.076	0.115	0.121
D1	2.350	2.550	0.093	0.100
E1	1.700	1.900	0.067	0.075
k	0.450	0.550	0.018	0.022
b	0.270	0.370	0.011	0.015
e	0.650TYP.		0.026	TYP.
L	0.324	0.476	0.013	0.019



12mΩ, 30V, N-Channel Power MOSFET

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